



April 27, 2021

Ms. Susanna Kooistra
Mr. Maurice Pope
3GPP Mobile Competence Centre (ETSI)
650, Route Des Lucioles
06921 Sophia-Antipolis
France

Subject: Nomination of Mr. Puneet Jain as a candidate for 3GPP TSG SA WG2 Chair

Dear Susanna, Dear Maurice,

On behalf of Intel (ATIS member), I am pleased to nominate Mr. Puneet Jain as a candidate for the position of 3GPP TSG SA WG2 Chair.

Mr. Puneet Jain has 23 years of experience in standardization, research, and product development in the telecom and wireless industry. Puneet is currently Chair of TSG SA WG2 appointed in May 2019. His brief Curriculum Vitae is attached.

Mr. Puneet Jain is informed about and aware of the antitrust/competition laws and regulations of relevant jurisdictions, and he shall comply with such laws while acting in his capacity as TSG SA WG2 Chair.

Intel is fully committed to the success of 3GPP and its diverse ecosystem. Thus, Intel will fully support and provide necessary resources to ensure that Puneet accomplishes the duties and carries out his responsibilities as the Chair of 3GPP TSG SA WG2.

Yours sincerely,

A handwritten signature in blue ink, appearing to read "Asha R Keddy".

Asha R Keddy
Corporate Vice President
General Manager, Next Generation and Standards (NGS), Intel



Position of candidacy: 3GPP TSG SA WG2 Chair

Candidate Name: Puneet Jain

Member: Intel

Partner Organization: ATIS

Brief Curriculum Vitae:

Puneet Jain is a Senior Principal Engineer and Senior Director of Technical Standards at Next Generation and Standards (NGS) Group at Intel Corporation. He is head of the Network Standards team at Intel and responsible for driving research and standardization on end-to-end network architecture for mobile broadband technologies such as 4G-LTE and 5G.

Puneet has 23+ years of rich experience in standardization, research, and product development in the telecom and wireless industry. He has actively participated in various global standardization bodies including 3GPP, WiMAX Forum, IEEE 802.16j, ETSI TC M2M, oneM2M, GSMA, TSDSI, and ATIS.

Puneet has been active in 3GPP since 2006 and head of Intel delegation to 3GPP SA/CT. He currently serves as the Chair of 3GPP SA WG2 since May 2019. He has also served as 3GPP SA WG2 Vice-Chair (2015 - 2019) and Rapporteur of several study/work items on Machine Type Communication (MTC) / Cellular Internet of Things (CIoT) in 3GPP SA WG2.

Puneet has also served as oneM2M Steering Committee Vice-Chair (2014 - 2016). In his previous roles at Intel, he was Chair of WiMAX-3GPP interworking sub-team at WiMAX Forum, involved in design and development of ATM RAN reference implementation for Intel Network Processors.

Prior to Intel, he worked at Centre for Development of Telematics (C-DoT) in India where he led a team for design and development for Mobility Management and BSS Application Protocol for GSM MSC/VLR and was a key contributor to technical evaluation and network configuration of C-DoT MSC/VLR during the launch of Indian Mobile Personal communication Services (IMPCS) by Department of Telecommunications, Government of India.