12th June 2013

Ms Susanna Kooistra
Mr Frederic Firmin
3GPP MCC
650, route des Lucioles
F-06921 Sophia-Antipolis
France

Dear Susanna and Frederic,

On behalf of Intel Corporation, an ETSI Member, I am pleased to announce that Mr Chen-Ho CHIN will stand as a candidate for the position of 3GPP TSG CT WG1 Vice-Chairman in the coming CT1#84 vice chair elections.

Chen-Ho has over 30 years working experience in telecommunications. He graduated with honours from Liverpool University, UK in 1979. After over 10 years working on fixed telecommunications systems, Chen-Ho moved to the GSM and mobile world in 1990. His work in mobile telecommunications since then has covered Standardisation, System Engineering, Design and Development, Integration and Testing. His standards work began with his involvement in GSM, in 1990, in SMG3 WPA, and since 2000 he has been in UMTS system and standardisation and then in 2007 Chen-Ho extended himself into LTE/SAE. In all these times, areas of work, different systems and Standards development in a variety of WGs, Chen-Ho has worked on both the terminal side and the network side. In addition, Chen-Ho has the experience of chairing CT1 breakout sessions on NAS topics, on IMS topics, and on non-3GPP access topics for the past 5 years, always supporting the needs of CT1 and the CT1 chairman.

With his delegate and chairing experience, wide ranging telecommunications background and knowledge of the various mobile and fixed telecommunications systems and standards, Chen-Ho as vice-chair will continue to contribute immensely to the continuing standardisation activities and success of 3GPP.

If elected, Chen-Ho will do all he can to ensure that the development of the 3GPP specifications continues in an efficient, fair and productive manner as like he has been doing.

Intel will provide all the necessary support for Chen-Ho, if elected, to allow him to undertake the role and responsibilities of vice chair, as part of Intel's ongoing commitments to 3GPP.

Chen-Ho will represent the Individual Member "Intel Corporation" and project partner "ETSI". Attached is a brief resume of Chen-Ho's past experiences.

Yours Sincerely,

Asha Keddy,
GM, MCG Standards & Advanced Technology, Intel Corporation

Curriculum Vitae of Chen-Ho CHIN

Chen-Ho graduated in 1979 from University of Liverpool, UK, in Electrical & Electronics Engineering. After graduation he started working on design and testing of electronic public exchanges and in the 1980s worked on the development of digital switching telephony.

Towards the end of the 1980s, when there was a concerted effort in Europe to begin design, standardise and develop the 2nd generation mobile system, Chen-Ho moved onto GSM handset design and development. In those early 1990s Chen-Ho participated in SMG3 WPA working on 04.08 and 08.08 design. Between 1992 - 1996, as a follow on of standards work, Chen-Ho moved back to implementation, working in the GSM BSC subsystem development.

In later half of 1990s when mobile satellite systems began their design uptake, Chen-Ho was recruited into a MEO (Medium Earth Orbit) satellite communication system project, specifically working on development of GSM over satellite. Upon completing that project, Chen-Ho in 1998 moved back to terrestrial mobile system in time to begin work on UMTS system design and standardisation, participating in the newly formed 3GPP project.

In UMTS, Chen-Ho participated in CN1, actively involved in developing TS 24.008 and the NAS subsystem, its signalling protocol and its state machines. In that time on UMTS, Chen-Ho was involved mostly on the mobile side of the business. By the time UMTS Rel-99 was completed and much of the CN1 work was focused on Rel-5, Chen-Ho in 2003, went onto test and integration and IoT of UMTS mobiles and networks - so to speak, checking, testing and putting into practice the UMTS system whose design he had participated in earlier.

In 2005, Chen-Ho moved back into system design and standards work, continuing with participation in 3GPP standardisation in CT1 and in time to see completion of UMTS Rel-7 and join in, from 2007, the beginning of EPS/SAE NAS signalling protocol design.

In CT1 SAE work, Chen-Ho took rapporteurship of 24.302 and played a pro-active part in completing the non-3GPP access Stage 3 specifications in CT1. He was not only an active delegate but also held the CT1 Vice Chair from Aug 2007 till Aug 2011. As vice chair he chaired most of the breakout sessions on non-3GPP access and NAS, maximising meeting and discussion time and enabled better progress in CT1 in support of CT1 chairman's objective of completing SAE in a timely and orderly way.

Chen-Ho in these years in CT1 (and CN1 before) not just involves himself expertly in NAS topics and non-3GPP access topics, but also in IMS topics. In this last 18 months, when requested by the CT1 chair, Chen-Ho chaired most of the IMS breakout sessions, showing himself able to cover and chair any topic in CT1, helping to progress work in CT1. In all the times he had chaired any of the sessions, Chen-Ho has shown himself to be fair, objective and able to progress work positively and go out of the way to support less experienced delegates giving everyone fair representation, time and attention.