3GPP TSG-SA WG3 #17 Gothenberg, Sweden, 27 February- 3 March 2001

CHANGE REQUEST								CR-Form-v3
33.105		CR CF	R-Num #	rev	_ #	Current vers	3.6.0	¥
For <u>HELP</u> on using this form, see bottom of this page or look at the pop-up text over the % symbols.								
Proposed change affects: \$\(\mathbb{X}\) \(\mathbb{X}\) \(\mathbb{X}\) \(\mathbb{Z}\) \(
Title: 第	Minimum	clock frequ	ency update	ed				
Source: #	SA3							
Work item code: 第	Security					Date: ♯	1/3/2001	
Category: Ж	F					Release: ೫	R99	
	F (es. A (co B (Ac C (Fu D (Ec) Detailed ex	ldition of feat Inctional mod litorial modific	etion) a correction in ure), lification of fea cation) the above ca	ature)		2	the following re (GSM Phase 2 (Release 1996 (Release 1997 (Release 1998 (Release 1999 (Release 4) (Release 5)	?) ?) ?)
Reason for change	e: # Advis	se from T3 is	taken into ac	count				
Summary of chang		imum clock n 3.25 MHz		sed by th	e termi	nal during US	IM sessions is	updated
Consequences if not approved:	ж The	requiremer	nts for auther	ntication	algorith	m are a bit to	o loose.	
Clauses affected:	99 54	-						
Clauses affected:	3.1.	5						
Other specs affected:	Т	Other core specificolombics Sest specificolombics Sest Specificolombics		*				
Other comments:	*							

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at: http://www.3gpp.org/3G Specs/CRs.htm. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked **%** contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under ftp://www.3gpp.org/specs/ For the latest version, look for the directory name with the latest date e.g. 2000-09 contains the specifications resulting from the September 2000 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

5.1.5 Implementation and operational considerations

The functions f1—f5, f1* and f5* shall be designed so that they can be implemented on an IC card equipped with a 8-bit microprocessor running at 3.25 MHz with 8 kbyte ROM and 300byte RAM and produce AK, XMAC-A, RES, CK and IK in less than 500 ms execution time.