**3GPP TSG-RAN2 Meeting #121-bis-e *R2-2304224***

**Online, 17th Apr 2023 - 26th Apr 2023**

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| *CR-Form-v12.2* |
| **CHANGE REQUEST** |
|  |
|  | **38.331** | **CR** | **4032** | **rev** | **1** | **Current version:** | **17.4.0** |  |
|  |
| *For* ***[HE](http://www.3gpp.org/3G_Specs/CRs.htm%22%20%5Cl%20%22_blank)******[LP](http://www.3gpp.org/3G_Specs/CRs.htm%22%20%5Cl%20%22_blank)*** *on using this form: comprehensive instructions can be found at <http://www.3gpp.org/Change-Requests>.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network | **x** | Core Network |  |

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| ***Title:***  | Corrections on deriving timer length for DRX timers |
|  |  |
| ***Source to WG:*** | ZTE Corporation, Sanechips, ASUSTeK, vivo |
| ***Source to TSG:*** | RAN2 |
|  |  |
| ***Work item code:*** | NR\_SL\_enh-Core |  | ***Date:*** | 2023-4-7 |
|  |  |  |  |  |
| ***Category:*** | **F** |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19)* |
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| ***Reason for change:*** | In the current specification, the unit of drx-HARQ-RTT-TimerSL and drx-RetransmissionTimerSL is number of symbols or slots of the BWP where the PDCCH was transmitted. For CG type1 which is configured by RRC signaling, no PDCCH scheduling was needed for CG type1 grant. In this case, considering only one activated BWP is supported per carrier in Uu interface, the reference of drx-HARQ-RTT-TimerSL and drx-RetransmissionTimerSL should be the activated BWP in PCell. |
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| ***Summary of change:*** | Modify the field description of drx-HARQ-RTT-TimerSL and drx-RetransmissionTimerSL.**Impact analysis**Impacted functionality: DRX for SL.Inter-operability: 1. If the UE is implemented according to this CR but the network is not, the length of drx-HARQ-RTT-TimerSL and drx-RetransmissionTimerSL may not be aligned between NW and UE.
2. If the network is implemented according to this CR but the UE is not, the length of drx-HARQ-RTT-TimerSL and drx-RetransmissionTimerSL may not be aligned between NW and UE.
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| ***Consequences if not approved:*** | The reference for drx-HARQ-RTT-TimerSL and drx-RetransmissionTimerSL to derive the length is missing |
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| ***Clauses affected:*** | 6.3.5 |
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|  | **Y** | **N** |  |  |
| ***Other specs*** |  |  |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  |  |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  |  |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** | R2-2303907 |

Start of the change

### 6.3.5 Sidelink information elements

<irrelevant text removed>

#### *– DRX-ConfigSL*

The IE *DRX-ConfigSL* is used to configure additional DRX parameters for the UE performing sidelink operation with resource allocation mode 1, as specified in TS 38.321 [3].

*DRX-ConfigSL* information element

-- ASN1START

-- TAG-DRX-CONFIGSL-START

DRX-ConfigSL-r17 ::= SEQUENCE {

 drx-HARQ-RTT-TimerSL-r17 INTEGER (0..56),

 drx-RetransmissionTimerSL-r17 ENUMERATED {sl0, sl1, sl2, sl4, sl6, sl8, sl16, sl24, sl33, sl40, sl64, sl80, sl96, sl112, sl128,

 sl160, sl320, spare15, spare14, spare13, spare12, spare11, spare10, spare9, spare8,

 spare7, spare6, spare5, spare4, spare3, spare2, spare1}

}

-- TAG-DRX-CONFIGSL-STOP

-- ASN1STOP

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| *DRX-ConfigSL* field descriptions |
| ***drx-HARQ-RTT-TimerSL***For sidelink configured grant Type 1, value in number of symbols of PDCCH on the activated BWP of PCell. For other cases,value in number of symbols of the BWP where the PDCCH was transmitted. Value 0 is used in case *sl-PUCCH-Config* is not configured and the corresponding resource pool is not configured with PSFCH. |
| *drx-RetransmissionTimerSL*For sidelink configured grant Type 1, value in number of slot of PDCCH on the activated BWP of PCell. For other cases,value in number of slot lengths of the BWP where the PDCCH was transmitted. *sl0* corresponds to 0 slots, *sl1* corresponds to 1 slot, *sl2* corresponds to 2 slots, and so on. |

<irrelevant text removed>

End of the change