3GPP TSG-RAN WG2 #117-e Tdoc R2-22xxxxx

Electronic meeting, 2022-02-21 - 2022-03-03

Agenda Item: 8.21.2

Source: Ericsson

Title: [AT117-e][050][NR17TEI] Explicit Indication of SI Scheduling start position (Ericsson)

Document for: Discussion, Decision

# 1 Introduction

# 1 Introduction

This document is to collect comments for the CR:

* [AT117-e][050][NR17TEI] Explicit Indication of SI Scheduling start position (Ericsson)

 Scope: Treat R2-2203365

 Intended outcome: Agreed CR.

 Deadline: W1 Friday (if possible)

Please provide your comments related to the CR

[R2-2203365](https://www.3gpp.org/ftp/tsg_ran/WG2_RL2/TSGR2_117-e/Docs/R2-2203365.zip) Explicit Indication of SI Scheduling start position [SI-SCHEDULING] Ericsson, Verizon, Softbank, Deutsche Telekom, vivo CR Rel-17 38.331 16.7.0 2953 - B TEI1

# 2 Contact Information

|  |  |
| --- | --- |
| Company | Contact: Name (E-mail) |
| Qualcomm Incorporated | Masato KITAZOE (mkitazoe@qti.qualcomm.com) |
| vivo | yangxiaodong5g@vivo.com |
| Samsung  | June Hwang (june77.hwang@samsung.com) |
| Apple | Zhibin Wu (zhibin\_wu@apple.com) |
| Huawei, HiSilicon | Dawid Koziol (dawid.koziol@huawei.com) |
| Lenovo | Hyung-Nam Choi (hchoi5@lenovo.com) |
| OPPO | Jiangsheng Fan (fanjiangsheng@oppo.com) |
| SoftBank | Katsunari Uemura (katsunari.uemura@g.softbank.co.jp) |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |

# 3 Comments

**Please provide the comments on the CR here:**

|  |  |
| --- | --- |
| Company | Comments |
| Qualcomm Incorporated | The following text for *Cond FIRST-SI* is more about semantics (rather than presence condition) and should be moved to the field description.* *If this field is absent for the subsequent SI messages, the field value is the value of the previous entry in the schedulingInfoList2 plus 1, i.e the SI messages are scheduled in consecutive SI window order (plus one) until the field is present again.*
 |
| vivo | Agree with Qualcomm. Moreover, suggest the “SI window start position” is changed to “SI window position”. |
| Samsung  | Agree with QC comment.  |
| Apple | 1. We share the same view as Qualcomm that this needs to be moved to field description.
2. Also, regarding the same sentence, there is no real “field value” if the field is absent. So, we suggest to make the following change:If this field is absent for the subsequent SI message, the window position of the corresponding SI message is determined based on the field value of the most recent present entry in the *schedulingInfoList2* by assumingthe SI message(s) after that entry are scheduled in consecutive SI window order (plus one) until the field is present again
 |
| Huawei, HiSilicon | 1. We agree with the comment from QCM.
2. We think it is better from overhead perspective to introduce this change via nonCriticalExtension of SIB1, i.e.:

SIB1-v17xy-IEs ::= SEQUENCE { SI-SchedulingInfo-v17xy SEQUENCE { schedulingInfoList2-r17 SEQUENCE (SIZE (1..maxSI-Message)) OF SchedulingInfo2-r17 OPTIONAL -– Need R } OPTIONAL, -- Need R nonCriticalExtension SEQUENCE {} OPTIONAL}1. On the following field:

si-WindowPosition-r17 INTEGER (1..256) We are wondering why such large values are required which wastes a lot of memory for the UE. Considering the maximum number of SI messages is 32, we think the maximum value for si-WindowPosition of 96 is enough.1. On “-- Cond FIRST-SI” – we would prefer to make this field always present (i.e. make it non-optional). Current handling of its absence is unnecessarily complicated.
2. On the following line:

 valueTag-r17 INTEGER (0..31) OPTIONAL, -- Cond SIB-TYPE “Cond SIB-TYPE” cannot be reused here as:* SIB6, SIB7 or SIB8 cannot be scheduled in SchedulingInfo2 anyway.
* Value tag is not used for posSIBs

So we would need a code described as follows for example:“The field is mandatory present when *sibType* is set to *type1*. Otherwise, it is absent.”1. For sib-MappingInfo field description, posSIBs should be mentioned as well.
2. In type1 and posSibType field descriptions, do we need to mention exact types that cannot be configured? Perhaps it is OK as a placeholder/reminder, but in the end the applicable posSIB and SIB types will be anyway part of ASN.1 signalling, right?
 |
| Lenovo | 1. We agree with Huawei’s comment #2, i.e. to limit signaling overhead schedulingInfoList2-r17 should be introduced using R17 SIB1 NCE.
2. Regarding the max value of 256 for si-WindowPosition-r17:

We did some calculations and we think the value of 256 is justified to support new SCS of 480/960kHz in the context of NR extension to 71GHz. Acc. to our calculations it covers the max configuration of 5120ms si-periodicity, 1280 slots si-WindowLength and 960 kHz SCS.1. Regarding the UE support of this feature we suppose it is conditionally mandatory, i.e. a UE that supports the R17 SIBs and posSIBs has to support this feature. This should be clarified/confirmed.
 |
| OPPO | We think *si-WindowPosition-r17* should be always present, handling of its absence is totally an overhead optimization and but this optimization is not so critical and make the spec complex, so prefer to not have this “-- Cond FIRST-SI”. |
| SoftBank | Agree with Lenovo’s comment#3. This feature should be conditionally mandatory for the UE supporting Rel-17 SIBs. We think 38.306 may be needed for clarifying this point.Minor suggestions: 1) use the latest version of coversheet, 2) remove 5.2.2.3.3 from the “Clauses affected:”. |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |

# Conclusion

In the previous sections we made the following observations: