

TSG-RAN Working Group 1 AH21

***TSGR1 (00)0825***

Espoo, Finland, June 14 ~ 15, 2000

**Agenda Item:** AH21  
**Source:** CWTS  
**To:** TSG RAN WG1  
**Title:** Example of multi-frame structure  
**Document for:** Information and discussion

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## **Introduction**

In this paper, example of the multi-frame structure for CCPCH is described. The period for the CCPCH of the low chip rate TDD is the same as high chip rate TDD that is 720ms.

## **Conclusion**

It is proposed to discuss and include the following text proposal in Annex B of chapter 7 in TR25.928.

----- changes to TR25.928 begin -----

## 4 Physical channels and mapping of transport channels onto physical channels

### Annex B (Informative): CCPCH Multiframe Structure

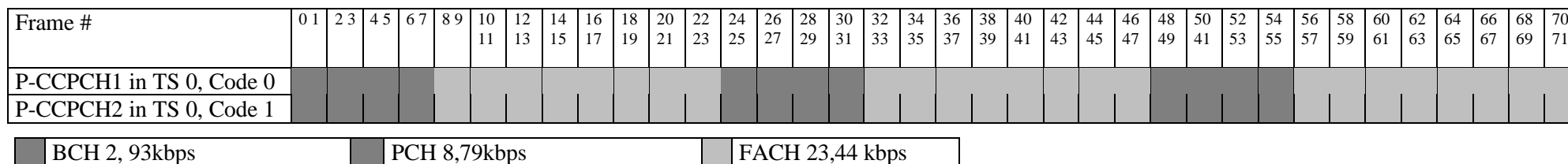


Figure B.1: Example for a multiframe structure for P-CCPCHs that is repeated every 72th frame (144 sub-frame)

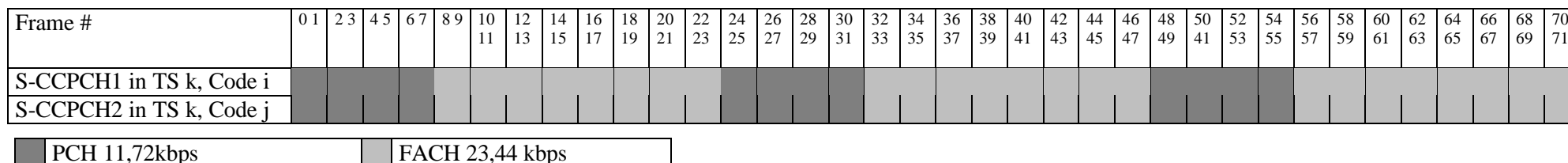


Figure B.2: Example for a multiframe structure for S-CCPCHs that is repeated every 72th frame,  $i, j=1 \dots 16$  (144 sub-frame)

The two codes at SF 16 which define the two P-(S)CCPCHs, are jointly used to deliver the same higher layer message! this means that it not possible to send contemporarily two different messages (e.g. BCH and PCH) onto the two different codes of the P-(S)CCPCHs.

----- changes to TR25.928 end -----