

Agenda Item: AH21
Source: CWTS
To: TSG RAN WG1
Title: Downlink transmit diversity
Document for: Discussion and Approval

Introduction

This paper describes the downlink transmit diversity in low chip rate TDD.

Conclusion

It's proposed to discuss and include the following text proposal into the TR25.928.

----- changes to TR25.928 begin -----

[Description:]

The different downlink transmit diversity schemes for different channel has been considered in low chip rate TDD option.

[Rationale:]

10.5 Downlink Transmit Diversity

10.5.1 Transmit Diversity for DPCH

Common with the high chip rate TDD.

10.5.2 Transmit Diversity for SCH

The SCH function in high chip rate TDD has been achieved by DwPTS in low chip rate TDD and transmit diversity schemes for the DwPTS in low chip rate TDD is common with that for SCH in high chip rate TDD.

10.5.3 Transmit Diversity for P-CCPCH

Transmit diversity for P-CCPCH as in the high chip rate option (i.e., Block STTD) is not supported in low chip rate TDD.

[Explanation difference:]

Because both the uplink and downlink physical channels are work under the synchronization mode and "smart antenna" method is strongly recommended to be used, so the interference on P-CCPCH is thought to be very little in low chip rate TDD option. As a result, "Tx diversity" has not been considered to be applied in low chip rate TDD. BSTTD applied for P-CCPCH can be taking into consideration.

10.5.2 Transmit Diversity for FPACH

The same scheme as for the DPCH can be used for the FPACH.