Agenda Item: AH21
Source: CWTS
To: TSG RAN WG1
Title: The synchronisation channels (DwPCH, UpPCH)
Document for: Discussion and Approval

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1 Summary

There are two dedicated physical synchronisation channels —DwPCH and UpPCH in each sub-frame of the 1.28Mcps TDD as described in subclause of 'Frame Structure'. DwPCH is used for the down link synchronisation and UpPCH is used for the uplink synchronisation.

2 Proposal

It's proposed to discuss and include the following text proposal into the clause 6.3.4 The synchronisation channels (DwPCH,UpPCH).

6.3.4   The synchronisation channels (DwPCH,UpPCH)

There are two dedicated physical synchronisation channels —DwPCH and UpPCH in each 5ms sub-frame of the 1.28Mcps TDD. DwPCH is used for the down link synchronisation and UpPCH is used for the up link synchronisation.

The position and the contents of the DwPCH are equal to the DwPTS as described in the subclause of the 'frame structure'. While the position and the contents of the UpPCH are equal to the UpPTS.
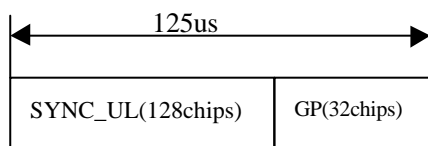
The burst structure of the DwPCH (DwPTS) is described in the figure X1.

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|<-------------------- 75us -------------------->|

| GP(32chips) |      SYNC_DL(64chips)      |
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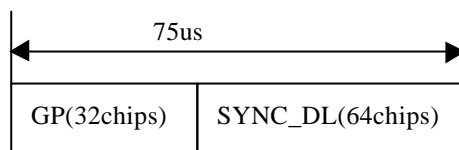
Figure X1: burst structure of the DwPCH (DwPTS)

Note:' GP' for 'Guard Period'

The burst structure of the UpPCH (UpPTS) is described in the figure X2.

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|<------------------- 125us ------------------->|

|      SYNC_UL(128chips)       | GP(32chips) |
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Figure X2: burst structure of the UpPCH (UpPTS)

The SYNC_DL code in DwPCH and the SYNC_UL code in UpPCH are not spreaded. The detail about the SYNC_DL and SYNC_UL code are described in the corresponding subclause and annex in TS25.223.