**3GPP TSG RAN WG1 #109-e R1-220xxxx**

**e-Meeting, May 9th – 20th, 2022**

**Agenda item:** 8.1.2

**Source:** Moderator (vivo)

**Title:** Feature lead Summary [109-e-R17-MIMO-03] Maintenance on multi-TRP inter-cell

**Document for:** Discussion and Decision

1. Introduction

Following email thread is assigned for email discussion on maintenance of intercell mTRP, please provide your comments in corresponding sections below

[109-e-R17-MIMO-03] Maintenance on multi-TRP inter-cell (description of issues in R1-220XXXX) by May 18 – Rakesh (vivo)

* Issues #1, #4, #5 and #8
* Issues #2 and #3
* Issue #6
* Issue #7 (no further discussion in next meeting if no consensus in this meeting)
1. Discussion

**Issues#1, #4, #5, #8:**

Following 4 issues are proposed for discussion,

* **Issue#1**

TPs for 38.213 in sections 9.2.6, 11.1, 11.1.1:

*ServingCellConfigCommon* or, if the UE is not provided *DLorJoint-TCIState* or *followUnifiedTCIstate*, by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* associated to physical cell ID with active TCI states for PDSCH or PDCCH

* **Issue#4**

TPs for 38.214 in sections 9.2.6, 11.1, 11.1.1

to a UE by *ssb-PositionsInBurst* in *SIB1* or *ssb-PositionsInBurst* in *ServingCellConfigCommon* or, if the UE is not provided *DLorJoint-TCIState* or *followUnifiedTCIstate*, by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI*~~associated to physical cell ID with active TCI states~~.

* **Issue#5**

TP for 38.214 in Section 6.1.2.1

--Unchanged part omitted------------------------

For PUSCH repetition Type B, the UE determines invalid symbol(s) for PUSCH repetition Type B transmission as follows:

- A symbol that is indicated as downlink by *tdd-UL-DL-ConfigurationCommon* or *tdd-UL-DL-ConfigurationDedicated* is considered as an invalid symbol for PUSCH repetition Type B transmission.

- For operation in unpaired spectrum, symbols indicated by *ssb-PositionsInBurst* in SIB1, or by *ssb-PositionsInBurst* in *ServingCellConfigCommon*, or by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* for reception of SS/PBCH blocks are considered as invalid symbols for PUSCH repetition Type B transmission.

- For a reduced capability half-duplex UE in paired spectrum and for PUSCH repetition Type B transmission, symbols indicated by *ssb-PositionsInBurst* in SIB1, or by *ssb-PositionsInBurst* in *ServingCellConfigCommon*, or by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* for reception of SS/PBCH blocks are considered as invalid symbols for PUSCH repetition Type B transmission.

--Unchanged part omitted----------------

- If the UE

- is configured with multiple serving cells within a cell group and is provided with *directionalCollisionHandling-r16* = 'enabled' for a set of serving cell(s) among the multiple serving cells, and

- indicates support of *half-DuplexTDD-CA-SameSCS-r16* capability, and

- is not configured to monitor PDCCH for detection of DCI format 2-0 on any of the multiple serving cells,

- a symbol indicated to the UE for reception of SS/PBCH blocks in a first cell of the multiple serving cells by *ssb-PositionsInBurst* in *SIB1,* or by *ssb-PositionsInBurst* in *ServingCellConfigCommon*, or by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* is considered as an invalid symbol for PUSCH repetition Type B transmission in

- any of the multiple serving cells if the UE is not capable of simultaneous transmission and reception as indicated by *simultaneousRxTxInterBandCA* among the multiple serving cells, and

- any one of the cells corresponding to the same band as the first cell, irrespective of any capability indicated by *simultaneousRxTxInterBandCA*

--Unchanged part omitted-------------------

* **Issue#8:** related TP on this proposal can be discussed together with above TPs

Clarify that the following Rel-15/16 based procedures consider SSBs with a serving cell PCI or SSBs with an active additional PCI – a) UE does not expect the set of SSB symbols to be indicated as uplink symbols (38.213, Section 11.1 and Section 11.1.1), b) such SSB symbols are assumed to be invalid symbols in a nominal repetition for Type B PUSCH (38.214, Section 6.1.2.1), c) a slot is not counted towards repetition if PUCCH resource in that slot overlaps with an SSB (38.213, Section 9.2.6).

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**Issues#2, #3**

Following 2 issues are proposed for discussion,

* **Issue#2**

3 alternatives TP proposal for 38.214 in section 5.1.4 are addressing same issue with different wordings.

Alt. 1:

When receiving PDSCH scheduled by PDCCH with CRC scrambled by C-RNTI, MCS-C-RNTI, CS-RNTI, G-RNTI, G-CS-RNTI, MCCH-RNTI or PDSCHs with SPS, the REs corresponding to the configured or dynamically indicated resources in Clauses 5.1.4.1, 5.1.4.2 are not available for PDSCH. Furthermore, the UE assumes SS/PBCH block transmission according to *ssb-PositionsInBurst* if the PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources, and the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block associated with the same PCI is transmitted.

Alt. 2:

When receiving PDSCH scheduled by PDCCH with CRC scrambled by C-RNTI, MCS-C-RNTI, CS-RNTI, or PDSCHs with SPS, the REs corresponding to the configured or dynamically indicated resources in Clauses 5.1.4.1, 5.1.4.2 are not available for PDSCH. Furthermore, the UE assumes SS/PBCH block transmission according to *ssb-PositionsInBurst* if the PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources, the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted. When the UE is configured with [*NumberOfAdditionalPCI*] and is not provided *DLorJoint-TCIState* or *followUnifiedTCIstate*, if the PDSCH resource allocation overlaps with PRBs containing a candidate SS/PBCH block corresponding to a SS/PBCH block index provided by *ssb-PositionsInBurst* in *SSB-MTC-AdditionalPCI-r17* with same physical cell identity as the one associated with a RS having same quasi-collocation properties as the PDSCH, the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted.

<Unchanged text omitted>

Alt. 3:

--Unchanged part omitted------------------------

When receiving PDSCH scheduled by PDCCH with CRC scrambled by C-RNTI, MCS-C-RNTI, CS-RNTI, G-RNTI, G-CS-RNTI, MCCH-RNTI or PDSCHs with SPS, the REs corresponding to the configured or dynamically indicated resources in Clauses 5.1.4.1, 5.1.4.2 are not available for PDSCH. Furthermore, the UE assumes SS/PBCH block transmission according to *ssb-PositionsInBurst* if the PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources, and the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted. If PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources according to *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* with same physical cell identity as the one associated with a RS having same quasi-collocation properties as the PDSCH, the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted.

A UE is not expected to handle the case where PDSCH DM-RS REs are overlapping, even partially, with any RE(s) not available for PDSCH*.*

* **Issue#3**

TP for38.214 in section 5.1.6.2:

**5.1.6.2 DM-RS reception procedure**

**< Unchanged parts are omitted >**

If the UE receives the DM-RS for PDSCH and an SS/PBCH block associated with the same PCI in the same OFDM symbol(s), then the UE may assume that the DM-RS and SS/PBCH block are quasi co-located with 'typeD', if 'typeD' is applicable. Furthermore, the UE shall not expect to receive DM-RS in resource elements that overlap with those of the SS/PBCH block associated with the same PCI as the DM-RS, and the UE can expect that the same or different subcarrier spacing is configured for the DM-RS and SS/PBCH block in a CC except for the case of 240 kHz where only different subcarrier spacing is supported. A DMRS for PDSCH is said to be associated with an additional PCI if the activated TCI states for the PDSCH is associated with the additional PCI; otherwise, DMRS for PDSCH is associated with serving cell PCI.

**< Unchanged parts are omitted >**

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**Issue#6**

TP for 38.214 in section 5.1.5 is proposed for discussion,

5.1.5 Antenna ports quasi co-location

[…]

For a periodic CSI-RS resource in an *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *trs-Info*, the UE shall expect that a TCI-State indicates one of the following quasi co-location type(s):

- 'typeC' with an SS/PBCH block and, when applicable, 'typeD' with the same SS/PBCH block, or

- 'typeC' with an SS/PBCH block and, when applicable,'typeD' with a CSI-RS resource in an *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *repetition*~~.~~ , or

- 'typeC' with an SS/PBCH block and, when applicable, 'typeD' with the same SS/PBCH block, the reference RS may additionally be an SS/PBCH block having a PCI different from the PCI of the serving cell. The UE can assume center frequency, SCS, SFN offset are the same for SS/PBCH block from the serving cell and SS/PBCH block having a PCI different from the serving cell.

[…]

For a CSI-RS resource in an *NZP-CSI-RS-ResourceSet* configured without higher layer parameter *trs-Info* and without the higher layer parameter *repetition*, the UE shall expect that a TCI-State indicates one of the following quasi co-location type(s):

- 'typeA' with a CSI-RS resource in a *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *trs-Info* and, when applicable, 'typeD' with the same CSI-RS resource, or

- 'typeA' with a CSI-RS resource in a *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *trs-Info* and, when applicable, 'typeD' with an SS/PBCH block, or

- 'typeA' with a CSI-RS resource in a *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *trs-Info* and, when applicable, 'typeD' with a CSI-RS resource in a *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *repetition*, or

- 'typeB' with a CSI-RS resource in a *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *trs-Info* when 'typeD' is not applicable~~.~~ , or

- 'typeA' with a CSI-RS resource in a *NZP-CSI-RS-ResourceSet* configured with higher layer parameter *trs-Info* and, when applicable, 'typeD' with an SS/PBCH block, the reference RS may additionally be an SS/PBCH block having a PCI different from the PCI of the serving cell. The UE can assume center frequency, SCS, SFN offset are the same for SS/PBCH block from the serving cell and SS/PBCH block having a PCI different from the serving cell.

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**Issue#7**

Following is proposed for discussion,

* + - For each cell with additional PCI, LTE CRS pattern for rate matching can be configured by RRC signaling.

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1. Outcome of pre-phase discussion

The issues are summarized in the following table:

**Table 1 Summary**

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| **#** | **Issue (summary of CR proposal)** | **Companies** | **FL assessment**  | **Company inputs (if any)** |
| 1  | In 38.213 sections 9.2.6, 11.1, 11.1.1, following TP is proposed:*ServingCellConfigCommon* or, if the UE is not provided *DLorJoint-TCIState* or *followUnifiedTCIstate*, by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* associated to physical cell ID with active TCI states for PDSCH or PDCCH([R1-2203259](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip))FL: Propose to discuss the text proposal | ZTE | H[comments from companies are aligned, and proposed to discuss #1, #4, #5 and #8 together] | Apple: We agree this issue needs to be discussed, since the definition of active TCI states are unclear. We think issue #1, 4 and 8 can be discussed together.QC: Agree with H. We suggest discussing issues #1, #4, #5, #8 together. The meaning of “active” needs to be clarified taking into account the SSBs used for L1-RSRP measurements.ZTE: Agree with FL’s assessment. We also agree to discuss issues #1, #4, #5 and #8 together.OPPO: Agree with H and discuss issue 1,4,5,8 together. LGE: Agree with FL’s assessment.DOCOMO: Agree with H and discuss issue 1,4,5,8 together.Spreadtrum: Support to discuss #1, 4, 5, 8 togetherCATT: Support to discuss #1,4,5,8 togetherIntel: Agree to discuss #1,4,5,8 togetherSS: OK to discuss #1,4,5,8 together. But instead of discussing one by one, we suggest to formulate the common issue out of these items. From our understanding, the common issue is to clarify “active TCI for PCI other than the serving cell PCI”Lenovo: Agree with H and discuss issue 1,4,5,8 together. |
| 2 | Following 3 alternatives are proposed to for PDSCH rate matching.Alt. 1: TP for 38.214, section 5.1.4, When receiving PDSCH scheduled by PDCCH with CRC scrambled by C-RNTI, MCS-C-RNTI, CS-RNTI, G-RNTI, G-CS-RNTI, MCCH-RNTI or PDSCHs with SPS, the REs corresponding to the configured or dynamically indicated resources in Clauses 5.1.4.1, 5.1.4.2 are not available for PDSCH. Furthermore, the UE assumes SS/PBCH block transmission according to *ssb-PositionsInBurst* if the PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources, and the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block associated with the same PCI is transmitted. ([R1-2203](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)506)Alt. 2: TP for 38.214, section 5.1.4,When receiving PDSCH scheduled by PDCCH with CRC scrambled by C-RNTI, MCS-C-RNTI, CS-RNTI, or PDSCHs with SPS, the REs corresponding to the configured or dynamically indicated resources in Clauses 5.1.4.1, 5.1.4.2 are not available for PDSCH. Furthermore, the UE assumes SS/PBCH block transmission according to *ssb-PositionsInBurst* if the PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources, the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted. When the UE is configured with [*NumberOfAdditionalPCI*] and is not provided *DLorJoint-TCIState* or *followUnifiedTCIstate*, if the PDSCH resource allocation overlaps with PRBs containing a candidate SS/PBCH block corresponding to a SS/PBCH block index provided by *ssb-PositionsInBurst* in *SSB-MTC-AdditionalPCI-r17* with same physical cell identity as the one associated with a RS having same quasi-collocation properties as the PDSCH, the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted.<Unchanged text omitted>([R1-2203](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)856)Alt. 3: TP for 38.214, section 5.1.4,--Unchanged part omitted------------------------When receiving PDSCH scheduled by PDCCH with CRC scrambled by C-RNTI, MCS-C-RNTI, CS-RNTI, G-RNTI, G-CS-RNTI, MCCH-RNTI or PDSCHs with SPS, the REs corresponding to the configured or dynamically indicated resources in Clauses 5.1.4.1, 5.1.4.2 are not available for PDSCH. Furthermore, the UE assumes SS/PBCH block transmission according to *ssb-PositionsInBurst* if the PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources, and the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted. If PDSCH resource allocation overlaps with PRBs containing SS/PBCH block transmission resources according to *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* with same physical cell identity as the one associated with a RS having same quasi-collocation properties as the PDSCH, the UE shall assume that the PRBs containing SS/PBCH block transmission resources are not available for PDSCH in the OFDM symbols where SS/PBCH block is transmitted.A UE is not expected to handle the case where PDSCH DM-RS REs are overlapping, even partially, with any RE(s) not available for PDSCH*.*([R1-220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)4977)FL: agreement on PDSCH rate matching is not yet captured in 38.214, proposed to discuss the text proposals, exact wording is for further discussion | vivo, Samsung, Qualcomm | H [H: 9N: 1Proposed to discuss #2 and #3 together] | Apple: This seems to be non-essential.QC: This is obviously essential as the agreement is not captured yet. Support to discuss as H.ZTE: Agree with FL’s assessment.OPPO: Support the TP. It is a similar issue as issue 3. Maybe they can be discussed together. Ericsson: HLGE: Agree with FL’s assessment.DOCOMO: Agree with H. It can be discussed with issue 3 together.Spreadtrum: Agree with H. Regarding the FFS in below agreement, we have no related agreement. Discussion is needed.AgreementAgree on scheme1* Scheme1: PDSCH/PDCCH from non-serving cell (PCI) associated with TCI state and/or QCL-info is rate matched around non-serving cell SSB with the same PCI
* FFS: whether PDSCH /PDCCH from serving cell (PCI) is rate matched around non-serving cell SSB
* FFS: whether PDSCH/PDCCH from non-serving cell (PCI) associated with TCI state and/or QCL-info is rate matched around serving cell SSB

CATT: support to discuss #2, 3 togetherIntel: same view as CATT, discuss #2, 3 togetherSS: This is based on an earlier agreement, which should be captured in 214.Lenovo: Support FL’s assessment. |
| 3 | In 38.214, following TP is proposed:**5.1.6.2 DM-RS reception procedure**…If the UE receives the DM-RS for PDSCH and an SS/PBCH block associated with the same PCI in the same OFDM symbol(s), then the UE may assume that the DM-RS and SS/PBCH block are quasi co-located with 'typeD', if 'typeD' is applicable. Furthermore, the UE shall not expect to receive DM-RS in resource elements that overlap with those of the SS/PBCH block associated with the same PCI as the DM-RS, and the UE can expect that the same or different subcarrier spacing is configured for the DM-RS and SS/PBCH block in a CC except for the case of 240 kHz where only different subcarrier spacing is supported. A DMRS for PDSCH is said to be associated with an additional PCI if the activated TCI states for the PDSCH is associated with the additional PCI; otherwise, DMRS for PDSCH is associated with serving cell PCI.**< Unchanged parts are omitted >**([R1-2203](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)949)FL: propose to discuss the text proposal | OPPO | H[H: 8N: 1Propossed to discuss #2 and #3  together] | Apple: We do not think this is necessary.QC: Support to discuss as H.ZTE: Agree with FL’s assessment.OPPO: Support to discuss. Similar wording can be applied to issue 2 and 3, and maybe they can be discussed together. Without this TP, serving cell PDSCH cannot be transmitted in symbol with neighboring cell SSBLGE: Agree with FL’s assessment.DOCOMO: Agree with H. It can be discussed with issue 2 together.Spreadtrum: Agree with H, can be discussed together with issue 2.CATT: support to discuss as HLenovo: Support FL’s assessment. |
| 4 | In 38.214, TP for sections 9.2.6, 11.1, 11.1.1to a UE by *ssb-PositionsInBurst* in *SIB1* or *ssb-PositionsInBurst* in *ServingCellConfigCommon* or, if the UE is not provided *DLorJoint-TCIState* or *followUnifiedTCIstate*, by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI*~~associated to physical cell ID with active TCI states~~.([R1-220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)4977)FL: propose to discuss the text proposal | Qualcomm | H[Proposed to discuss #1, #4, #5 and #8 together] | Apple: We think issue #1, 4 and 8 can be discussed together.QC: Support to discuss as H. As mentioned above, we suggest discussing issues #1, #4, #5, #8 together.ZTE: Discuss issues #1, #4, #5 and #8 together.OPPO: Support to discuss issue 1,4,5,8 together. LGE: Discuss issues #1, #4, #5 and #8 together.DOCOMO: Discuss issues #1, #4, #5 and #8 together.Spreadtrum: Support to discuss #1, 4, 5, 8 togetherCATT: Support to discuss #1,4,5,8 together |
| 5 | =======TP for 38.214 Section 6.1.2.1 =====--Unchanged part omitted------------------------For PUSCH repetition Type B, the UE determines invalid symbol(s) for PUSCH repetition Type B transmission as follows:- A symbol that is indicated as downlink by *tdd-UL-DL-ConfigurationCommon* or *tdd-UL-DL-ConfigurationDedicated* is considered as an invalid symbol for PUSCH repetition Type B transmission. - For operation in unpaired spectrum, symbols indicated by *ssb-PositionsInBurst* in SIB1, or by *ssb-PositionsInBurst* in *ServingCellConfigCommon*, or by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* for reception of SS/PBCH blocks are considered as invalid symbols for PUSCH repetition Type B transmission.- For a reduced capability half-duplex UE in paired spectrum and for PUSCH repetition Type B transmission, symbols indicated by *ssb-PositionsInBurst* in SIB1, or by *ssb-PositionsInBurst* in *ServingCellConfigCommon*, or by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* for reception of SS/PBCH blocks are considered as invalid symbols for PUSCH repetition Type B transmission.--Unchanged part omitted----------------- If the UE - is configured with multiple serving cells within a cell group and is provided with *directionalCollisionHandling-r16* = 'enabled' for a set of serving cell(s) among the multiple serving cells, and- indicates support of *half-DuplexTDD-CA-SameSCS-r16* capability, and- is not configured to monitor PDCCH for detection of DCI format 2-0 on any of the multiple serving cells, - a symbol indicated to the UE for reception of SS/PBCH blocks in a first cell of the multiple serving cells by *ssb-PositionsInBurst* in *SIB1,* or by *ssb-PositionsInBurst* in *ServingCellConfigCommon*, or by *ssb-PositionsInBurst* in *SSB-MTCAdditionalPCI* is considered as an invalid symbol for PUSCH repetition Type B transmission in- any of the multiple serving cells if the UE is not capable of simultaneous transmission and reception as indicated by *simultaneousRxTxInterBandCA* among the multiple serving cells, and- any one of the cells corresponding to the same band as the first cell, irrespective of any capability indicated by *simultaneousRxTxInterBandCA*--Unchanged part omitted-------------------([R1-220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)4977)FL: propose to discuss the text proposal | Qualcomm | H[Proposed to discuss #1, #4, #5 and #8 together] | Apple: This seems to be non-essential.QC: Support to discuss as H. As mentioned above, we suggest discussing issues #1, #4, #5, #8 together.ZTE: Discuss issues #1, #4, #5 and #8 together.OPPO: Support to discuss issue 1,4,5,8 together.LGE: Discuss issues #1, #4, #5 and #8 together.DOCOMO: Discuss issues #1, #4, #5 and #8 together.Spreadtrum: Support to discuss #1, 4, 5, 8 togetherCATT: Support to discuss #1,4,5,8 togetherIntel: CATT: Support to discuss #1,4,5,8 togetherLenovo: Support to discuss #1,4,5,8 together |
| 6 | Corresponding TP for 5.1.5 is also proposed* + ***Specify QCL source RS of the TRS and CSI-RS for CSI can be SSB with PCI different from serving cell for M-TRP inter-cell in TS38.214.***

([R1-220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)4336)FL: This issue has been discussed in past meetings, it can be further discussed in this meeting, and conclude in this meeting.  | NTT DOCOMO | [Companies can indicate H or N][H: 10N: 0Proposed for discussion] | Apple: OK to discussQC: The issue seems valid, and may be different than the previous discussions. In current spec, only CSI-RS for BM can have SSB with PCI different from serving cell PCI as QCL source RS. In our understanding, there is no agreement for the existing restriction in the spec.ZTE: Support to discuss this issue as H.OPPO: Support to discuss as H.LGE: Support to discuss as H.DOCOMO: H.Spreadtrum: fine to discussCATT: Support to discussIntel: Good to discussLenovo: OK to discuss. |
| 7 | * + ***For each cell with additional PCI, LTE CRS pattern for rate matching can be configured by RRC signaling.***

([R1-220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)4336)FL: This issue has been discussed in past meetings, it can be further discussed in this meeting, and conclude in this meeting. | NTT DOCOMO | [Companies can indicate H or N][H: 5N: 4Proposed to discuss in this meeting, no further discussion in next meeting if no consensus in this meeting ] | Apple: OK to discussQC: This is N. It has been discussed multiple times before. Optimizations for DSS use case in this AI at this stage is not needed.ZTE: Support to discuss this issue as H. It is very critical to ease the complexity of UE implementation (similar as X1 and X2 were introduced for non-serving cell SSB related rate matching) and avoid too much restriction of gNB schedule (i.e. RRC reconfiguration of LTE-CRS rate matching pattern is needed when considering the PCI of one CORESET pool index is updated by MAC-CE). Btw, we fail to see logic that this issue is relevant to the optimization of DSS as QC commented before.OPPO: We can discuss this issue, but with lower priority.LGE: N. we have the same view with QC.DOCOMO: H.Ericsson: H. We don’t see the argument why DSS should be excluded from inter-cell mTRP? Why does the operator have to choose between these two features?Spreadtrum: fine to discussCATT: Support to discussLenovo: Share similar view with QC. |
| 8 | ***Clarify that the following Rel-15/16 based procedures consider SSBs with a serving cell PCI or SSBs with an active additional PCI – a) UE does not expect the set of SSB symbols to be indicated as uplink symbols (38.213, Section 11.1 and Section 11.1.1), b) such SSB symbols are assumed to be invalid symbols in a nominal repetition for Type B PUSCH (38.214, Section 6.1.2.1), c) a slot is not counted towards repetition if PUCCH resource in that slot overlaps with an SSB (38.213, Section 9.2.6).***([R1-220](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip)4764)FL: in RAN1#108-e, it was agreed that UE does not transmit PUCCH/PUSCH/PRACH in a slot or SRS in the symbols if in time domain the PUCCH/PUSCH/PRACH/SRS overlaps with an SSB of a serving cell PCI or an SSB associated with the active additional PCI. Whether the proposed clarification is needed or not can be discussed, and conclude in this meeting. | Intel | [Companies can indicate H or N][Propose to discuss #1, #4, #5 and #8 together] | Apple: We think issue #1, 4 and 8 can be discussed together.QC: For a) and c), this is already captured by the spec (some discussions on “active” is needed as in Issue 1 and 4). For b), this is same as issue 5.ZTE: Agree with QC’s elaboration and suggest to discuss issues #1, #4, #5 and #8 together.OPPO: Support to discuss issue 1,4,5,8 together.LGE: Discuss issues #1, #4, #5 and #8 together.DOCOMO: Discuss issues #1, #4, #5 and #8 together.Spreadtrum: Support to discuss #1, 4, 5, 8 togetherCATT: Support to discuss #1,4,5,8 togetherIntel: Support to discuss #1,4,5,8 togetherLenovo: Support to discuss #1,4,5,8 together. |
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1. Observations from pre-phase discussion

From the inputs shared by participating companies during the preparation phase, the following **observation** can be made:

* Almost all companies agree to discuss #1, #4, #5 and #8 together
* Vast majority of companies agree to discuss #2 and #3, few companies proposed to discuss them together
* All companies agree to discuss #6
* Views are split on #7, almost half/half on necessity to discuss.

FL proposal: in this meeting

* Discuss #1, #4, #5 and #8 together
* Discuss #2 and #3 together
* Discuss #6
* Discuss #7 and no further discussion in next meeting if no consensus in this meeting

# References

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| --- | --- | --- | --- |
| 1 | [R1-2203259](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203259.zip) | Remaining issues on multi-TRP deployment | ZTE |
| 2 | R1-2203506 | Maintenance on enhancements for multi-TRP Deployment | vivo |
| 3 | [R1-2203856](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203856.zip) | Maintenance on Rel-17 multi-TRP and HST-SFN | Samsung |
| 4 | [R1-2203949](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2203949.zip) | Maintenance on enhancements for mTRP deployment | OPPO |
| 5 | [R1-2204336](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2204336.zip) | Remaining issues on enhancements for Multi-TRP Deployment | NTT DOCOMO, INC. |
| 6 | R1-2204764 | MIMO Enhancements for Multi-TRP Deployment | Intel Corporation |
| 7 | [R1-2204977](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_109-e/Docs/R1-2204977.zip) | Remaining Details for Multi-TRP Operation | Qualcomm Incorporated |