**3GPP TSG RAN WG1 #108-e** **R1-22xxxxx**

e-Meeting, February 21st – March 3rd, 2022

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| --- |
| *CR-Form-v12.1* |
| **DRAFT CHANGE REQUEST** |
|  |
|  | **38.213** | **CR** |  | **rev** |  | **Current version:** | **17.0.0** |  |
|  |
| *For* [***HELP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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|  |
| ***Title:***  | Introduction of carrier BW smaller than 40 MHz for n79 |
|  |  |
| ***Source to WG:*** | Samsung |
| ***Source to TSG:*** | R1 |
|  |  |
| ***Work item code:*** | NR\_bands\_R17\_BWs-Core  |  | ***Date:*** | 2022-03-07 |
|  |  |  |  |  |
| ***Category:*** | **C** |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
|  |  |
| ***Reason for change:*** | Correction to maintain backward compatibility due to the minimum bandwidth change of n79 (see LS R4-2202286). |
|  |  |
| ***Summary of change:*** | Capture how a UE determines a number of consecutive RBs and a number of consecutive symbols for the CORESET linked to the Type0-PDCCH CSS set when the UE is in band n79. |
|  |  |
| ***Consequences if not approved:*** | Legacy UEs and Rel-17 UEs may use different tables to determine the CORESET for Type0-PDCCH CSS set.  |
|  |  |
| ***Clauses affected:*** | 13 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

## 13 UE procedure for monitoring Type0-PDCCH CSS sets

\*\*\* Unchanged text is omitted \*\*\*

Table 13-3: Set of resource blocks and slot symbols of CORESET for Type0-PDCCH search space set when {SS/PBCH block, PDCCH} SCS is {30, 15} kHz for frequency bands with minimum channel bandwidth 5 MHz or 10 MHz except for the frequency bands given in [5, TS38.101-1]

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Index | SS/PBCH block and CORESET multiplexing pattern  | Number of RBs  | Number of Symbols   | Offset (RBs)  |
| 0 | 1 | 48 | 1 | 2 |
| 1 | 1 | 48 | 1 | 6 |
| 2 | 1 | 48 | 2 | 2 |
| 3 | 1 | 48 | 2 | 6 |
| 4 | 1 | 48 | 3 | 2 |
| 5 | 1 | 48 | 3 | 6 |
| 6 | 1 | 96 | 1 | 28 |
| 7 | 1 | 96 | 2 | 28 |
| 8 | 1 | 96 | 3 | 28 |
| 9 | Reserved |
| 10 | Reserved |
| 11 | Reserved |
| 12 | Reserved |
| 13 | Reserved |
| 14 | Reserved |
| 15 | Reserved |

Table 13-4: Set of resource blocks and slot symbols of CORESET for Type0-PDCCH search space set when {SS/PBCH block, PDCCH} SCS is {30, 30} kHz for frequency bands with minimum channel bandwidth 5 MHz or 10 MHz except for the frequency bands given in [5, TS38.101-1]

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Index | SS/PBCH block and CORESET multiplexing pattern  | Number of RBs  | Number of Symbols   | Offset (RBs)  |
| 0 | 1  | 24 | 2 | 0 |
| 1 | 1  | 24 | 2 | 1 |
| 2 | 1  | 24 | 2 | 2 |
| 3 | 1  | 24 | 2 | 3 |
| 4 | 1  | 24 | 2 | 4 |
| 5 | 1  | 24 | 3 | 0 |
| 6 | 1  | 24 | 3 | 1 |
| 7 | 1  | 24 | 3 | 2 |
| 8 | 1  | 24 | 3 | 3 |
| 9 | 1  | 24 | 3 | 4 |
| 10 | 1  | 48 | 1 | 12 |
| 11 | 1  | 48 | 1 | 14 |
| 12 | 1  | 48 | 1 | 16 |
| 13 | 1  | 48 | 2 | 12 |
| 14 | 1  | 48 | 2 | 14 |
| 15 | 1  | 48 | 2 | 16 |

\*\*\* Unchanged text is omitted \*\*\*

Table 13-5: Set of resource blocks and slot symbols of CORESET for Type0-PDCCH search space set when {SS/PBCH block, PDCCH} SCS is {30, 15} kHz for frequency bands with minimum channel bandwidth 40MHz or for the frequency bands given in [5, TS38.101-1]

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Index | SS/PBCH block and CORESET multiplexing pattern  | Number of RBs  | Number of Symbols   | Offset (RBs)  |
| 0 | 1 | 48 | 1 | 4 |
| 1 | 1 | 48 | 2 | 4 |
| 2 | 1 | 48 | 3 | 4 |
| 3 | 1 | 96 | 1 | 0 |
| 4 | 1 | 96 | 1 | 56 |
| 5 | 1 | 96 | 2 | 0 |
| 6 | 1 | 96 | 2 | 56 |
| 7 | 1 | 96 | 3 | 0 |
| 8 | 1 | 96 | 3 | 56 |
| 9 | Reserved |
| 10 | Reserved |
| 11 | Reserved |
| 12 | Reserved |
| 13 | Reserved |
| 14 | Reserved |
| 15 | Reserved |

Table 13-6: Set of resource blocks and slot symbols of CORESET for Type0-PDCCH search space set when {SS/PBCH block, PDCCH} SCS is {30, 30} kHz for frequency bands with minimum channel bandwidth 40MHz or for the frequency bands given in [5, TS38.101-1]

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Index | SS/PBCH block and CORESET multiplexing pattern  | Number of RBs  | Number of Symbols   | Offset (RBs)  |
| 0 | 1 | 24 | 2 | 0 |
| 1 | 1 | 24 | 2 | 4 |
| 2 | 1 | 24 | 3 | 0 |
| 3 | 1 | 24 | 3 | 4 |
| 4 | 1 | 48 | 1 | 0 |
| 5 | 1 | 48 | 1 | 28 |
| 6 | 1 | 48 | 2 | 0 |
| 7 | 1 | 48 | 2 | 28 |
| 8 | 1 | 48 | 3 | 0 |
| 9 | 1 | 48 | 3 | 28 |
| 10 | Reserved |
| 11 | Reserved |
| 12 | Reserved |
| 13 | Reserved |
| 14 | Reserved |
| 15 | Reserved |

\*\*\* Unchanged text is omitted \*\*\*