3GPP TSG RAN WG1 #106bis-e R1-200xxxx

e-Meeting, October 11th – 19th, 2021

**Agenda item: 8.1.2.1**

**Source:** **Moderator (Nokia, Nokia Shanghai Bell)**

**Title: Summary #1 of Multi-TRP PUCCH and PUSCH Enhancements**

**Document for: Discussion and Decision**

# Introduction

The Rel-17 work item for enhancements on MIMO for NR includes an objective to extend specification support for enhancements on multi-TRP/panel transmission. In RAN #86, the objectives were agreed to read as follows:

*Enhancement on the support for multi-TRP deployment, targeting both FR1 and FR2:*

* 1. *Identify and specify features to improve reliability and robustness for channels other than PDSCH (that is, PDCCH, PUSCH, and PUCCH) using multi-TRP and/or multi-panel, with Rel.16 reliability features as the baseline*

In this document, proposals on the reliability and robustness improvements for PUCCH and PUSCH are summarized in section 2 and 3. The agreements reached in previous RAN1 meetings are provided in Section 5.

# Multi-TRP PUCCH transmission

The remaining open issues and company views are summarized below.

## 2.1 Summary

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| **Issue** | **Summary from Tdocs** | **Moderator comments** |
| **Issue #2.1:** Mapping with Frequency hopping | Beam mapping for PUCCH scheme 1 with FH   * Option 1: **Fujitsu**, **Lenovo, CATT, CMCC, SS, Apple, Xiaomi, LG** * Option 2: * Option 3: **HW, vivo, MTek**   Related discussion in PUSCH   * Perform frequency hopping among the PUSCH repetitions with the same beam – **Lenovo, CATT, Fujitsu, E///, Xiaomi, vivo** | * Discussed during last three RAN1 meetings. FL suggested to select Option 1, but **objections were raised by MTek, vivo, OPPO, HW, Intel, FW.** * As the number of issues are limited on PUCCH, we can afford one more round of discussion.   **See FL proposal 2.1** |
| **Issue #2.2:** Number of Repetitions | Suggestions on number of repetitions   * Scheme 1 - PUCCH formats 1/3/4: 16 **(E///**) * Scheme 1 - PUCCH format 0/2: 4, 8, and 16 (**E///)** * Scheme 3: only 2 repetitions (**Xiaomi**)   Dynamic indication   * Reuse the mechanism for dynamic indication of number of repetitions agreed in Rel-17 coverage enhancement for multi-TRP PUCCH operation – **OPPO**   Other   * When the MAC-CE activating two spatial relation info or power control sets for a PUCCH resource, further clarifications (cases such as PUCCH format not configured with *nrofSlots* or indicated repetition factor is 1) is needed for UE assumption on the used spatial relation info or power control set. **MTek** | * Xiaomi’s proposal on limiting the number of repetitions for scheme 3 as 2 may not be aligned with the previous RAN1 agreement, as RAN1 agreed that the number of repetitions for Scheme 3 may be reused from Rel-17 IIoT sub-slot repetition discussion. * E///’s proposal (#repetitions = 16) was tried before and there was not enough support on that in earlier meetings. * OPPO’s suggestion is already mentioned before in Rel-17 feMIMO conclusion. * MTek’s suggestion may be needing some discussion.   **See FL proposal 2.2** |
| **Issue #2.3:** UCI multiplexing | * Support enhancements on UCI multiplexing for multi-TRP based PUCCH repetition in Rel-17 - **HW** * Rel-15/16 collision handling between PUCCH repetition and other channels/signals are also applied also for M-TRP schemes – **ZTE, E///** * When mTRP PUCCH needs to be dropped due to collision handling rule, only the PUCCH repetition in the overlapped symbols should be dropped. - **Apple** | * HW and Apple highlighting the need of discussing certain aspects on collision handling. * For m-TRP operation, as design is based on s-TRP repetitions, the rules define there shall be reused. This is also mentioned in ZTE and E/// proposals.   **See FL proposal 2.3** |
| **Issue #2.4:** switching of scheme & mapping patterns | * Need to discuss how to indicate mTRP PUCCH repetition. – **SS** * Support dynamic switching between the different multi-TRP PUCCH schemes. – **Nokia**   + Some details provided * DCI to indicate whether the PUCCH repetition should be based on sequential mapping or cyclic mapping – **Apple** | * mTRP vs sTRP PUCCH repetition is identified based on number of spatial relation info’s or number of power control parameter sets. RAN1 had a conclusion on that. * Also, sTRP inter-slot repetition and sTRP intra-slot switching may be discussed in Rel-17 IIoT/URLLC, and there was not enough support on this in the last meeting. * Apple’s suggestion was discussed before and there was no consensus on introducing additional indications of mapping pattern in dynamic manner.   **No FL proposal** |
| **Other** | * Support to configure the following RRC parameters as TRP specific. - **ZTE**   + 'initialCyclicShift' of PUCCH Format 0;   + 'initialCyclicShift' and 'timeDomainOCC' of PUCCH Format 1;   + 'dataScramblingIdentityPUSCH' of PUCCH Formats 2, 3 and 4. * Study how to determine up to two default beams and up to two default power control parameter sets, and further study how to indicate with which default beam(s) and default power control parameter set(s) a PUCCH is associated- **Lenovo**   Note: Some other proposals such as supporting Scheme 2 was not considered as we reached conclusion on no consensus. | * ZTE’s suggestion did not get good support in previous meetings and there may be no further time in Rel-17 to discuss these. * Lenovo’s suggestion is not clear enough to identify what is missing in Rel-17 work.   **No FL proposal** |

## 2.2 Feature lead Proposals

### Issue #2.1: Mapping with Frequency hopping

**Proposal 2.1:** When inter-slot frequency hopping is configured with Scheme 1, support the following,

* If sequential mapping pattern is configured, frequency hopping is performed on slot level (as in Rel-15).
* If cyclical mapping pattern is configured, frequency hopping is performed among the repetitions with the same beam.

Please comment on preferred changes to the proposal. Only a single round may be allowed on this given that we spent lot of time in last three meetings.

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| **Company** | **Comments** |
| **QC** | **Support the proposal.** |
| **InterDigital** | Don’t support, we think this can already be achieved through intra-slot FH. |
| **Apple** | Support |
| **Lenovo/MotM** | Support. |
| **vivo** | We don’t support FL’s proposal.  As we have discussed several times before, the performance gain on cyclical beam mapping pattern with FH among same beam versus sequential beam mapping pattern with Rel-15 FH is unclear. For PUCCH with 2 repetitions, cyclic mapping is applied regardless the configured beam pattern, and there is no FH diversity for both cases. For PUCCH with more repetitions, there are same level of beam and FH diversity gain for both cases. The early termination benefit as the proponents depicted is opportunistic for either case. As shown in the following figure where Case a) is cyclical beam mapping pattern with FH among same beam, and Case b) is sequential beam mapping pattern with Rel-15 FH.    Case a) Case b)  In the following table, we give the required number of repetitions for successful decoding if PUCCH is to be decoded successfully with frequency diversity for each beam   |  |  |  | | --- | --- | --- | |  | Case a | Case b | | Blockage on beam 1 | 4 | 4 | | Blockage on beam 2 | 3 | 2 |   Unfortunately, Case a, i.e., the proposed FH pattern for cyclical beam mapping pattern performs worse than Case b, i.e., sequential beam mapping pattern with Rel-15 FH, as Case b terminates earlier than or equal to Case a.  Thus, we don’t see the need to introduce a new frequency hopping. |
| **OPPO** | Not support since it is an overoptimization without much benefit. |
| **ZTE** | We prefer FH on slot level for both sequential and cyclical mapping, which was support by many companies in previous meetings and is indeed in line with Rel-15 design with no spec impact/change. |
| **Xiaomi** | Support the proposal |
| **LG** | Support the proposal |
| **Huawei, HiSilicon** | Do not support the proposal. We do not see the needs to support new FH pattern for cyclic mapping as the benefits have already been achieved by sequential mapping. |
| **CMCC** | Support the proposal. |
| **Fujitsu** | Support the proposal. |
| **CATT** | Support FL’s proposal. We prefer FH to be supported by both sequential and cyclical mapping and to achieve frequency diversity gain for each beam. |
| **Samsung** | Support the proposal. |
| **Nokia/NSB** | Not an essential proposal and prefer avoiding discussing this repetitively. |
| **FGI/APT** | Support the proposal. If this proposal cannot be agreed, we only need sequential beam mapping pattern when performing FH. Then, why do we need cyclical beam mapping pattern? |
| **Sharp** | Support the proposal |
| **Ericsson** | For inter-slot repetition, we also think that the existing Rel-15 FH mechanism should be adequate enough. |
| **Futurewei** | Not needed |
| **Intel** | Not essential in our view, this was not agreed for multiple meetings in the past |
| **FL update #1** | Around 8 companies (IDC, vivo, OPPO, HW, Nokia, FGI, E///, FW, Intel) have concerns. Proposal dropped. |

### Issue #2.2: Number of repetitions

**Proposal 2.2:** For Rel-17 multi-TRP PUCCH repetition schemes,

* If the PUCCH resource is associated with two spatial relation info’s or two power control parameter sets and the number of repetitions for the PUCCH resource is equal to one, the UE may only use the first spatial relation info or the first power control parameter set indicated by the MAC-CE.
* Note: The PUCCH resource may have a single repetition when the PUCCH format is not configured with *nrofSlots* or when the indicated repetition factor is 1 (when using dynamic indication as introduced in Rel-17 Coverage Enhancement WI)

Please comment on preferred changes to the proposal.

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| **Company** | **Comments** |
| **QC** | **We are not sure about the motivation. Why would the network activate two beams for a PUCCH resource that is not configured with repetitions (either per format or per resource)?** |
| **InterDigital** | Support FL’s proposal. |
| **Apple** | This should be considered as an error case |
| **NTT Docomo** | Similar view with QC |
| **Lenovo/MotM** | Similar view with Apple. |
| **vivo** | We share similar views as QC.  Besides, if NW does activate two spatial relations for a PUCCH resource without repetition, Scheme 2, if supported, should be applied with one beam mapped to one hop respectively. |
| OPPO | Not support. If I understood correctly, this proposal is motivated by the case raised in last meeting that PUCCCH resource(s) with repetition and without repetition are in the same PUCCH resource group. In our understanding, it can be avoided by proper configuration such as not to configure such types of PUCCH resources in a same PUCCH resource group. |
| **ZTE** | We share similar view with QC/Apple/etc that this is a error case in fact. |
| **Xiaomi** | Similar view that this can be considered as an error case. |
| **LG** | Similar view that this can be considered as an error case. |
| **Huawei, HiSilicon** | It seems the first step would be to decide whether such configuration is allowed, and if so, the behavior in the proposal would be aligned with current agreement of beam mapping. |
| **NEC** | We are fine to regard this as error case. |
| **CMCC** | This case can be avoided by network scheduling, there is no need to specify the default behavior. |
| **Fujitsu** | Similar view as majority that this can be considered as an error case. |
| **CATT** | Similar view as majority that this is an error case. Maybe can have a conclusion that “A PUCCH resource with repetition factor equals to 1 is not expected to be activated with two spatial relation info’s or two power control parameter sets by MAC-CE.” |
| **Samsung** | NW can handle this error case, i.e. NW will not activate two spatial relation info (or two sets of PC parameters) for the PUCCH resource which repetition = 1. |
| **Nokia** | We are fine with the FL’s proposal but are also fine to treat this as an error case. |
| **FGI/APT** | Similar view as QC. Perhaps we need to clarify what’s scenario needs such a configuration firstly. |
| **Sharp** | Similar view that this can be considered as an error case. |
| **Ericsson** | Isn’t this the same as for example, two repetitions are configured but the 2nd repetition was dropped for some reason. There should be no spec impact anyway. |
| **Futurewei** | Fine to view this as an error case not expected by a UE. |
| **Intel** | We think that PUCCH with 2 activated spatial relation info and 1 activated spatial relation info would not be in the same PUCCH resource group – in that case PUCCH resource is provisioned separately and this proposal is not needed |
| **FL update #1** | Majority views that no clarification is needed. Proposed conclusion is dropped. |

### Issue #2.3: UCI multiplexing

**Proposed conclusion 2.3:** Rel-15/16 collision handling between PUCCH repetition and other channels/signals are also applied also for Rel-17 M-TRP PUCCH repetition schemes

Please comment on preferred changes to the proposal.

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| **Company** | **Comments** |
| **QC** | **Ok with the conclusion.** |
| **InterDigital** | Support FL’s proposal. |
| **Apple** | We think the principle for legacy dropping rule can be maintained, but when PUCCH needs to be dropped, we suggest we only drop the PUCCH repetition in the overlap symbols instead of the whole PUCCH. Currently we support up to 8 PUCCH repetitions, it is not feasible to drop the entire PUCCH especially when collision happens at late repetition, since UE cannot predict the collision.  For multiplexing, we think when PUCCH + mTRP PUSCH, we can transmit UCI in two beams. |
| **NTT Docomo** | Support |
| **Lenovo/MotM** | OK with the conclusion. |
| **vivo** | Support. |
| **OPPO** | Support |
| **ZTE** | We can support this conclusion in principle. To be more precise, we suggest:  **Proposed conclusion 2.3:** Rel-15/16 collision handling between PUCCH repetition and other channels/signals are also maintained for Rel-17 M-TRP PUCCH repetition schemes |
| **Xiaomi** | Support the proposal in general |
| **LG** | Support |
| **Huawei, HiSilicon** | We support to multiplexing instead of dropping UCI when PUCCH repetitions collision with other PUCCH transmission, as the dropping may degrade URLLC traffic performance especially when the dropped UCI contains SR.  For collision between PUCCH and multi-TRP PUSCH, we support to multiplex UCI on multi-TRP PUSCH to improve the reliability of UCI transmission. |
| **CMCC** | Support the conclusion. |
| **Fujitsu** | Similar view as Apple. But also OK if majority view is to support the proposed conclusion. |
| **CATT** | Support. |
| **Samsung** | Support the proposed conclusion. We are fine with ZTE’s modification |
| **Nokia** | Support the conclusion |
| **Sharp** | Support |
| **Ericsson** | Support FL’s proposed conclusion. |
| **Futurewei** | We think what proposed by Apple and Huawei are better solutions. |
| **Intel** | OK |
| **FL Update #1** | Majority support is to conclude this discussion without any enhancement in Rel-17. Change in the wording a bit as ZTE commenting to use “maintained”. FL views that ‘applicable” makes more sense.  **Proposed conclusion 2.3:** Rel-15/16 collision handling between PUCCH repetition and other channels/signals are also applicable for Rel-17 M-TRP PUCCH repetition schemes. |

## 2.3 Additional high priority proposals

If you wish to bring any additional aspects related to PUCCH during RAN1 #106-bis-e, please comment below.

Based on Chairman’s guidance, please note that the proposals that do not have critical impact on completing the Rel-17 work will not be discuss further.

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| **Company** | **Comments** |
| Apple | We think whether the mapping pattern (cyclic mapping and sequential mapping) should be configured by RRC or DCI needs to be discussed. |
| vivo | Support PUCCH Scheme 2 for reliability and low latency. |
| ZTE | We suggest to support scheme 2, which is very benefit to improve reliability and reduce latency. In addition, scheme 2 is actually similar to FDM-A based MTRP PDSCH repetition in Rel-16 and which was supported in the current specs. |
| Xiaomi | Support the PUCCH scheme 2 |
| LG | Support the PUCCH scheme 2 |
| Huawei, HiSilicon | We support PUCCH scheme 2 to enhance the reliability in latency limited scenarios. |
| FGI/APT | Support the PUCCH scheme 2 to further increase reliability and reduce latency for Rel-17 MTRP PUCCH transmission. |
| Futurewei | Support PUCCH scheme 2. |
| FL Update #1 | @**Apple** >> RAN1 agreed that the mapping pattern is configured via RRC. Additional DCI support was discussed before and no support on that to consider further.  @**vivo, ZTE, Xiaomi, LG, HW, FGI** >> RAN1 discussed supporting Scheme 2 over multiple meetings including RAN1 #106-e GTW, and no consensus to support it. FL views that there is no point on further discussion. |

# Multi-TRP PUSCH transmission

The remaining open issues and company views are summarized below. The topics discussed by one/two companies or proposals not aligned with earlier RAN1 agreements are not listed to simplify the summary.

## 3.1 Summary

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| **Issue** | **Summary from Tdocs** | **Moderator comments** |
| **Issue #3.1**: PHR reporting | * For two PHR reporting, support PHR triggering event enhancement for mTRP – **IDC, ZTE, CMCC, SS (indicate to RAN2)** * When the UE is not supporting two PHR reporting, support UE to report a single actual PHR - **Apple, vivo**   Detailed proposals   * Configure the higher layer parameters of PHR trigger events as TRP specific – **ZTE, CMCC** * 'phr-PeriodicTimer', 'phr-ProhibitTimer', 'phr-Tx-PowerFactorChange' – **ZTE** * 'phr-Tx-PowerFactorChange' - **CMCC** * Path loss change exceeding phr-Tx-PowerFactorChange is calculated between path loss measured by PL-RS from the same TRP – **vivo, DCM** * A PHR is triggered if the required power backoff for any of the two pathloss references in a cell has changed more than phr-Tx-PowerFactorChange dB since the last transmission of PHR. -**MTek** * Both PHRs are reported in a single multi-TRP MAC-CE instance – **IDC, CMCC** * Support the triggering condition to report P-MPR and MPE as TRP specific, and report the first and second P-MPR together with the first and second PHR value respectively. – **CMCC** * If mpe-Reporting-FR2 is configured, P-MPR is reported per TRP and a PHR is triggered if the existing triggering conditions are satisfied by any of the two P-MPRs in a cell. -**MTek** * The first PHR and the second PHR corresponds to the ordering of the TRPs (SRS resource sets) based on the DCI codepoint – **Intel** | Overall, there are different proposals and hard to identify common view among companies.  From FL perspective, RAN1 can discuss the following,   * Clarify PHR trigger as m-TRP PUSCH may have two different PL-RS to compare the path loss change. Clarify that PHR report shall also contain per-TRP P-MPR. * Associating PHRs reported in MAC-CE corresponding to different SRS resource sets. * The UE behavior when Option 4 is not supported by the UE.   All other proposals on new RRCs, MAC-CE design aspects, and other may not be essential. As not many companies provided inputs, FL proposal can be a starting point to discuss all open points associated with PHR reporting.  **See FL Proposal 3.1.** |
| **Issue #3.2:** PTRS-DMRS association | PTRS-DMRS association for maxRank > 2   * Option 1 (4 bits): **vivo, CMCC, Xiaomi, Apple (CB), QC** * Option 2 (2 bits): **ZTE, QC** * Option 3 (2 bits): **vivo, CATT, SS, LG, E///** * Other suggestions: new MAC-CE (**Spreadtrum**), no change to legacy (**QC, LG**), fixed association for NCB (**Apple**)   Other   * For maxRank = 2, PTRS-DMRS association field should be interpreted differently according to the total number of PTRS ports and the actual number of PTRS ports that is indicated by SRI or TPMI. – **SS, Lenovo** | * The PTRS-DMRS association for maxRank > 2 was discussed over multiple meetings without any outcome. Apple provided **simulation results** showing gains of Option 1. We could go ahead with that approach to close this issue. * On the issue mentioned by SS and Lenovo, FL thinks that the spec is clear. For maxRank = 2, Tables in 38.212 (Table 7.3.1.1.2-25 and 7.3.1.1.2-26) are first decided based on the *maxNrofPorts.* For mTRP, MSB and LSB should consider the association from the decided table. If helps, we can try to clarify this further.   **See FL proposal 3.2-1 and 3.2-2** |
| **Issue #3.3:** Number of PTRS ports | Discussion on working assumption   * Confirm WA– **ZTE, Lenovo, CMCC, Nokia, E///** * Confirm as conclusion **– QC** * Capture the agreement in the spec **– E///** * Available REs or PUSCH is based on max PTRS port number **– Lenovo** | * No objections to confirm the working assumption. E/// sees that there will be spec impact. * On the Lenovo’s proposal: From FL understanding, REs for each repetition shall be calculated from the available REs which are not used for PTRS (not from total PTRS ports).   **See FL proposal 3.3** |
| **Issue #3.4:** SRS resources | Number of SRS resources   * Alt.1 – (9) **ZTE, Spreadtrum, OPPO, TCL, Xiaomi, Nokia, LG, QC(2nd), E///** * Alt.2 – (9) **HW, IDC, vivo, NEC, CATT, CMCC, MTek, DCM, QC(1st)** * Alt.3 – (4) **FW, Lenovo, CATT, Sony**   “SRI field is present or not present”   * The SRI field for each TRP depends on the SRS resource number of the SRS resource set for that TRP – **HW, FW, CATT** * If the second SRS resource set has one SRS resource, the first SRS resource set should also be configured with one SRS resource - **QC**   DCI 0\_1 and DCI 0\_2   * Number of SRS resource sets shall be configured per DCI format 0\_1 and 0\_2. – **vivo, QC** * dynamic switching (2bits) is separately determined for DCI format 0\_1 and DCI format 0\_2 - **QC** * The SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModListDCI-0-2* is composed of the first NSRS,0\_2 SRS resources in the SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModList*. – **vivo, QC**   Other   * The first/second indicated SRIs in slot n can be associated with the most recent transmission of SRS resources in the first and second SRS resource sets, respectively **– SS** * For CB based mTRP PUSCH, with regard to the uplink full power mode 2, the number of SRS ports indicated by the two SRIs should be the same, instead of the number of SRS ports configured for all SRS resources for CB should be the same. – **Apple** | * There is no majority for Alt.1 or Alt.2. Alt.3 should not be considered further. From FL perspective, details/benefits were discussed in the last meeting, and we can close many related discussions if Alt.1 is selected.   **See FL proposal 3.4-1**   * DCI 0\_1 and 0\_2 related details are considered by two companies (vivo, QC) and those seems to be valid open items.   **See FL proposal 3.4-2**   * SS proposal seems to be the understanding that companies already have on associating most recent transmission of SRS resource set to SRI indications (there can not be cross mapping of SRIs as they associated to SRS resource sets). * Apple’s suggestion is to clarify a text in an older agreement (“*The number of SRS ports between two TRPs should be same.*”). From FL perspective, the older agreement is referring to “indicated” SRS ports, but we can clarify it to avoid misunderstanding.   **See FL proposal 3.4-3** |
| **Issue #3.5**: CG PUSCH | * Introduce a new field for associating SRS resource sets in the *“rrc-ConfiguredUplinkGrant”/ “ConfiguredGrantConfig*” – **Lenovo, TCL, QC** * When the higher layer parameter *rrc-ConfiguredUplinkGrant* is not included in *ConfiguredGrantConfig*, the PL-RS resource index for two TRPs should be determined – **TCL** * Support PT-RS to DMRS port association cycling - **Apple** | * TCL, Lenovo, QC seems to be suggesting the same thing on associating SRS resource sets for s-TRP/m-TRP CG PUSCH. QC proposal is used for further discussion. * TCL and Apple have other proposals, but nothing critical to complete Rel-17 work.   **See FL proposal 3.5** |
| **Issue #3.6:** SP-CSI multiplexing | * Support a second *p0alpha* RRC parameter in “*semiPersistentOnPUSCH*” which is used for a CSI report Config when a DCI activates it on mTRP PUSCH repetitions- **QC** | * QC proposal is needed to complete SP-CSI multiplexing discussion.   **See FL proposal 3.6** |
| **Issue #3.7:** A-SRS triggering | * For NCB based mTRP PUSCH, the minimal gap between associated CSI-RS and aperiodic SRS for NCB should be 42+d symbols, where d indicates the number of overlapped symbols for the two pairs of associated CSI-RS and aperiodic SRS for NCB. – **Apple** | * Spec says the following “*A UE is not expected to update the SRS precoding information if the gap from the last symbol of the reception of the aperiodic NZP-CSI-RS resource and the first symbol of the aperiodic SRS transmission is less than 42 OFDM symbols*.”. some discussion may be needed based-on the Apple’s proposal.   **See FL proposal 3.7** |
| **Issue #3.8:** Collision between PUCCH(s) and PUSCH(s) | * When mTRP PUSCH collides with PUCCH, support that UCI can be transmitted in the PUSCH repetition corresponding to each beam. – **Apple (for Type B repetition), HW** * When A-CSI or SP-CSI is multiplexed on two mTRP PUSCH repetitions without data, and the mTRP PUCCH repetitions overlap with the PUSCH repetitions, and the overlapped PUCCH and PUSCH is targeted to the same TRP (which is ensured by gNB implementation), multiplex the UCI on both PUSCH repetitions and drop the PUCCH repetitions. – **Intel** * When SRS for CB collides with another SRS for CB, UE only transmits the SRS with lowest resource ID. - **Apple** * When SRS for NCB collides with SRS in another resource set for NCB, UE only transmits the SRS with lowest resource set ID. - **Apple** | * From FL perspective, any optimization on collision handling among PUCCH(s) and PUSCH(s) is not essential and Rel-17 does not have enough time to introduce further multiplexing modes.   No FL proposal |
| **Issue #3.9:** Other | * Discuss the cases of UE is not provided *pathlossReferenceRSs* or PUSCH repetitions are scheduled by DCI format 0\_0– **Nokia, TCL, Lenovo**   Note: Other individual proposals are not listed here to avoid long FL summary. Companies have freedom to raise them again in section 3.3 and justify the importance. | * DCI 0\_0 is not considered in the mTRP PUSCH repetitions. Also, it is hard to see the validity of the case where *pathlossReferenceRS* is not provided for mTRP PUSCH repetition schemes, and more information may be needed from the proponents in which cases *pathlossReferenceRS* is not provided when supporting mTRP PUSCH repetitions.   No FL proposal |

## 3.2 Feature lead Proposals

Issue #3.1: PHR reporting

**Proposal 3.1:** For PHR reporting in MTRP PUSCH repetition,

* A PHR is triggered if the required power backoff for any of the two pathloss references (corresponding to two SRS resource sets) in a cell has changed more than phr-Tx-PowerFactorChange dB since the last transmission of PHR.
* When per-TRP PHR reporting is supported,
* If *mpe-Reporting-FR2* is configured, P-MPR is reported per TRP and a PHR is triggered if the existing triggering conditions are satisfied by any of the two P-MPRs in a cell.
* The associated SRS resource set ID corresponding to the first PHR is indicated in the PHR MAC-CE.
* When per-TRP PHR is not supported,
* If the PHR reporting is actual PHR, the UE use the set of power control parameters for a first (earliest) repetition that overlaps with the first slot in which the PUSCH that carries the PHR MAC-CE is transmitted.
* If the PHR reporting is virtual PHR, it is reported based on legacy procedures.

Please comment on preferred changes to the proposal.

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| **Company** | **Comments** |
| **QC** | **First bullet: Not clear. Why PL of SRS resource set should be used? For PUSCH, even in Rel-15, we can have multiple PL-RS per CC.**  Mod: Please see the update suggested by vivo. The intension is to support PHR can trigger if pathloss has changed more than phr-Tx-PowerFactorChange dB on any of two TRPs.  **Second bullet: MPE related issue are being discussed in 8.1.1. The issue is not specific to PUSCH repetitions**  **Third bullet: Ok. A behavior is needed for this case, which is same as legacy with the clarification that which PUSCH repetition is considered.** |
| **InterDigital** | Support FL’s proposal in general.  To avoid triggering many PHRs, the triggering should be done by measuring the pathloss difference between PL-RS on the same TRP. The UE can determine the PL-RS per TRP through the associated SRS resource sets used for the repetitions. |
| **Apple** | First bullet: The PHR triggering condition should be a RAN2 issue. There are more than 2 candidate PL-RSs for PUSCH, where each SRI can be associated with one PL-RS, and the mapping can be updated by MAC CE. Multiple candidate PL-RSs were supported in R15, maybe we need RAN2 to decide whether there is any issue.  Second bullet: MPE is discussed under 8.1.1, now it was agreed UE can report multiple P-MPRs for different SSBRIs/CRIs. This proposal is not needed.  Third bullet: Support |
| **NTT Docomo** | First bullet: support  Second bullet: similar view with QC and Apple  Third bullet: support. in this case, legacy behavior can be reused. |
| **vivo** | Regarding the first bullet, to avoid frequent and unnecessary PHR triggering, power backoff shall be calculated TRP specifically. So we support the first bullet with minor modification.  For the second bullet, we share similar views as QC. Whether to report one or two P-MPRs may be determined by the number of panels equipped to UE. In MTRP PUSCH, there is no restriction on mandating two panels towards two TRPs, so it is unnecessary to discuss P-MPR reporting here.    Support the third bullet with minimum spec impact and clear behavior for UE to report single PHR.  Overall, we can support the Proposal 3.1 with following revisions:  **Proposal 3.1:** For PHR reporting in MTRP PUSCH repetition,   * A PHR is triggered if the required power backoff for any of the two pathloss references (corresponding to PUSCH repetitions associated with two SRS resource sets) in a cell has changed more than phr-Tx-PowerFactorChange dB since the last transmission of PHR, where phr-Tx-PowerFactorChange is configured per TRP. * ~~When per-TRP PHR reporting is supported,~~ * ~~If~~ *~~mpe-Reporting-FR2~~* ~~is configured, P-MPR is reported per TRP and a PHR is triggered if the existing triggering conditions are satisfied by any of the two P-MPRs in a cell.~~ * ~~The associated SRS resource set ID corresponding to the first PHR is indicated in the PHR MAC-CE.~~ * When per-TRP PHR is not supported, * If the PHR reporting is actual PHR, the UE use the set of power control parameters for a first (earliest) repetition that overlaps with the first slot in which the PUSCH that carries the PHR MAC-CE is transmitted. * If the PHR reporting is virtual PHR, it is reported based on legacy procedures. |
| **OPPO** | 1st bullet: Support PHR is triggered if pathloss has changed more than phr-Tx-PowerFactorChange dB on any of two TRPs  2nd bullet: Share the same view as QC/Apple  3rd bullet: Support |
| **ZTE** | For the first bullet, we can support it in principle. However, given the difference of channel propagation between two TRPs, PHR triggering condition on PL-RS power change should be TRP specific. With respect to the case when frequent PHR reporting occurs, it has nothing to do with cell-specific or TRP-specific PHR triggering. Actually, when MTRP operation, cell-specific PHR triggering condition may easily cause frequent PHR reporting than TRP-specific PHR reporting. In light of the above elaboration, we think at least the configuration of per TRP '*phr-Tx-PowerFactorChange*' should be stated in this bullet, and we can live with the modification from vivo.  For the second bullet, we share the similar view with QC and others.  For the third bullet, we are supportive of it. |
| **Xiaomi** | Support in general, for the 1st bullet, okay with Vivo’s revision. |
| **LG** | Support in general, for the 1st bullet.  For the second bullet, we share the similar view with QC and others. |
| **Huawei, HiSilicon** | We are fine with the first and the second bullets.  For the second bullet, we share similar view with QC/Apple/Vivo and others. |
| **CMCC** | First bullet: Support  Second bullet: Support. For MPE issue, it may be that the beam towards to TRP 1 has MPE issue and needs to apply power backoff, while the beam towards TRP 2 is without MPE issue. So, the triggering condition for MPE issues should be TRP-specific. We also agree that whether to report one or two P-MPRs may be determined by the number of panels equipped to UE. So, the flexibility of reporting could be left to UE. If only one of the TRP has MPE issue, the P-MPR of this TRP is reported together with the PHR value of this TRP, and the field of P-MPR for the other TRP is reserved. If both TRPs have MPE issue, the P-MPRs of these two TRPs are reported together with the first PHR value and second PHR value, respectively.  Third bullet: Support. |
| **CATT** | We prefer PHR to be triggered per TRP at least when per-TRP PHR reporting is supported.  We support the 3rd bullet. |
| **Samsung** | Support the proposal in principle (first and third bullet). |
| **Nokia** | Ok with vivo’s suggestion |
| **FGI/APT** | First bullet: We are fine with vivo’s suggestion.  Second bullet: Similar view as QC/APPle/Vivo.  Third bullet: Spport. |
| **Sharp** | We are fine with the first and the third bullet  Similar view with QC/Apple/Vivo for the second bullet |
| **Ericsson** | 1st bullet: we are fine for triggering PHR if pathloss associated to any TRP has changed more than phr-Tx-PowerFactorChange dB.  2nd bullet: share the view with other companies that it should be discussed in 8.1.1 and not here.  3nd bullet: ok to clarify. |
| **Futurewei** | First bullet: support. The PL values to the TRPs may change rather independently, and may increase or decrease. In any case, it seems useful for the UE to report to the network.  Second bullet: similar view as other companies.  Third bullet: Just a question for our understanding. If the repetition later in time has a “worse” PHR and is the one limiting the PC and resource scheduling, then it seems reporting for the first repetition is not very useful for the gNB to know anyway. Would this be considered as a problem?  Mod: In legacy behavior, first repetition is considered, and companies wish to consider a similar approach when Option 4 is not supported by the UE. Therefore, the third bullet is not an optimization for PHR reporting but extending the legacy operation. |
| **Intel** | first bullet – we are not sure specification changes is needed here (38.321 spec)  second bullet – good to consider in 8.1.1  third bullet – “not supported” may be replaced by “not configured” to allow a UE supporting per-TRP PHR to report sTRP PHR |
| **FL support #1** | Few answers provided for the questions from QC and FW.  First bullet: many companies seem to be ok with the update from vivo.  Second bullet: seems bit controversial and majority do not support any mentioning of MPE in this discussion.  Third bullet: generally Ok with all companies. As we have not agreed to new RRC on configuring “not supported” case, it is ok to keep that wording for now.  FL use the vivo’s update as no one objected to that.  **Proposal 3.1:** For PHR reporting in MTRP PUSCH repetition,   * A PHR is triggered if the required power backoff for any of the two pathloss references (corresponding to PUSCH repetitions associated with two SRS resource sets) in a cell has changed more than phr-Tx-PowerFactorChange dB since the last transmission of PHR, where phr-Tx-PowerFactorChange is configured per TRP. * ~~When per-TRP PHR reporting is supported,~~ * ~~If~~ *~~mpe-Reporting-FR2~~* ~~is configured, P-MPR is reported per TRP and a PHR is triggered if the existing triggering conditions are satisfied by any of the two P-MPRs in a cell.~~ * ~~The associated SRS resource set ID corresponding to the first PHR is indicated in the PHR MAC-CE.~~ * When per-TRP PHR is not supported, * If the PHR reporting is actual PHR, the UE use the set of power control parameters for a first (earliest) repetition that overlaps with the first slot in which the PUSCH that carries the PHR MAC-CE is transmitted. * If the PHR reporting is virtual PHR, it is reported based on legacy procedures. |

Issue #3.2: PTRS-DMRS association

**Proposal 3.2-1:** For the indication of PTRS-DMRS association for maxRank > 2 in mTRP PUSCH repetition type B, support Option 4.

* Option 1 (4 bits): with a second PTRS-DMRS association field (similar to the existing field), and each field separately indicating the association between PTRS port and DMRS port for two TRPs.

**Proposed conclusion 3.2-2:** For the indication of PTRS-DMRS association for maxRank = 2 in mTRP PUSCH repetition type B, the Table used to indicate the association between PTRS port(s) and DMRS port(s) (i.e., Table 7.3.1.1.2-25 or 7.3.1.1.2-26 in 38.212) shall be determined based on legacy procedure (i.e., Tables are associated with the *maxNrofPorts* in *PTRS-UplinkConfig*).

Please comment on preferred changes to the proposal.

|  |  |
| --- | --- |
| **Company** | **Comments** |
| **QC** | **Support Proposal 3.2-1.**  **For the conclusion, we are not sure if it is needed.** |
| **InterDigital** | Support FL’s proposal. |
| **Apple** | Support proposal 3.2-1. We also think conclusion 3.2-2 is not needed. |
| **NTT Docomo** | Support FL proposal. |
| **vivo** | Support proposal 3.2-1 with a typo correction.  **Proposal 3.2-1:** For the indication of PTRS-DMRS association for maxRank > 2 in mTRP PUSCH repetition type B, support Option ~~4~~1.   * Option 1 (4 bits): with a second PTRS-DMRS association field (similar to the existing field), and each field separately indicating the association between PTRS port and DMRS port for two TRPs.   For proposed conclusion 3.2-2, we think it is needed either. We have agreed that MSB and LSB separately indicate the association between PTRS port and DMRS port for two TRPs for maxRank=2. The following table is needed for the case in which one PT-RS port is configured by *maxNrofPorts* in *PTRS-UplinkConfig*. If two PT-RS ports are configured, the actual number of PT-RS ports is 1, the table can be reused as well. If the actual number of PT-RS ports is 2, then there is no need to indicate the association as clarified in TS 38.214. So, only one table designed following the agreement is required instead of design tables associated with the *maxNrofPorts* in *PTRS-UplinkConfig.*  PTRS-DMRS association for UL PTRS ports 0   |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Value of MSB** | **DMRS port (TRP1)** |  | **Value of LSB** | **DMRS port (TRP2)** | | 0 | 1st scheduled DMRS port |  | 0 | 1st scheduled DMRS port | | 1 | 2nd scheduled DMRS port |  | 1 | 2nd scheduled DMRS port | |
| **ZTE** | For proposal 3.2-1, we do NOT support it. We have discussed the pros and cons of option 1 to 3 in the recent four meetings, it is very intuitive and reasonable that either option 2 or option 3 is better than option 1. Although option 1 is a straightforward way to support this feature, it will double the DCI overhead with 2 additional bits, which is indeed the most sensitive issue in single DCI based MTRP PUSCH repetition scheme. Considering the tough situation in previous meetings, even though we are not the big fan of option 3, we can live with it for progress. Hence we suggest to down-select between option 1 and option 3 in this meeting, instead of rush into option 1 in the first round.  **Proposal 3.2-1:** For the indication of PTRS-DMRS association for maxRank > 2 in mTRP PUSCH repetition type B, down-select between option 1 and option 3 in RAN1#106b-e:   * Option 1 (4 bits): with a second PTRS-DMRS association field (similar to the existing field), and each field separately indicating the association between PTRS port and DMRS port for two TRPs. * Option 3 (2 bits): 1 bit MSB is used to indicate PTRS-DMRS association for the first TRP, and 1 bit LSB is used to indicate PTRS-DMRS association for the second TRP. |
| **Xiaomi** | Support the proposal |
| **LG** | Support the proposal even though this is not our preference. |
| **Huawei, HiSilicon** | We are fine with proposal 3.2-1 with the correction from Vivo. |
| **NEC** | Support the proposal. |
| **CMCC** | Support the proposal 3.2-1 with the correction from vivo. |
| **Fujitsu** | Support the proposal. |
| **CATT** | We prefer Option 3 for less DCI overhead. We prefer not to increase DCI overhead for PT-RS indication. If the majority oppose to option 3, no change to legacy(i.e., the same PTRS-DMRS association field is applied to both TRPs) is acceptable for us. |
| **Samsung** | For **Proposal 3.2-1**, we prefer Option 3 than Option 1, but if we can make agreement with Option 3, we can live with Option 1.  For **Proposed conclusion 3.2-2**, we support this proposed conclusion. As vivo’s detailed explanation, whether the association described in legacy procedure is needed or not can be determined according to the number of actual PTRS ports in maxRank=2. So, proposed conclusion 3.2-2 is needed. For the clarification, we suggest following modification:  **Proposed conclusion 3.2-2:** For the indication of PTRS-DMRS association for maxRank = 2 in mTRP PUSCH repetition type B, when the number of actual PTRS ports for a certain TRP is 1, the Table used to indicate the association between PTRS port(s) and DMRS port(s) for the TRP (i.e., Table 7.3.1.1.2-25 or 7.3.1.1.2-26 in 38.212) shall be determined based on legacy procedure (i.e., Tables are associated with the *maxNrofPorts* in *PTRS-UplinkConfig*). |
| **Nokia** | We are fine with the proposal and conclusion. |
| **Sharp** | Support the proposal |
| **Futurewei** | Support the proposal |
| **Intel** | we are not supportive of having 4 bits DCI for this purpose, same view as ZTE |
| **FL update #1** | Proposal 3.2-1: Looks ok to majority. Intel, ZTE seems to have concerns. SS, LG, CATT seems ok to accept it.    Conclusion 3.2-2: few companies think it is not needed. However, having this conclusion may make things clearer at least for some companies and there will not be any spec impact. FL views that conclusion is clear even without SS update.  **Proposal 3.2-1:** For the indication of PTRS-DMRS association for maxRank > 2 in mTRP PUSCH repetition type B, support Option 1.   * Option 1 (4 bits): with a second PTRS-DMRS association field (similar to the existing field), and each field separately indicating the association between PTRS port and DMRS port for two TRPs.     **Proposed conclusion 3.2-2:** For the indication of PTRS-DMRS association for maxRank = 2 in mTRP PUSCH repetition type B, the Table used to indicate the association between PTRS port(s) and DMRS port(s) (i.e., Table 7.3.1.1.2-25 or 7.3.1.1.2-26 in 38.212) shall be determined based on legacy procedure (i.e., Tables are associated with the *maxNrofPorts* in *PTRS-UplinkConfig*). |

Issue #3.3: Number of PTRS ports

**Proposal 3.3:**  Confirm the following working assumption (*by additionally mentioning the spec impact in the second bullet*)

For non-codebook based multi-TRP PUSCH repetition, select Alt.2.

* Alt. 2: the actual number of PT-RS ports corresponding to the 1st SRS resource set can be different from the actual number of PT-RS ports corresponding to the 2nd SRS resource set.
* This may have a spec impact (e.g. “*the actual number of PT-RS ports associated with PUSCH transmission occasions corresponding to the kth SRS resource set is determined using the SRI(s) indicated by the kth SRI field, where k = 0,1)*

Please comment on preferred changes to the proposal.

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| --- | --- |
| **Company** | **Comments** |
| **QC** | **This is the text in current spec:**  For non-codebook based UL transmission, the actual number of UL PT-RS port(s) to transmit is determined based on SRI(s) in DCI format 0\_1 and DCI format 0\_2 or higher layer parameter *sri-ResourceIndicator* in *rrc- ConfiguredUplinkGrant*.  **It seems to us that spec impact is not needed. Anyway, if there is still a chance of misunderstanding, we are ok with a conclusion.** |
| **InterDigital** | Support FL’s proposal. |
| **Apple** | Support the proposal. We agree with FL that spec impact is needed. |
| **NTT Docomo** | Support FL proposal |
| **Lenovo/MotM** | Support the proposal. However, it seems spec impact is not needed. Besides, it is not accurate that “*the kth SRS resource set is determined using the SRI(s) indicated by the kth SRI field*” when considering dynamic switching between S-TRP and M-TRP, since for S-TRP PUSCH transmission, the first SRI field is always used. |
| **vivo** | Share similar views with QC. We fail to see the spec impact. |
| **OPPO** | We can follow majority views on this issue although we still don’t think this case (different numbers of PT-RS for different SRS resource set) will happen for typical UEs. |
| **ZTE** | Support to confirm the WA, but we fail to see any spec impact because the linkage of SRI field and SRS resource set is clear enough based on the previous agreements. |
| **Xiaomi** | Confirm the WA and support the proposal |
| **LG** | We share the same view with ZTE. Confirm the WA with following revision.  **Proposal 3.3:**  Confirm the following working assumption (*by additionally mentioning the spec impact in the second bullet*)  For non-codebook based multi-TRP PUSCH repetition, select Alt.2.   * Alt. 2: the actual number of PT-RS ports corresponding to the 1st SRS resource set can be different from the actual number of PT-RS ports corresponding to the 2nd SRS resource set. * ~~This may have a spec impact (e.g. “~~*~~the actual number of PT-RS ports associated with PUSCH transmission occasions corresponding to the kth SRS resource set is determined using the SRI(s) indicated by the kth SRI field, where k = 0,1)~~* |
| **Huawei, HiSilicon** | Support. |
| **NEC** | Support |
| **CMCC** | Support. |
| **Fujitsu** | Support FL’s proposal. |
| **CATT** | Support FL’s proposal. We are not sure whether there is spec impact. |
| **Samsung** | Support and fine with LG’s revision (we cannot see any spec impact with the proposal) |
| **Nokia** | Support the FL’s proposal. |
| **Sharp** | Support |
| **Ericsson** | Support FL proposal |
| **Futurewei** | Support the revised proposal |
| **Intel** | We are supportive of the spirit but not sure whether specification impact is necessary |
| **FL update #1** | There are few companies have concerns on mentioning of a spec impact. Also, Lenovo highlighted that the mentioned spec text is not accurate. From FL view, two SRS resources sets for PUSCH is only introduced in Rel-17 and this WA is related to two SRS resource sets. Anyways, we do not have to debate on this point and can let the Editor to handle it.  **Proposal 3.3:**  Confirm the following working assumption (*with additional note*)  For non-codebook based multi-TRP PUSCH repetition, select Alt.2.   * Alt. 2: the actual number of PT-RS ports corresponding to the 1st SRS resource set can be different from the actual number of PT-RS ports corresponding to the 2nd SRS resource set. * Note: Capturing any spec impact related to this is up to the Editor. |

Issue #3.4: SRS resources

**Proposal 3.4-1:**  On the number of SRS resources configured in the two SRS resource sets, select Alt.1,

* Alt.1: Support the same number of SRS resources for both CB and NCB based m-TRP PUSCH repetition.

**Proposal 3.4-2:**  For both CB and NCB based mTRP PUSCH repetition schemes,

* The *SRS-ResourceSets* (the first and second SRS resource sets) applicable for multi-TRP PUSCH scheduled by DCI format 0\_1 and DCI format 0\_2 are defined by the entries of the higher layer parameter *srs-ResourceSetToAddModList* and *srs-ResourceSetToAddModListDCI-0-2* in SRS-config, respectively.
* The first/second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModListDCI-0-2* is composed of the first SRS resources in the first/second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModList*.
* The presence of the new field in the DCI for dynamic switching (2bits) is separately determined for DCI format 0\_1 and DCI format 0\_2 (based on whether two SRS resource sets are configured for that DCI format).

**Proposed conclusion 3.4-3:**  For CB based mTRP PUSCH repetition, the number of SRS ports **indicated** by the two SRIs should be the same.

* Note: This is to clarify an older agreement on the indication of two SRIs/TPMIs, where it mentioned that “The number of SRS ports between two TRPs should be same”.

Please comment on preferred changes to the proposal.

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| --- | --- |
| **Company** | **Comments** |
| **QC** | **3.4-1: Ok, but prefer Alt2 with the restriction that “only one SRI field is present” cannot happen.**  **3.4-2: Support.**  **3.4-3: Support.** |
| **InterDigital** | 3.4-1: Do not support FL’s proposal. We support Alt. 2 to enable more network configuration flexibility.  3.4-2: Support FL’s proposal.  3.4-5: Support FL’s proposal. |
| **Apple** | 3.4-1: We can be open for majority’s view  3.4-2: Do not support the proposal. We failed to see the necessity to have different design for DCI format 0\_1 and 0\_2. Too many SRS resource sets would lead to higher overhead.  3.4-3: Support |
| **NTT Docomo** | 3.4-1: prefer Alt.2  3.4-2: support  3.4-3: support |
| **Lenovo/MotM** | 3.4-1: Do not support FL’s proposal. We support Alt. 3 since it is most flexible. And we can accept Alt2.  3.4-2: Support FL’s proposal since it’s an extension of legacy configuration.  3.4-3: Support. |
| **vivo** | **3.4-1:** We don’t support the proposal but support Alt.2.  Configuring same number of SRS resources for both CB and NCB-based MTRP PUSCH deviates from the reality that different configurations are required due to different channel states between the UE and two TRPs, different capabilities of two UE Tx panels corresponding to two TRPs, and different UL inter-UE interference of two TRPs. For instance, for CB-based UL transmission, two UL beams may be identified for TRP1 while only one UL beam is possibly identified for TRP2 according to beam management results; in the case of NCB-based UL transmission, one SRS resource set may support a larger value of the maximum number of layers to flexibly reflect the channel state between the UE and the TRPs. In addition, if two panels supporting different number of SRS resources are equipped towards two TRPs, how the panel transmits the SRS resources exceed its capability? So, different number of SRS resources can be configured in two SRS resource sets.  **3.4-2:** We support the first and third bullet.  For the second bullet, we think there is no need to restrict that the first/second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModListDCI-0-2* is composed of the first SRS resources in the first/second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModList*. For more flexibility, the second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModListDCI-0-2* can be composed of the first SRS resources in the first or second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModList*. In addition, the number of SRS resources in two SRS resource sets configured by *srs-ResourceSetToAddModListDCI-0-2* may be different if Alt.2 in Proposal 3.4.1 is agreed So, the proposal is modified as follows:  **Proposal 3.4-2:**  For both CB and NCB based mTRP PUSCH repetition schemes,   * The *SRS-ResourceSets* (the first and second SRS resource sets) applicable for multi-TRP PUSCH scheduled by DCI format 0\_1 and DCI format 0\_2 are defined by the entries of the higher layer parameter *srs-ResourceSetToAddModList* and *srs-ResourceSetToAddModListDCI-0-2* in SRS-config, respectively. * The first~~/second~~ SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModListDCI-0-2* is composed of the first SRS resources in the ~~first/second~~one SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModList*~~.~~ , and the second SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModListDCI-0-2* is composed of the first SRS resources in the other SRS resource set configured by higher layer parameter *srs-ResourceSetToAddModList*. * The presence of the new field in the DCI for dynamic switching (2bits) is separately determined for DCI format 0\_1 and DCI format 0\_2 (based on whether two SRS resource sets are configured for that DCI format).   **3.4-3:** Support. |
| **OPPO** | 3.4-1: Support.  3.4-2: Support  3.4-3: Support |
| **ZTE** | **3.4-1:** Support.  **3.4-2:** Support.  **3.4-3:** Support. |
| **Xiaomi** | **3.4-1 support**  **3.4.2 support**  **3.4.3 support** |
| **LG** | **3.4-1 support**  **3.4.2 support**  **3.4.3 support** |
| **Huawei, HiSilicon** | **3.4-1: We support Alt 2 for a better flexibility for network configuration and believe that the restriction in Alt 1 is unnecessary.**  **3.4-2: Support.**  **3.4-3: Support.** |
| **NEC** | **3.4-1: Not support. We support Alt 2 for flexibility.**  **3.4-2: Support.**  **3.4-3: Support.** |
| **CMCC** | 3.4-1: Do not support Alt.1, support Alt.2.  For Alt.1, considering the case that the performance of 4 layers single-TRP transmission is good for TRP1, and the performance of 2 layers single-TRP transmission is good for TRP2. If 2 SRS resources are configured in both of the two SRS resource sets as Alt. 1, the maximum layers will be restricted to 2 for TRP1, which reduces the performance of single-TRP transmission for TRP 1. If 4 SRS resources are configured in both of the two SRS resource sets as Alt.1, the precoding flexibility for TRP2 could be improved with 4 SRS resources, but the SRS resources overhead and the SRI field in DCI are significantly increased.  3.4-2: Support  3.4-3: Support |
| **Fujitsu** | 3.4-1: support  3.4-2: support  3.4-3: support |
| **CATT** | 3.4-1: Not support. We have similar view as vivo, Huawei, NEC and CMCC that Alt.2 should be supported.  3.4-3: support |
| **Samsung** | 3.4-1: We prefer Alt2 because of more scheduling flexibility than Alt1.  3.4-2: Support  3.4-3: Support |
| **Nokia** | **Support Proposal 3.4-1**  **Support Proposal 3.4-2**  **Support Proposal 3.4-3** |
| **FGI/APT** | **3.4-1 support**  **3.4.2 support**  **3.4.3 support** |
| **Sharp** | 3.4-1: Support.  3.4-2: Support  3.4-3: Support |
| **Ericsson** | 3.4-1: Support.  3.4-2: Support.  3.4-3: Support. |
| **Futurewei** | 3.4-1: We prefer Alt3 and can support Alt2.  We support the other two proposals. |
| **Intel** | 3.4-1: We prefer Alt2. But OK with majority.  3.4-2: Support.  3.4-3: Support |
| **FL update #1** | 3.4-1 : **vivo, Lenovo, Huawei, NEC, CMCC, CATT** have concerns on supporting Alt.1.  3.4-2: Apple has concern but it is not fully clear. FL used to extension from Rel-15/16 for two SRS resource sets, and Vivo suggest going bit beyond that to provide further flexibility. As a majority of companies support this proposal, FL assumes Apple and vivo can live with this.  3.4-3: Offline agreement. |

Issue #3.5: CG PUSCH

**Proposal 3.5:** For a CC configured with two SRS resource sets for CB or NCB based mTRP PUSCH repetition, for Type 1 CG configuration, an additional field is added in *'rrc-ConfiguredUplinkGrant'* that indicates one of the two possibilities based on:

* If the CG is configured with only one field for each of ‘*pathlossReferenceIndex*’, *'srs-ResourceIndicator*', '*precodingAndNumberOfLayers*', *'p0-PUSCH-Alpha'* and *'powerControlLoopToUse'*
  + The additional field indicates whether PUSCH is associated with the first SRS resource set or the second SRS resource set
* If the CG is configured with first and second fields for each of ‘*pathlossReferenceIndex*’, *'srs-ResourceIndicator*', '*precodingAndNumberOfLayers*', *'p0-PUSCH-Alpha'* and *'powerControlLoopToUse'*, where the first fields are associated with the first SRS resource set and the second fields are associated with the second SRS resource set
  + The additional field indicates whether the first repetition in time is associated with the first SRS resource set or the second SRS resource set.

Please comment on preferred changes to the proposal.

|  |  |
| --- | --- |
| **Company** | **Comments** |
| **QC** | **Support** |
| **InterDigital** | Support FL’s proposal. |
| **Apple** | Support |
| **NTT Docomo** | Support |
| **Lenovo/MotM** | Support |
| **vivo** | We don’t support the proposal.  We think the additional RRC field is not needed and it can associate with the first SRS resource set by default if only one field for each of ‘*pathlossReferenceIndex*’, *'srs-ResourceIndicator*', '*precodingAndNumberOfLayers*', *'p0-PUSCH-Alpha'* and *'powerControlLoopToUse'* is configured for Type 1 CG, since we have following agreement in RAN1#106-e,  **Agreement**  When a DCI that includes the new 2-bits DCI field for dynamic switching activates a type 2 CG or schedules a retransmission of a type 1 or type 2 CG, and the CG configuration is RRC-configured with only one set of power control parameters (one ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’):   * The UE expects the new DCI field for dynamic switching is set to “00”, and all PUSCH repetitions are associated with the first SRS resource set. |
| **OPPO** | Both FL Proposal and vivo’s proposal can work. We are open to down-select one of them. |
| **ZTE** | We agree with vivo’s assessment that this proposal is not needed. |
| **Xiaomi** | We share the same view with vivo and ZTE, a default association can be applied. |
| **LG** | We share the same view with vivo, Xiaomi and ZTE. |
| **Huawei, HiSilicon** | Support. |
| **CMCC** | Share similar view with vivo that the default behavior is enough. |
| **Fujitsu** | Share similar view with vivo. |
| **Samsung** | We share the same view with vivo. |
| **Nokia** | We share similar view as vivo.. |
| **Sharp** | We agree with Vivo |
| **Ericsson** | We shared the same view with Vivo and others  On the 1st bullet, prior to RRC configuration gNB doesn’t know which TRP is better to a UE, so adding a RRC parameter to indicate which SRS resource set doesn’t help in practice. With RRC reconfiguration, gNB could configure a preferred TRP by using the new RRC parameter, but the gNB could also reconfigure the 1st and 2nd SRS resource set ID instead. Therefore, we think a new RRC parameter is not needed. The same comment applies to the 2nd bullet. Therefore, we don’t think these RRC parameters are needed. |
| **Futurewei** | Seems not absolutely necessary |
| **Intel** | We agree with vivo |
| **FL update #1** | Majority seems aligned with vivo’s comment. Therefore, the proposal is updated as below.  **Proposal 3.5:** For a CC configured with two SRS resource sets for CB or NCB based mTRP PUSCH repetition, for Type 1 CG configuration, ~~an additional field is added in~~ *~~'rrc-ConfiguredUplinkGrant'~~* ~~that indicates one of the two possibilities based on:~~   * If the CG is configured with only one field for each of ‘*pathlossReferenceIndex*’, *'srs-ResourceIndicator*', '*precodingAndNumberOfLayers*', *'p0-PUSCH-Alpha'* and *'powerControlLoopToUse',* ~~The additional field indicates whether~~ PUSCH repetitions are ~~is~~ associated with the first SRS resource set ~~or the second SRS resource set~~ * ~~If the CG is configured with first and second fields for each of ‘~~*~~pathlossReferenceIndex~~*~~’,~~ *~~'srs-ResourceIndicator~~*~~', '~~*~~precodingAndNumberOfLayers~~*~~',~~ *~~'p0-PUSCH-Alpha'~~* ~~and~~ *~~'powerControlLoopToUse'~~*~~, where the first fields are associated with the first SRS resource set and the second fields are associated with the second SRS resource set~~   ~~The additional field indicates whether the first repetition in time is associated with the first SRS resource set or the second SRS resource set.~~ |

Issue #3.6: SP-CSI multiplexing

**Proposal 3.6:** Support a second *p0alpha* RRC parameter in “*semiPersistentOnPUSCH*” which is used for a CSI report Config when a DCI activates it on mTRP PUSCH repetitions

Please comment on preferred changes to the proposal.

|  |  |
| --- | --- |
| **Company** | **Comments** |
| **QC** | **Support** |
| **InterDigital** | Support FL’s proposal. |
| **Apple** | Support |
| **NTT Docomo** | Support |
| **Lenovo/MotM** | Support. |
| **vivo** | Support |
| **OPPO** | Support |
| **ZTE** | Before we reach an agreement of this proposal, a general question may need to be answered at first.  In Rel-15/16, *p0alpha* in *semiPersistentOnPUSCH* is used for a CSI report Config when a DCI activates it on PUSCH. However, according to the current TS38.213, this RRC parameter *p0alpha* in *semiPersistentOnPUSCH* is not actually used for PUSCH transmission power anywhere. If this legacy issue is true, why should this unused RRC parameter still be introduced in Rel-17? |
| **Xiaomi** | support |
| **Huawei, HiSilicon** | Support. |
| **CMCC** | Support |
| **Fujitsu** | Support |
| **CATT** | Support |
| **Samsung** | As ZTE’s assessment, we need to check the usage of *p0alpha* before agreement. If *p0alpha* is not used as ZTE’s mention, we think this proposal is not required. |
| **Nokia** | Similar view as ZTE. |
| **Sharp** | Support |
| **Ericsson** | We share ZTE’s concern. |
| **Futurewei** | Seems needed only if p0alpha is actually used in PUSCH UL PC |
| **Intel** | Similar view as ZTE, Samsung, and Nokia. |
| **FL update #1** | Based on ZTE comment and as few others agree, a further check on actual use of *p0alpha* RRC parameter in “*semiPersistentOnPUSCH*” is needed without adding unnecessary RRC parameters. From FL’s review on TS 38.213, it seems this parameter is not used. However, proponents can further explain the details. |

Issue #3.7: A-SRS triggering

**Proposal 3.7:** For NCB based mTRP PUSCH repetition, on the minimal gap between associated CSI-RS and aperiodic SRS, select one the following,

* Alt. 1: If both SRS resource sets are triggered in an overlapped manner in time domain, the UE is not expected to update the SRS precoding information if the gap from the last symbol of the reception of the aperiodic NZP-CSI-RS resource and the first symbol of the aperiodic SRS transmission is less than 42 + d OFDM symbols, where d indicates the number of overlapped symbols for the two pairs of associated NZP-CSI-RS and aperiodic SRS for NCB.
* Alt. 2: Both SRS resource sets are not expected to be triggered in an overlapped manner in time domain.

Please comment on preferred changes to the proposal.

|  |  |
| --- | --- |
| **Company** | **Comments** |
| **QC** | **The issue is already addressed by Rel. 15. Even though we only have one SRS resource set in Rel-15, back-to-back DCIs may trigger same SRS resource set.**  **Please see FG 2-15b (Component 4): “UE can process Y SRS resources associated with CSI-RS resources simultaneously in a CC.”**  **Below is an illustration that can happen even in Rel-15:**    **Some change/modification for this FG may be needed (e.g., to increase the candidate value range), but that should be discussed as part of UE capability.** |
| **InterDigital** | Support Alt. 1. |
| **Apple** | Support Alt 1.  In our view, R15 UE FG 2-15b cannot handle the issue, since it only defines the number of SRS resources, but the key problem is the number of CSI-RS resources instead of SRS resources, since UE only calculates precoder based on CSI-RS resources. Without any relaxation, UE can only support one CSI-RS resource. Even if a new UE capability is introduced, the outcome would be the same as Alt2. |
| **vivo** | Share similar views as QC. |
| **OPPO** | We are open to discuss it. The motivation of Apple’s tdoc is clear. However, the wording of the proposal is quite confusing. For example, regarding “SRS resource sets are triggered in an overlapped manner in time domain”, there may be different understanding, e.g.,  \* Alt.1: the transmission of two SRS resources are overlapped  \* Alt.2: The timing of triggering and transmission are shown in Fig.4 of Apple’s tdoc.  Similar confusion also exists on the value of “d”.  Based on the above discussion, we prefer to discuss firstly whether any relaxation is needed for the case raised by Apple or not. If the group agree to support some relaxation, then we can further discuss how to formulate the proposal/agreement. |
| **ZTE** | Support Alt. 1. |
| **LG** | We are open to discuss this issue with potential modification on FG 2-15b |
| **Huawei, HiSilicon** | We are fine to discuss it. But the issues raised by QC and OPPO should be clarified, on i) whether the FG 2-15b in Rel-15 can cover this case already (thus the d symbols postpone is not needed), ii) the wording needs to be improved to remove any confusion. |
| **CATT** | We are open to discuss this issue with potential improvement in the proposal. |
| **Samsung** | We are open to discuss this issue. |
| **Nokia** | Support Alt.2 |
| **Sharp** | We are open to discuss this issue. |
| **Ericsson** | Prefer Alt 1 |
| **Futurewei** | Open for further discussion |
| **Intel** | We are open for further discussion |
| **FL update #1** | Majority of companies are ok with Alt.1.  However, there are few companies commenting that FG 2-15b already handle this issue. Some further clarification on that was provided by Apple. From FL reading, relevant text in 38.214 is “*A UE is not expected to update the SRS precoding information if the gap from the last symbol of the reception of the aperiodic NZP-CSI-RS resource and the first symbol of the aperiodic SRS transmission is less than 42 OFDM symbols*.” and it is related to the reception of the CSI-RS associated that is triggered with SRS, and for different SRS resource sets can have different CSI-RS association. In that sense, the issue highlighted by Apple seems not addressed by the FG 2-15b. QC and others can provide further clarification if that is not the case.  Oppo’s comment is about use of wording in the proposal and possible other ways of interpreting it. The proposal is coming from Apple and understanding should be overlapping on precoder calculation timelines. FL will clarify that if the group converge that there is real issue that needing some discussion.  **Proposal 3.7:** For NCB based mTRP PUSCH repetition, on the minimal gap between associated NZP-CSI-RS and aperiodic SRS, select,   * Alt. 1: If both SRS resource sets are triggered in an overlapped manner in time domain (overlapping refer to overlapping of minimal gaps between two pairs of associated NZP-CSI-RS and aperiodic SRS corresponding to two SRS resource sets), the UE is not expected to update the SRS precoding information if the gap from the last symbol of the reception of the aperiodic NZP-CSI-RS resource and the first symbol of the aperiodic SRS transmission is less than 42 + d OFDM symbols, where d indicates the number of overlapped symbols for the two pairs of associated NZP-CSI-RS and aperiodic SRS for NCB.   + FFS: value of d |

## 3.3 Additional high priority proposals

If you wish to bring any additional aspects related to PUSCH during RAN1 #106-bis-e, please comment below. Based on Chairman’s guidance, please note that the proposals that do not have critical impact on completing the Rel-17 work will not be discuss further.

|  |  |
| --- | --- |
| Company | Comments |
| Apple | Since we have introduced 2 sets of SRS for CB/NCB, we think the collision handling for the SRS+SRS needs to be discussed. We have the following proposal to fix this issue:   * **When SRS resource for CB collides with another SRS resource for CB, UE only transmits the SRS with lowest resource ID.** * **When SRS resource for NCB collides with SRS resource in another resource set for NCB, UE only transmits the SRS with lowest resource set ID.**   Mod: Will create new discussion in phase 1 discussion. |
| vivo | The second TPMI field can be optionally configured via RRC. PUSCH repetitions transmitting towards multiple TRPs are sharing the same TPMI indicated by one TPMI field when the second TPMI field is absent.  Mod: there was some discussion on this before. No support from others.  When DCI indicates BWP switch from a BWP with MTRP configuration to a BWP with STRP configuration, UE ignores the new field and the second SRI/TPMI/TPC fields in the DCI, while when DCI indicates BWP switch from a BWP with STRP configuration to a BWP with MTRP configuration, UE assumes the only SRI/TPMI/TPC field in the DCI are associated to the first SRS resource set in the indicated BWP.  Mod: It is unclear what is different from legacy behaviour for BWP switch. Configurations of new BWP is applied when there is a switch. |
| Huawei, HiSilicon | When PUCCH without repetition carrying HARQ-ACK and/or CSI overlaps with multi-TRP PUSCH transmission, support that the UCI of the PUCCH is multiplexed on two PUSCH repetitions with different beams.  For PUSCH repetition TypeA:    For PUSCH repetition TypeB:    Mod: there may be no further time in Rel-17 to discuss further multiplexing scenarios. We made agreements that UCI other than A-CSI (or SP-CSI) will not be present when there is multiplexing. In some sense, we discussed these possibilities before and no majority support. |

# Reference

|  |  |  |
| --- | --- | --- |
| **[R1-2108757](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2108757.zip)** | Enhancements on multi-TRP for reliability and robustness in Rel-17 | Huawei, HiSilicon |
| [**R1-2108790**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2108790.zip) | Multi-TRP/panel for non-PDSCH | FUTUREWEI |
| [**R1-2108809**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2108809.zip) | Remaining Details on Enhancements for PDCCH, PUCCH, and PUSCH | InterDigital, Inc. |
| [**R1-2108871**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2108871.zip) | Multi-TRP enhancements for PDCCH, PUCCH and PUSCH | ZTE |
| [**R1-2108896**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2108896.zip) | Discussion on enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | Spreadtrum Communications |
| [**R1-2108952**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2108952.zip) | Further discussion on Multi-TRP for PDCCH, PUCCH and PUSCH enhancements | vivo |
| [**R1-2109030**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109030.zip) | Enhancements on Multi-TRP for PDCCH PUCCH and PUSCH | Fujitsu |
| [**R1-2109039**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109039.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | OPPO |
| [**R1-2109104**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109104.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | Lenovo, Motorola Mobility |
| [**R1-2109109**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109109.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | TCL Communication Ltd. |
| [**R1-2109123**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109123.zip) | Discussion on multi-TRP for PDCCH, PUCCH and PUSCH | NEC |
| [**R1-2109185**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109185.zip) | On multi-TRP/panel PDCCH, PUCCH and PUSCH transmission | CATT |
| [**R1-2109271**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109271.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | CMCC |
| [**R1-2109351**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109351.zip) | On multi-TRP enhancements for PDCCH | Fraunhofer IIS, Fraunhofer HHI |
| [**R1-2109379**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109379.zip) | Enhancements on Multi-TRP for PDCCH, PUSCH and PUCCH | Xiaomi |
| [**R1-2109469**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109469.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | Samsung |
| [**R1-2109544**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109544.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | MediaTek Inc. |
| [**R1-2109592**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109592.zip) | Multi-TRP enhancements for PDCCH, PUCCH and PUSCH | Intel Corporation |
| [**R1-2109659**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109659.zip) | Discussion on MTRP for reliability | NTT DOCOMO, INC. |
| [**R1-2109773**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109773.zip) | Considerations on Multi-TRP for PDCCH, PUCCH, PUSCH | Sony |
| [**R1-2109824**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109824.zip) | Discussion on enhancements on multi-TRP for uplink channels | FGI, Asia Pacific Telecom |
| [**R1-2109871**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2109871.zip) | Enhancements for Multi-TRP URLLC schemes | Nokia, Nokia Shanghai Bell |
| [**R1-2110014**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2110014.zip) | Views on Rel-17 multi-TRP reliability enhancement | Apple |
| [**R1-2110078**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2110078.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | LG Electronics |
| [**R1-2110104**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2110104.zip) | Multi-TRP Enhancements for PDCCH | Convida Wireless |
| [**R1-2110166**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2110166.zip) | Enhancements on Multi-TRP for PDCCH, PUCCH and PUSCH | Qualcomm Incorporated |
| [**R1-2110286**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2110286.zip) | Discussion on mTRP PXXCH | ASUSTeK |
| [**R1-2110289**](https://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_106b-e/Docs/R1-2110289.zip) | Remaining issues on PDCCH, PUSCH and PUCCH enhancements for multi-TRP | Ericsson |

# Previous Agreements

## 5.1 PUCCH Agreements

### 102-e (August 2020)

**Agreement**

* Detailed assumptions for PUCCH evaluation:

|  |  |
| --- | --- |
| Parameters | Potential values |
| Baseline scheme | Rel-15 PUCCH repetition |
| PUCCH format | Format 1 and 3.  Other PUCCH Formats can be optionally considered. |
| # of RBs/symbols | PUCCH Format 1: 4 symbols, 1 RB  PUCCH Format 3: 4 and 8 symbols, 1 RB  Other combinations are not precluded. |
| UCI payload | 2 bits for PUCCH Format 1 (and Format 0, if considered).  Companies to report assumptions on other PUCCH Formats |
| Frequency hopping | Reported by companies |
| Number of repetitions (when applicable) | 2, 4, 8 |
| Schemes | TDM  Details to be reported by companies |
| Receiver assumption | Reported by companies |

* Detailed assumptions for PUSCH evaluation:

|  |  |
| --- | --- |
| Parameters | Potential values |
| Baseline scheme | Rel-15/-16 PUSCH repetition |
| # of RBs/symbols | Companies to Report. |
| DMRS pattern | DM-RS configuration type 1  DM-RS Configuration type 2 (optional) |
| # of layers | 1, 2 (optional) |
| Code rates | Low (<0.2) and moderate (<0.4) |
| Frequency hopping | Reported by companies |
| UL transmission scheme | Codebook based UL transmission is baseline. Non-codebook based can be optional. |
| Redundancy Version | Reported by companies |
| Number of repetitions (when applicable) | 2, 4, 8  Other numbers are not precluded |
| Schemes | TDM  Details to be reported by companies |
| Receiver assumption | Reported by companies |

**Agreement**

To improve reliability and robustness for PUCCH using multi-TRP and/or multi-panel, consider all PUCCH formats.

**Agreement**

To enable TDMed PUCCH transmission with different beams, support configuring/activating of multiple PUCCH Spatial Relation Info. RAN1 shall further study the exact schemes considering the following aspects,

* Method of configuration/activation of multiple spatial relation info
* Use of the same PUCCH resource or different PUCCH resource for PUCCH transmission
* Mapping between PUCCH repetition/symbol and spatial relation info among multiple PUCCH repetitions / multiple PUCCH symbols.

**Agreement**

For configuration/indication of the number of PUCCH repetitions, RAN1 shall further study the following,

* Alt.1: Use Rel-15 like framework
* Alt.2: Dynamic indication of the number of PUCCH repetitions

**Agreement**

For multi-TRP PUCCH transmission, further investigate required power control enhancement.

**Agreement**

Support TDMed PUCCH scheme(s) to improve reliability and robustness for PUCCH using multi-TRP and/or multi-panel. Study the following alternatives,

* Alt.1: supporting both inter-slot repetition and intra-slot repetition / intra-slot beam hopping.
* Alt.2: supporting only inter-slot repetition
* Note1: It is not precluded to study the use of multiple PUCCH resources to repeat the same UCI in both inter-slot repetition and intra-slot repetition.
* Note2: The alternatives are clarified as below,
  + inter-slot repetition: One PUCCH resource carries UCI , another one or more PUCCH resources or the same PUCCH resource in another one or more slots carries a repetition of the UCI .
  + intra-slot repetition: One PUCCH resource carries UCI , another one or more PUCCH resources or the same PUCCH resource in another one or more sub-slots carries a repetition of the UCI
  + intra-slot beam hopping: UCI is transmitted in one PUCCH resource in which different sets of symbols have different beams

### 103-e (November 2020)

**Agreement**

For multi-TRP PUCCH transmission schemes.

* Support multi-TRP inter-slot repetition (Scheme 1)
  + One PUCCH resource carries UCI, another PUCCH resource or the same PUCCH resource in another one or more slots carries a repetition of the UCI.
  + FFS: Number of repetitions
* Further study the support (one or both) of the following schemes
  + Multi-TRP intra-slot beam hopping (Scheme 2)
    - UCI is transmitted in one PUCCH resource in which different sets of symbols within the PUCCH resource have different beams.
    - FFS: More than 2 beam hopping instances per PUCCH resource.
  + Multi-TRP intra-slot repetition (Scheme 3)
    - One PUCCH resource carries UCI, another PUCCH resource or the same PUCCH resource in another one or more sub-slots within a slot carries a repetition of the UCI.
* Note1: whether to support two PUCCH resources or the same PUCCH resource with different beams for Scheme 1 and 3 to be discussed separately.

**Agreement**

For multi-TRP PUCCH transmission schemes,

* For Scheme 1, at least PUCCH format 1/3/4 can be used.
* FFS: Support of PUCCH format 0/2 for Scheme 1
* FFS: Support of PUCCH formats for Scheme 2 and/or Scheme 3 (if schemes are agreed).

**Agreement**

For multi-TRP TDM-ed PUCCH transmission schemes,

* Support the use of a single PUCCH resource
* Up to two spatial relation info’s can be activated per PUCCH resource via MAC CE
* FFS: Required enhancements for FR1
* FFS: Use of multiple PUCCH resources.

**Agreement**

For PUCCH multi-TRP enhancements in FR2,

* Support separate power control parameters for different TRP via associating power control parameters via PUCCH spatial relation info.
  + Note: No spec impact.
* For per TRP closed-loop power control for PUCCH, further study the following alternatives considering TPC command when the “closedLoopIndex” values associated with the two PUCCH spatial relation info’s are not the same.
  + Option.1: A single TPC field is used in DCI formats 1\_1 / 1\_2, and the TPC value applied for both PUCCH beams
  + Option.2: A single TPC field is used in DCI formats 1\_1 / 1\_2, and the TPC value applied for one of two PUCCH beams at a slot. The TPC value may be applied for the other PUCCH beam at an another slot.
  + Option 3: A second TPC field is added in DCI formats 1\_1 / 1\_2.
  + Option 4: A single TPC field is used in DCI formats 1\_1 / 1\_2, and indicates two TPC values applied to two PUCCH beams, respectively.
* FFS: Transition period for beam / power / frequency change.
* FFS: Required power control enhancements for FR1

**Agreement**

For configuration/indication of the number of PUCCH repetitions for Scheme 1, there is no restriction on using Rel-15 framework on configuring the number of repetitions.

* Rel-17 feMIMO may additionally consider supporting the dynamic indication of the number of repetitions in RAN1 #104 meeting.

**Agreement**

For PUCCH multi-TRP enhancements in FR1,

* Support separate power control for different TRP.
* FFS: how to define the association between PUCCH and TRP.
* FFS: required enhancements.

**Working Assumption**

For PUCCH multi-TRP enhancements in Scheme 1, it is possible to configure either cyclic mapping or sequential mapping of spatial relation info’s over PUCCH repetitions.

* FFS: Applicability of mapping patterns for different beam switching gaps
* The support of cyclic mapping can be optional UE feature for the cases when the number of repetitions is larger than 2.
* Note: For Scheme 1, cyclical mapping pattern and sequential mapping pattern are as follows,
  + Cyclical mapping pattern: the first and second beam are applied to the first and second PUCCH repetition, respectively, and the same beam mapping pattern continues to the remaining PUCCH repetitions.
  + Sequential mapping pattern: the first beam is applied to the first and second PUCCH repetitions, and the second beam is applied to the third and fourth PUCCH repetitions, and the same beam mapping pattern continues to the remaining PUCCH repetitions.

**Agreement**

LS to RAN4 on beam switching gaps for multi-TRP UL transmission is endorsed in R1-2009807.

### 104-e (February 2021)

**Agreement**

For M-TRP PUCCH scheme 1,

* Support PUCCH formats 0 and 2 (in addition to agreed PUCCH formats 1,3,4)

**Agreement**

For M-TRP PUCCH scheme 1,

* For PUCCH formats 1/3/4, values for the total number of repetitions at least contain values 2, 4, and 8.
  + FFS: maximum repetition number can be extended to 16.
* For PUCCH formats 0/2, the total number of repetitions at least contain 2.
  + FFS: other values.
* RRC configured number of slots (repetitions) are applied across both TRPs (e.g if the number of repetitions given by *nrofSlots* in *PUCCH-config* is 8, per TRP limit is 4).

**Agreement**

To support per TRP power control for multi-TRP PUCCH schemes in FR1,

* Two sets of power control parameters are used, and each set has a dedicated value of p0, pathloss RS ID and a closed-loop index.
* FFS: details on how a PUCCH resource can be linked to one or both of the two sets of power control parameters.
* FFS: whether PUCCH resource group can be linked to power control parameter sets.

**Working Assumption**

For PUCCH reliability enhancement, support multi-TRP intra-slot repetition (Scheme 3) for all PUCCH formats.

1. The same PUCCH resource carrying UCI is repeated for X = 2 [consecutive] sub-slots within a slot.
2. Refer the design details related to sub-slot configurations (e.g. other values of X) to Rel-17 eIIoT

Note1: The decision of supporting scheme 3 is only applicable for multi-TRP operation.

**Conclusion**

For Multi-TRP PUCCH Scheme 1/3 at least containing HARQ ACK, supporting dynamic switching between multi-TRP PUCCH scheme and single-TRP PUCCH transmission is not restricted, and can be done by associating,

* a PUCCH resource activated with one or two spatial-relation-info and PRI bit-field indicating a PUCCH resource,
* or a PUCCH resource with one or two power control parameter sets and PRI bit-field indicating a PUCCH resource

FFS: Support of dynamic switching for Scheme 2 (if the schemes supported)

**Conclusion**

Strive to reuse the specification support for dynamic indication of number of repetitions introduced in the Rel-17 coverage enhancement work item for multi-TRP operation. Decide whether further enhancements for multi-TRP operation are necessary in RAN1#106bis. No further discussion on this topic until RAN1#106bis under agenda item 8.1.

**Agreement**

Further study following aspects related to beam mapping and default behaviors for multi-TRP PUCCH/PUSCH schemes,

* Whether enhancements needed on beam mapping in case of PUCCH/PUSCH dropping due to invalid UL symbols
* Whether frequency hopping is performed among the repetitions with the same beam
* Whether defining default beam for PUSCH is needed when PUSCH scheduled by DCI format 0\_0 when two spatial relation info’s are configured for a PUCCH resource

**Agreement**

Further study following alternatives to support per TRP closed-loop power control for PUCCH , select  from the below options during the RAN1 #104-e-bis meeting.

* Option.1: A single TPC field (the existing TPC field) is used in DCI formats 1\_1 / 1\_2, and the TPC value applied for both PUCCH beams
* Option.2: A single TPC field (the existing TPC field) is used in DCI formats 1\_1 / 1\_2, and the TPC value applied for one of two PUCCH beams at a slot. The TPC value may be applied for the other PUCCH beam at an another slot.
* Option 3: A second TPC field (similar to the existing TPC field) is added in DCI formats 1\_1 / 1\_2.
* Option 4: A single TPC field is used in DCI formats 1\_1 / 1\_2, and indicates two TPC values applied to two PUCCH beams, respectively.

**Working assumption**

For beam mapping /power control parameter set mapping for PUCCH repetitions,

* For M-TRP PUCCH Scheme 1 in FR1, it is possible to configure either cyclic mapping or sequential mapping of power control parameter sets over PUCCH repetitions (similar to spatial relation info’s over PUCCH repetitions).
* For M-TRP PUCCH Scheme 3, reuse the same methods as Scheme 1 (by replacing slots with sub-slots) for beam mapping or power control resource set mapping to sub-slots.
* This working assumption is also subjected to the RAN4 LS R1-2009807 and confirmed based on the RAN4 reply.

### 104-bis-e (April 2021)

**Agreement**

For the case of multi-TRP, to support per-TRP power control in FR1, the linking of PUCCH resource with [one or] two power control parameter sets, the following is supported

* MAC-CE indicates RRC IE that configures power control parameter sets (p0, pathloss RS ID, and a closed-loop index).
  + The exact design of RRC IE is up to RAN2 but from RAN1 point of view, one possible example is to reuse *PUCCH-SpatialRelationInfo* except for the *referenceSignal*

Note: It is common understanding in RAN1 that one PUCCH resource can be linked to one power control parameter set.

**Conclusion**

With reference to the normative work on NR-feMIMO:

Related to the support of switching gap between UL transmissions towards two TRPs in RAN1 specifications, there is no consensus in RAN1 to specify symbol gap(s) for the following cases

* PUSCH Type A
* PUCCH scheme 1
* PUSCH Type B
* PUCCH scheme 3

The above applies for the case included in the LS from RAN4 in R1-2102297.

**Agreement**

When inter-slot frequency hopping is configured with Scheme 1, decide one from the below options in RAN1#105-e meeting,

* Option 1
  + If sequential mapping pattern is configured, frequency hopping is performed on slot level (as in Rel-15).
  + If cyclical mapping pattern is configured, frequency hopping is performed among the repetitions with the same beam.
* Option 2:
  + gNB always configures sequential mapping pattern and frequency hopping is performed on slot level. (no spec impact)
* Option 3:
  + Frequency hopping is performed on slot level as in Rel-15 (no spec impact).

**Agreement**

**Confirm the following Working Assumption**:

For PUCCH multi-TRP enhancements in Scheme 1, it is possible to configure either cyclic mapping or sequential mapping of spatial relation info’s over PUCCH repetitions.

* FFS: Applicability of mapping patterns for different beam switching gaps
* The support of cyclic mapping can be optional UE feature for the cases when the number of repetitions is larger than 2.
* Note: For Scheme 1, cyclical mapping pattern and sequential mapping pattern are as follows,
  + Cyclical mapping pattern: the first and second beam are applied to the first and second PUCCH repetition, respectively, and the same beam mapping pattern continues to the remaining PUCCH repetitions.
  + Sequential mapping pattern: the first beam is applied to the first and second PUCCH repetitions, and the second beam is applied to the third and fourth PUCCH repetitions, and the same beam mapping pattern continues to the remaining PUCCH repetitions.

**Agreement**

**Confirm the following Working Assumption** (with small correction of typo and clarification on UE capability in RED):

* For beam mapping /power control parameter set mapping for PUCCH repetitions,
  + For M-TRP PUCCH Scheme 1 in FR1, it is possible to configure either cyclic mapping or sequential mapping of power control parameter sets over PUCCH repetitions (similar to spatial relation info’s over PUCCH repetitions).
  + For M-TRP PUCCH Scheme 3, reuse the same methods as Scheme 1 (by replacing slots with sub-slots) for beam mapping or power control ~~resource~~ parameter set mapping ~~to sub-slots~~.
  + The support of cyclic mapping can be optional UE feature for the cases when the number of repetitions is larger than 2.

### 105-e (May 2021)

**Agreement**

For multi-TRP PUCCH (scheme 1 and 3) and PUSCH (Type A and B) repetition, when the number of repetitions is equal to two, the first and second transmission occasion shall be associated with two TRPs, respectively (two UL beams or Power control parameter sets), regardless of the configured mapping pattern.

* Note: For M-TRP PUSCH type B, the number of repetitions refers to ‘nominal’ repetition.

**Agreement**

Confirm the working assumption with removing brackets on [consecutive] and adding UE capability.

* For PUCCH reliability enhancement, support multi-TRP intra-slot repetition (Scheme 3) for all PUCCH formats.
  + The same PUCCH resource carrying UCI is repeated for X = 2 ~~[~~consecutive~~]~~ sub-slots within a slot.
  + Refer the design details related to sub-slot configurations (e.g. other values of X) to Rel-17 eIIoT
* Note1: The decision of supporting scheme 3 is only applicable for multi-TRP operation.
* This feature is optional.

**Conclusion**

For multi-TRP PUCCH schemes, only one ‘twoPUCCH-PC-AdjustmentStates’ parameter is configured for both TRPs, and the parameter is shared across both TRPs, which means there will be two closed loops in total (no RAN1 spec impact).

**For future meetings:**

Further study the enhancements needed on grouping of PUCCH resources for Rel-17 multi-TRP PUCCH repetition

**Agreement**

* To support per TRP closed-loop power control for PUCCH with DCI formats 1\_1 / 1\_2, a second TPC field can be configured via RRC.
* When the second field is configured by RRC, a second TPC field (similar to the existing TPC field) is added in DCI formats 1\_1 / 1\_2 (option 3).
  + Each TPC field is for each closed-loop index value respectively
    - FFS: Whether or not the mapping between the TPC field and the PUCCH transmissions is needed
* When the second field is not configured by RRC, a single TPC field (the existing TPC field) is used in DCI formats 1\_1 / 1\_2, and the TPC value applied for the closed loop index(es) for the scheduled PUCCH
* To support per TRP closed-loop power control for PUSCH with DCI formats 0\_1 / 0\_2, adopt the same solution as with M-TRP PUCCH schemes.
  + FFS: any additional considerations
* Support UE to report the capability on whether it supports the second TPC field
* Note1: Per TRP closed-loop power control is only applicable when the “closedLoopIndex” values are not the same for TRPs.

### 106-e (August 2021)

**Agreement**

For per-TRP closed-loop power control, when the indicated PUCCH transmission in DCI format 1\_0 (fallback DCI) is associated with two “*closedLoopIndex*” values for multi-TRP PUCCH transmission schemes, the single TPC field (the existing TPC field) is applied to both closed loop indices for the scheduled PUCCH.

**Agreement**

For the grouping of PUCCH resources in Rel-17 multi-TRP PUCCH repetition schemes,

* Support MAC-CE activating two spatial relation info’s (for FR2) for a group of PUCCH resources in a CC.
* Support MAC-CE activating two sets of power control parameters (for FR1) for a group of PUCCH resources in a CC.
* When the PUCCH resource is indicated with two spatial relation info’s or two sets of power control parameters (via a MAC-CE that activating two spatial relation info’s or a MAC-CE that activating two sets of power control parameters for a group of PUCCH resources, respectively), the other PUCCH resources in the group also get updated to have the same two spatial relation info’s or two sets of power control parameters.
* When the PUCCH resource is indicated with one spatial relation info or one set of power control parameters (via a MAC-CE that activating single spatial relation info or a MAC-CE that activating single set of power control parameters for a group of PUCCH resources, respectively), then the other PUCCH resources in the group also get updated to have the same spatial relation info or the same set of power control parameters.
* The signalling details are up to RAN2 to decide.
* Note: Impacts coming from coverage enhancement work item on associating PUCCH resource with repetition factor can be discussed separately

**Agreement**

For per-TRP closed-loop power control,

* When the second TPC field is configured and the indicated PUCCH transmission in DCI formats 1\_1/1\_2  (or PUSCH transmission in DCI formats 0\_1/0\_2) is associated with one “*closedLoopIndex*” value for single TRP transmission, the other TPC field associated with the other “*closedLoopIndex*” value is unused.
* Note1: Each TPC field is for each closed-loop index value respectively (i.e., 1st /2nd TPC fields correspond to “*closedLoopIndex*” value = 0 and 1, respectively).
* Note2: When the other TPC field associated with the other “*closedLoopIndex*” value is unused, the unused TPC field is not applied for any legacy procedures of calculating sum of TPC command values.

**Agreement**

For mTRP PUCCH (or PUSCH) repetitions schemes,

* When the second TPC field is configured and the indicated PUCCH transmission in DCI formats 1\_1/1\_2 (or PUSCH transmission in DCI formats 0\_1/0\_2) is associated with the same “*closedLoopIndex*” value for mutli-TRP tranmission, the other TPC field associated with the other “*closedLoopIndex*” value is unused.
* Note: When the other TPC field associated with the other “*closedLoopIndex*” value is unused, the unused TPC field is not applied for any legacy procedures of calculating sum of TPC command values.

**Agreement**

If the PUCCH resource with the lowest ID is activated with two spatial relation info, the spatial relation info with lower ID, is used as the default beam for PUSCH scheduled by DCI format 0\_0.

**Conclusion**

There is no consensus in RAN1 to support inter-slot PDCCH repetition in Rel. 17.

## 5.2 PUSCH Agreements

### 102-e (August 2020)

**Agreement**

For M-TRP PUSCH reliability enhancement, support single DCI based PUSCH transmission/repetition scheme(s).

* Further study multi-DCI based PUSCH transmission/repetition scheme(s) to identify potential gains and required enhancements.
* Note: This agreement does not reflect any prioritization of single DCI based PUSCH transmission/repetition over multi-DCI based PUSCH transmission/repetition. Ran1 can further discuss that in the next meeting.

**Agreement**

For single DCI based M-TRP PUSCH reliability enhancement, support TDMed PUSCH repetition scheme(s) based on Rel-16 PUSCH repetition Type A and Type B.

* Further study PUSCH transmission without repetition as a potential candidate M-TRP PUSCH scheme

**Agreement**

To support single DCI based M-TRP PUSCH repetition scheme(s), up to two beams are supported. RAN1 shall further study the details considering,

1. Codebook based and non-codebook based PUSCH
2. Enhancements on SRI/TPMI/power control parameters/any other

Note1: Companies are encouraged to provide additional details on how above enhancements are applied to different PUSCH repetitions (e.g. mapping between PUSCH repetitions and beams)

Note2: Studying enhancements/aspects related to TA is not precluded.

**Agreement**

Further study M-TRP CG PUSCH reliability enhancements in Rel-17.

**Agreement**

On the mapping between PUSCH repetitions and beams in single DCI based multi-TRP PUSCH repetition Type A and Type B, further study the following,

* For both PUSCH repetition Type A and B, how the beams are mapped to different PUSCH repetitions (or slots/frequency hops),
  + Alt.1: cyclical mapping pattern (the first and second beam are applied to the first and second PUSCH repetition, respectively, and the same beam mapping pattern continues to the remaining PUSCH repetitions).
  + Alt.2: sequential mapping pattern (the first beam is applied to the first and second PUSCH repetitions, and the second beam is applied to the third and fourth PUSCH repetitions, and the same beam mapping pattern continues to the remaining PUSCH repetitions).
  + Alt.3: Half-Half pattern (the first beam is applied to the first half of PUSCH repetitions, and the second beam is applied to the second half of PUSCH repetitions)
  + Alt.~~3~~4: Other variants (e.g. configurable mapping patterns)
  + Note1: For PUSCH repetition type B, the variants considering slot level beam mapping with the same mapping principals (replacing repetition with slot) in Alt.1/2/3 are also included.
  + Note2: For PUSCH repetition type A and B with frequency hopping, the variants considering frequency hop level beam mapping with the same mapping principals (replacing repetition with frequency hop) in Alt.1/2/3 can also be studied further. Final selection of such schemes also depends on the number of beams allowed per PUSCH repetition.
* For PUSCH repetition Type B, which repetition type that the beams shall consider for the mapping,
  + Alt.1: beams are mapped to the nominal repetitions
  + Alt.2: beams are mapped to the actual repetitions
  + Alt.3: beams are mapped to different slots (not in the granularity of actual/nominal repetition)
  + Alt.4: Other variants
* Consider additional requirements on switching gap(s) between two PUSCH repetitions towards different TRPs considering beam switching latency aspects.
* Note: use of the above solutions to multi-DCI based PUSCH repetition and TDMed PUSCH transmission without repetition (when there are agreed to support) is not precluded.

### 103-e (November 2020)

**Agreement**

For single DCI based M-TRP PUSCH repetition schemes, support codebook based PUSCH transmission with following enhancements.

* Support the indication of two SRIs.
  + Alt1: Bit field of SRI shall be enhanced.
  + Alt2: No changes on SRI field
* Support the indication of two TPMIs.
  + The same number of layers are applied for both TPMIs if two TPMIs are indicated
  + The number of SRS ports between two TRPs should be same.
  + FFS: Details on indicating two TPMIs (e.g, one TPMI field or two TPMI fields)
* Increase the maximum number of SRS resource sets to two
* FFS: configuration details of each SRS resource set (e.g., number of SRS resources in a resource set)

**Agreement**

For single DCI based M-TRP PUSCH repetition schemes, support non-codebook based PUSCH transmission with following considerations.

* Increase the maximum number of SRS resource sets to two, and associated CSI-RS resource can be configured per SRS resource set.
* FFS: Enhancements on SRI field in DCI to indicate the two beams for repetitions

**Agreement**

For single DCI based M-TRP PUSCH repetition Type B, at least nominal repetitions are used to map beams

* Further study details and applicability of each mapping method
* Further study the slot based beam mapping in the cases of nominal repetition across slot boundaries

**Agreement**

For PUSCH multi-TRP enhancements,

* For per TRP closed-loop power control for PUSCH, further study the following alternatives when the “closedLoopIndex” values are different.
  + Option.1: A single TPC field is used in DCI formats 0\_1 / 0\_2, and the TPC value applied for both PUSCH beams
  + Option.2: A single TPC field is used in DCI formats 0\_1 / 0\_2, and the TPC value applied for one of two PUSCH beams at a slot.
  + Option 3: A second TPC field is added in DCI formats 0\_1 / 0\_2.
  + Option 4: A single TPC field is used in DCI formats 0\_1 / 0\_2, and indicates two TPC values applied to two PUSCH beams, respectively.
* FFS: Transition period for beam / power / frequency change.

**Agreement**

Support both type 1 and type 2 CG PUSCH transmission towards MTRP. Further study the following alternatives,

* Alt.1 : single CG configuration
  + Repetitions of a TB transmitted towards MTPR on multiple PUSCH transmission occasions of single CG configuration.
  + At least for codebook-based CG PUSCH, support configuring 2 SRIs/TPMIs.
* Alt.2 : multiple CG configurations
  + Repetitions of a TB transmitted towards MTRP on more than one PUSCH transmission occasions, where one or more transmission occasions are from one CG configuration and another one or more PUSCH transmission occasions are from another CG configuration.
  + 1 SRI/TPMI is configured/indicated for each CG configuration.
* Further study required beam mapping principals, low overhead mechanisms for beam selection, and other enhancements for Alt.1 and Alt.2.

**Agreement**

For M-TRP PUSCH reliability enhancement, further discuss multi-DCI based PUSCH transmission/repetition scheme(s) considering the following aspects.

* The same TB is repeated towards multiple TRPs with different beams, where one or more PUSCH repetitions are scheduled by one DCI and another one or more PUSCH repetitions are scheduled by another DCI.
* FFS: Details related to timeline restrictions and beam mapping
* Changes on Rel-15/16 MCS, TBS determination, and UL resource allocation are not expected from this scheme.
* The scheme is considered to be supported only if there are gains over single DCI based PUSCH repetition schemes and a similar scheme is not supported by m-TRP PDCCH (e.g. Option 3).

Companies are encouraged to provide simulation results to decide the support of the scheme in next RAN1 meetings

The support of multi-DCI based PUSCH transmission/repetition scheme(s) in Rel-17 will be decided in RAN1#104-e

**Agreement**

For single DCI based PUSCH multi-TRP enhancements, support the following RV mapping for PUSCH repetition Type A,

* DCI indicates the first RV for the first PUSCH repetition, and the RV pattern (0 2 3 1) is applied separately to PUSCH repetitions of different TRPs with a possibility of configuring RV offset for the starting RV for the second TRP (The same method as PDSCH scheme 4)
* FFS: Reuse of the same method for PUSCH repetition Type B.

**Agreement**

For single DCI based M-TRP PUSCH repetition Type A and B, further study required enhancements on PTRS-DMRS association.

**Working Assumption**

For single DCI based M-TRP PUSCH repetition Type A and B, it is possible to configure either cyclic mapping or sequential mapping of UL beams.

* The support of cyclic mapping can be optional UE feature for the cases when the number of repetitions is larger than 2.
* FFS: Support of half-half mapping.
* FFS: Additional considerations on mapping patterns (including required beam switching gaps)
* Companies are encouraged to provide further simulation results to decide details.

**Agreement**

LS to RAN4 on beam switching gaps for multi-TRP UL transmission is endorsed in R1-2009807.

### 104-e (February 2021)

**Agreement**

For single DCI based M-TRP PUSCH repetition Type B, support the following RV mapping,

* DCI indicates the first RV for the first PUSCH actual repetition, and the RV pattern (0 2 3 1) is applied separately to PUSCH actual repetitions of different TRPs with a possibility of configuring RV offset for the starting RV for the first actual repetition towards second TRP (The same method as PDSCH scheme 4).

**Agreement**

Support CG PUSCH transmission towards M-TRPs using a single CG configuration.

* Use same beam mapping principals as dynamic grant PUSCH repetition scheme.
* FFS: Required changes on CG parameters (ConfiguredGrantConfig)
* The feature is UE optional

**Agreement**

For single-DCI based M-TRP PUSCH repetition schemes, up to two power control parameter sets (using *SRI-PUSCH-PowerControl*) can be applied when SRS resources from two SRS resource sets indicated in DCI format 0\_1/0\_2.

* FFS1: Details on linking SRI fields to two power control parameters,
  + Alt. 1: Add second *sri-PUSCH-MappingToAddModList*, and select two *SRI-PUSCH-PowerControl* from two *sri-PUSCH-MappingToAddModList*
  + Alt. 2: Add SRS resource set ID in *SRI-PUSCH-PowerControl*, and select *SRI-PUSCH-PowerControl* from *sri-PUSCH-MappingToAddModList* considering the SRS resource set ID
  + Alt. 3: Let RAN2 handle this
  + Alt.4: Add second *sri-PUSCH-PathlossReferenceRS-Id*/*sri-P0-PUSCH-AlphaSetId*/*sri-PUSCH-ClosedLoopIndex* in *SRI-PUSCH-PowerControl*.
* FFS2: Enhancements on open-loop power control parameter set indication
* FFS3: Consideration on *srs-PowerControlAdjustmentStates*
* FFS4: Impact of multi-TRP PUSCH repetition on PHR reporting
* FFS5: Enhancement on power control parameters per TRP when SRI(s) indication of two SRS resource sets is absent.

**Agreement**

For single DCI based M-TRP PUSCH repetition schemes, in codebook based PUSCH,

* Support two SRI fields corresponding to two SRS resource sets are included in DCI formats 0\_1/0\_2.
  + Each SRI field indicating SRI per TRP, where the SRI field based on Rel-15/16 framework
* Support dynamic switching between multi-TRP and single-TRP operation
* FFS: Support dynamic switching the order of two TRPs

**Agreement**

For single DCI based M-TRP PUSCH Type B repetition schemes,

* For maxRank = 2, the number of bits for the indication of PTRS-DMRS association is the same as Rel-15/16, MSB and LSB separately indicating the association between PTRS port and DMRS port for two TRPs.
* FFS: the indication of PTRS-DMRS association for maxRank > 2.

**Agreement**

For s-DCI based multi-TRP PUSCH repetition Type A and B, if the DCI schedules A-CSI, support multiplexing A-CSI on the first PUSCH repetition corresponding to the first beam and the X-th PUSCH repetition corresponding to the second beam.

* For PUSCH repetition Type A, X=1 (the first PUSCH repetition corresponding to the second beam)
* For PUSCH repetition Type B, the first actual PUSCH repetition corresponding to the first beam and the X-th actual repetition corresponding to the second beam are considered,
  + The UE does not expect the first actual repetition corresponding to the first beam and the X-th actual repetition corresponding to the second beam to have a single symbol duration (similar restriction as in Rel-16 NR for the single TRP case).
  + The first actual repetition corresponding to the first beam and the X-th actual repetition corresponding to the second beam are expected to have the same number of symbols
  + FFS: X = 1 or X = the first actual repetition corresponding to the second beam that contains the same number of symbols as the first actual repetition with the first beam
* FFS: Any further restrictions/enhancements needed on supporting A-CSI multiplexing on PUSCH repetitions
* FFS: whether to support multiplexing SP-CSI/P-CSI on PUSCH repetitions towards multiple TRPs.

**Agreement**

Further study following aspects related to beam mapping and default behaviors for multi-TRP PUCCH/PUSCH schemes,

* Whether enhancements needed on beam mapping in case of PUCCH/PUSCH dropping due to invalid UL symbols
* Whether frequency hopping is performed among the repetitions with the same beam
* Whether defining default beam for PUSCH is needed when PUSCH scheduled by DCI format 0\_0 when two spatial relation info’s are configured for a PUCCH resource

**Agreement**

For single DCI based M-TRP PUSCH repetition schemes, in codebook based PUSCH,

* Two TPMI fields are indicated in DCI formats 0\_1/0\_2.
  + The first TPMI field uses the Rel-15/16 TPMI field design (which includes TPMI index and the number of layers) of DCI format 0\_1/0\_2. The second TPMI field only contains~~indicates~~ the second TPMI index. The same number of layers are applied as indicated in the first TPMI field.
  + FFS: Details of second TPMI field interpretation including changes expected in Tables 7.3.1.1.2-2/2A/2B/3/3A/4/4A/5/5A in 38.212
  + FFS: Interpreting TPMI fields when multi-TRP and single-TRP PUSCH repetition is applied.
* FFS: whether to support of PUSCH repetitions transmitting towards two TRPs sharing the same TPMI indicated by a TPMI field.
* FFS: The size of the second TPMI field can be equal to or smaller than the size of the first TPMI field

**Agreement**

For single DCI based M-TRP PUSCH repetition schemes, in non-codebook based PUSCH,

* Support two SRI field(s) corresponding to two SRS resource sets are included in DCI formats 0\_1/0\_2.
  + Each SRI field indicating SRI per TRP, where the first SRI field based on Rel-15/16 framework,
  + Support the same number of layers applied over repetitions
  + FFS: details of second SRI field including the specification change for Table 7.3.1.1.2-28/29/30/31 in 38.212.
* Support dynamic switching between multi-TRP and single-TRP operation
  + FFS: whether/how to use SRI field(s) and additional details of SRI field(s) interpretations
* FFS: Minimizing the DCI overhead for PUSCH repetition Type A as a result of number of layers being limited to 1 when more than one repetition is scheduled.
* FFS: Support dynamic switching the order of two TRPs
* Companies are encouraged to provide total payload size of the two SRI fields and scheduling restriction, if any

**Agreement**

Further study following alternatives to support per TRP closed-loop power control for PUSCH , select from the below options during the RAN1 #104-e-bis meeting.

* Option.1: A single TPC field (the existing TPC field) is used in DCI formats 0\_1 / 0\_2, and the TPC value applied for both PUSCH beams
* Option.2: A single TPC field (the existing TPC field) is used in DCI formats 0\_1 / 0\_2, and the TPC value applied for one of two PUSCH beams at a slot.
* Option 3: A second TPC field (similar to the existing TPC field) is added in DCI formats 0\_1 / 0\_2.
* Option 4: A single TPC field is used in DCI formats 0\_1 / 0\_2, and indicates two TPC values applied to two PUSCH beams, respectively.

### 104-bis-e (April 2021)

**Agreement**

When SRS resources from two SRS resource sets indicated in DCI format 0\_1/0\_2, for linking SRI fields to two power control parameters, it is up to RAN2 to finalize the RRC details related to linking. RAN1 identified that the following options could be used.

* Alt. 1: Add second *sri-PUSCH-MappingToAddModList*, and select two *SRI-PUSCH-PowerControl* from two *sri-PUSCH-MappingToAddModList*
* Alt. 2: Add SRS resource set ID in *SRI-PUSCH-PowerControl*, and select *SRI-PUSCH-PowerControl* from *sri-PUSCH-MappingToAddModList* considering the SRS resource set ID

**Agreement**

For PHR reporting related to M-TRP PUSCH repetition, select one from the following options in RAN1 #105-e meeting.

* Option 1:  Calculate one PHR associated with the first PUSCH occasion (earliest repetition that overlaps with the first slot in which the PUSCH that carries the PHR MAC-CE is transmitted)
* Option 2: Calculate two PHRs, each associated with a first PUSCH occasion to each TRP, but report one of them
  + FFS: How to select the PHR for reporting.
* Option 4: Calculate two PHRs, each associated with a first PUSCH occasion to each TRP, and report two PHRs
* Option 5: No changes to legacy PHR reporting

**Agreement**

When MAC-CE indicates a PL-RS ID for one or more SRI IDs, it also indicates whether the SRI IDs are associated with the first or the second SRS resource set.

**Agreement**

For multiplexing A-CSI on two PUSCH repetitions in the case of multi-TRP PUSCH repetition,

* For S-DCI based multi-TRP PUSCH repetition Type B, support multiplexing A-CSI on the first PUSCH repetition corresponding to the first beam and the first (X = 1) PUSCH repetition corresponding to the second beam.
  + The UE is expected to follow the above operation for multiplexing A-CSI on two PUSCH repetitions only if
    - the first actual repetition corresponding to the first beam and the first actual repetition corresponding to the second beam have the same number of symbols, and
    - UCIs other than the A-CSI are not multiplexed on any of the two PUSCH repetitions.
  + When the UE does not follow the above operation, UE multiplexes A-CSI only on the first PUSCH repetition similar to Rel. 15/16.
* The content for the two A-CSI should be the same
* Note: RAN1 has the assumption on CSI timelines are followed as rel-15/16, including UE shall expect the timeline for the first A-CSI meets Z and Z’ requirement
* FFS: For s-DCI based multi-TRP PUSCH repetition Type A and B, support multiplexing of A-CSI on the first PUSCH repetition corresponding to the first beam and the first PUSCH repetition corresponding to the second beam when there is no TB carried in the PUSCH.
  + The UE assumes that the number of repetitions is 2 regardless of the indicated number of repetitions.
  + For PUSCH repetition Type B, the first and second nominal repetitions are expected to be the same as the first and second actual repetitions, respectively (no segmentation).

**Working Assumption**

For indicating STRP/MTRP dynamic switching for non-CB/CB based MTRP PUSCH repetition,

* Introduce a new field in DCI to indicate at least the S-TRP or M-TRP operation
  + FFS: Whether the new field is 1 bit or 2 bits

**Working Assumption**

For non-codebook based multi-TRP PUSCH, the first SRI field is used to determine the entry of the second SRI field which only contains the SRI(s) combinations corresponding to the indicated rank (number of layers) of the first SRI field. The number of bits, *N2*, for the second SRI field is determined by the maximum number of codepoint(s) per rank among all ranks associated with the first SRI field. For each rank x, the first *Kx* codepoint(s) are mapped to *Kx* SRIs of rank x associated with the first SRS field, the remaining (2N2-*Kx*) codepoint(s) are reserved.



**Agreement**

For the indication of open-loop power control parameter (OLPC) in DCI format 0\_1/0\_2, support enhanced open-loop power control parameter (OLPC) set indication by indicating per-TRP OLPC set.

* FFS: Details of indication.

**Agreement**

For CB based M-TRP PUSCH repetition, the first TPMI field is used to determine the entry of the second TPMI field which only contains TPMIs corresponding to the indicated rank (number of layers) of the first TPMI field. The second TPMI field’s bit width, *M2*, is determined by the maximum number of TPMIs per rank among all ranks associated with the first TPMI field. For each rank y, the first *Ky* codepoint(s) of the second TPMI field are mapped to *Ky* TPMI(s) of rank y associated with the first TPMI field in increasing order codepoint index, the remaining (2M2-*Ky*) codepoint(s) are reserved.



* How to describe/capture this in 38.212 is up to the editor.

**Agreement**

**Confirm the following working assumption** (with removing the last bullet):

For single DCI based M-TRP PUSCH repetition Type A and B, it is possible to configure either cyclic mapping or sequential mapping of UL beams.

* The support of cyclic mapping can be optional UE feature for the cases when the number of repetitions is larger than 2.
* FFS: Support of half-half mapping.
* FFS: Additional considerations on mapping patterns (including required beam switching gaps)

**Agreement**

For single DCI based M-TRP PUSCH Type B repetition, the indication of PTRS-DMRS association for maxRank > 2 is supported, down select one of the following options in RAN1 #105-e meeting,

* The support of cyclic mapping can be optional UE feature for the cases when the number of repetitions is larger than 2.
* Option 1 (4 bits): with a second PTRS-DMRS association field (similar to the existing field), and each field separately indicating the association between PTRS port and DMRS port for two TRPs.
* Option 2 (2 bits): using the existing PTRS-DMRS association field in DCI for the first TRP, and using reserved entries/bits in DM-RS port indication field for the second TRP.
* Option 3 (2 bits): 1 bit MSB is used to indicate PTRS-DMRS association for the first TRP, and 1 bit LSB is used to indicate PTRS-DMRS association for the second TRP
  + if *maxNrofPorts* = 1, the 1 bit indicates one of the first two DMRS ports.
  + if *maxNrofPorts* = 2, the 1 bit indicates one of two DMRS ports sharing the same PTRS port.

**Agreement**

For type 1 or type 2 CG based multi-TRP PUSCH repetition,

* Introduce the second fields of *'p0-PUSCH-Alpha*' and '*powerControlLoopToUse*' in '*ConfiguredGrantConfig*’
* For type 1 CG based m-TRP PUSCH repetition, introduce the second fields of ‘*pathlossReferenceIndex*’, *'srs-ResourceIndicator*' and '*precodingAndNumberOfLayers*' in *'rrc-ConfiguredUplinkGrant*'.
* For type 2 CG based M-TRP PUSCH, two SRIs/TPMIs are indicated via the activating DCI.
* FFS1: UL PT-RS port(s) and DM-RS port(s) for CG type 1
* FFS3: Details on RV mapping.
* FFS4: Possible transmission occasion for initial transmission
* FFS5: Other TRP specific parameters in '*rrc-ConfiguredUplinkGrant*', e.g., *'dmrs-SeqInitialization*'.

### 105-e (May 2021)

**Agreement**

For indicating per-TRP OLPC set in DCI format 0\_1/0\_2, if two SRI fields present in the DCI,

* Use the existing field (1 bit) for OLPC set indication and a second p0-PUSCH-SetList-r16.
  + if value of the field equals to ‘0’, the UE determine value of P0 from*SRI-PUSCH-PowerControl* with a sri-*PUSCH-PowerControlId* value mapped to the SRI field value corresponding to each TRP.
  + if value of the field equals to ‘1’, the UE determine value of P0 from a first value in P0-PUSCH-Set with a p0-PUSCH-SetId value mapped to the SRI field value corresponding to each TRP.

**Agreement**

For s-DCI based multi-TRP PUSCH repetition Type A and B, support transmitting A-CSI on the first PUSCH repetition corresponding to the first beam and the first PUSCH repetition corresponding to the second beam when there is no TB carried in the PUSCH.

* The UE assumes that the number of repetitions is 2 regardless of the indicated number of repetitions.
* The UE is expected to follow the above operation for transmitting A-CSI on two PUSCH repetitions only if
  + For PUSCH repetition Type B, the first and second nominal repetitions are expected to be the same as the first and second actual repetitions, respectively (no segmentation).
  + For PUSCH repetition Type A and B, UCIs other than the A-CSI are not multiplexed on any of the two PUSCH repetitions.
* When the UE does not follow the above operation, UE transmits A-CSI only on the first PUSCH repetition similar to Rel. 15/16.
* Note: The scheduling offset for the first A-CSI should meet the Z and Z’ requirement

**Agreement**

For s-DCI based multi-TRP PUSCH repetition Type A, the UE is expected to multiplex A-CSI on two PUSCH repetitions only if UCIs other than the A-CSI are not multiplexed on any of the two PUSCH repetitions.

* When the UE does not follow the above operation, UE multiplexes A-CSI only on the first PUSCH repetition similar to Rel. 15/16.

**Agreement**

For multi-TRP PUCCH (scheme 1 and 3) and PUSCH (Type A and B) repetition, when the number of repetitions is equal to two, the first and second transmission occasion shall be associated with two TRPs, respectively (two UL beams or Power control parameter sets), regardless of the configured mapping pattern.

* Note: For M-TRP PUSCH type B, the number of repetitions refers to ‘nominal’ repetition.

**Agreement**

The following working assumption is confirmed.

For non-codebook based multi-TRP PUSCH, the first SRI field is used to determine the entry of the second SRI field which only contains the SRI(s) combinations corresponding to the indicated rank (number of layers) of the first SRI field. The number of bits, *N2*, for the second SRI field is determined by the maximum number of codepoint(s) per rank among all ranks associated with the first SRI field. For each rank x, the first *Kx* codepoint(s) are mapped to *Kx* SRIs of rank x associated with the first SRS field, the remaining (2N2-*Kx*) codepoint(s) are reserved.

**Agreement**

For type 2 CG based multi-TRP PUSCH repetition:

* The first (legacy) RRC-configured fields ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’ are associated with the first SRS resource set.
* The second (new) RRC-configured fields ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’ are associated with the second SRS resource set.
* Applying the first, second, or both first and second RRC-configured fields ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’ is determined from the new DCI field (for dynamic switching) of the activating DCI similar to the case of DG-PUSCH.

**Agreement**

Confirm the Working Assumption (with supporting two bits for the new field).

* For indicating STRP/MTRP dynamic switching for non-CB/CB based MTRP PUSCH repetition,
  + Introduce a new field in DCI to indicate at least the S-TRP or M-TRP operation.
  + The new field is 2 bits

**Agreement**

For the new field in the DCI for dynamic switching, support Alt.1 (modified).

**Alt.1**

* Support 2 bits with the following combinations.

|  |  |  |
| --- | --- | --- |
| **Codepoint** | **SRS resource set(s)** | **SRI (for both CB and NCB)/TPMI (CB only) field(s)** |
| 00 | s-TRP mode with 1st SRS resource set (TRP1) | 1st SRI/TPMI field (2nd field is unused) |
| 01 | s-TRP mode with 2nd SRS resource set (TRP2) | 1st SRI/TPMI field (2nd field is unused) |
| 10 | m-TRP mode with (TRP1,TRP2 order)  1st SRI/TPMI field: 1st  SRS resource set  2nd SRI/TPMI field: 2nd SRS resource set | Both 1st and 2nd SRI/TPMI fields |
| 11 | m-TRP mode with (TRP2,TRP1 order)  1st SRI/TPMI field: FFS  2nd SRI/TPMI field: FFS | Both 1st and 2nd SRI/TPMI fields |

* The SRS resource set with lower ID is the first SRS resource set, and the other SRS resource set is the second SRS resource set.
  + For codebook and non-codebook usage, respectively
* ~~The same number of SRS resource shall be configured in the two SRS resource sets.~~

**Agreement**

For SP-CSI report on mTRP PUSCH repetition Type A and B activated by a DCI, further study the use of a similar mechanism to A-CSI multiplexing on M-TRP PUSCH without a TB, which includes the following,

* When SP-CSI multiplexed on m-TRP PUSCH, SP-CSI multiplexed on the two repetitions associated with the two TRPs, and the number of repetitions is always assumed to be 2, regardless of the value indicated.
* Reuse similar conditions (e.g. UCIs other than the A-CSI are not multiplexed, same number for first actual repetitions, the content of the CSI is the same) to support SP-CSI multiplexing on m-TRP PUSCH as defined in A-CSI multiplexing on M-TRP PUSCH.

**Agreement**

* To support per TRP closed-loop power control for PUCCH with DCI formats 1\_1 / 1\_2, a second TPC field can be configured via RRC.
* When the second field is configured by RRC, a second TPC field (similar to the existing TPC field) is added in DCI formats 1\_1 / 1\_2 (option 3).
  + Each TPC field is for each closed-loop index value respectively
    - FFS: Whether or not the mapping between the TPC field and the PUCCH transmissions is needed
* When the second field is not configured by RRC, a single TPC field (the existing TPC field) is used in DCI formats 1\_1 / 1\_2, and the TPC value applied for the closed loop index(es) for the scheduled PUCCH
* To support per TRP closed-loop power control for PUSCH with DCI formats 0\_1 / 0\_2, adopt the same solution as with M-TRP PUCCH schemes.
  + FFS: any additional considerations
* Support UE to report the capability on whether it supports the second TPC field
* Note1: Per TRP closed-loop power control is only applicable when the “closedLoopIndex” values are not the same for TRPs.

**Agreement**

For single-DCI based M-TRP PUSCH repetition schemes, when one SRS resource per SRS resource set is configured (i.e., when two SRI fields are absent in DCI formats 0\_1 / 0\_2), default P0, alpha, PL-RS, and closed loop index is defined per TRP. Select one from the following in RAN1 #106-e meeting,

* Alt.1
  + The first P0/alpha, PL-RS, and closed loop index are determined by *sri-PUSCH-PathlossReferenceRS-Id*, *sri-P0-PUSCH-AlphaSetId*, and *sri-PUSCH-ClosedLoopIndex* mapped to the first *sri-PUSCH-PowerControl* associated with the first SRS resource set.
  + The second P0/alpha, PL-RS, and closed loop index are determined by *sri-PUSCH-PathlossReferenceRS-Id*, *sri-P0-PUSCH-AlphaSetId*, and *sri-PUSCH-ClosedLoopIndex* mapped to the first *sri-PUSCH-PowerControl* associated with the second SRS resource set.
  + Note: How to design the signaling link *sri-PUSCH-PowerControl with*two SRS resource sets is up to RAN2.
* Alt.2
  + The first set of values {the first value in P0-AlphaSet, the PL-RS corresponded to *PUSCH-PathlossReferenceRS-Id* = 0 and closed-loop index l = 0} can be used for TRP1, and the second set of values {the second value in P0-AlphaSet, the PL-RS corresponded to *PUSCH-PathlossReferenceRS-Id* = 1 and closed-loop index l = 1 if  *twoPUSCH-PC-AdjustmentStates* is configured, *l*=0 otherwise } can be used for TRP2.
  + Note: How to design the signaling link sri-PUSCH-PowerControl with two SRS resource sets is up to RAN2.
* Alt.3
  + If the UE is provided*enablePL-RS-UpdateForPUSCH-SRS*, the first set of values {the first value in *P0-AlphaSet*, the PL-RS corresponding to the first *sri-PUSCH-PowerControl* associated with the first SRS resource set and closed-loop index *l* = 0} is used for TRP1, and the second set of values {the second value in *P0-AlphaSet*, the PL-RS corresponding to the first *sri-PUSCH-PowerControl* associated with the second SRS resource set and closed-loop index *l* = 1 if  *twoPUSCH-PC-AdjustmentStates* is configured, *l*=0 otherwise} is used for TRP2.
  + Otherwise, the first set of values {the first value in *P0-AlphaSet*, the PL-RS with *PUSCH-PathlossReferenceRS-Id=0* and closed-loop index *l* = 0} can be used for TRP1, and the second set of values {the second value in P0-AlphaSet, the PL-RS with *PUSCH-PathlossReferenceRS-Id*= 1 and closed-loop index *l* = 1 if  *twoPUSCH-PC-AdjustmentStates* is configured, *l*=0 otherwise } can be used for TRP2.
  + Note: How to design the signaling link sri-PUSCH-PowerControl with two SRS resource sets is up to RAN2.

**For further study in future meetings:**

For PHR reporting related to M-TRP PUSCH repetition, study following aspects related to option 4,

* Option 4: Calculate two PHRs (at least corresponding to the CC that applies m-TRP PUSCH repetitions), each associated with a first PUSCH occasion to each TRP, and report two PHRs.
* FFS1: How the PHRs are calculated for reporting (actual PHR or virtual PHR)
* FFS2: How the PHRs are calculated for reporting for other CCs if the multi-cell PHR MAC CE is applied.
* FFS3: Required changes to triggering conditions including the required higher layer parameters (e.g.,’phr-PeriodicTimer’, ‘phr-ProhibitTimer’, ‘phr-Tx-PowerFactorChange’ as TRP specific).
* FFS4: Report P-MPR and MPE per TRP within the same MAC-CE extension.

Note: Down-selection between Options 1-5 will be based on this study as well as the trade-off between benefit versus UE complexity.

### 106-e (August 2021)

**Agreement**

When DCI schedules a retransmission of CG-PUSCH for type 1 CG or type 2 CG (DCI with CRC scrambled with CS-RNTI and NDI=1) while the CG configuration is RRC-configured with two fields of power control parameters, apply the same procedure as DCI activation for CG type 2 agreed before, i.e.,

* The first (legacy) RRC-configured fields ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’ are associated with the first SRS resource set.
* The second (new) RRC-configured fields ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’ are associated with the second SRS resource set.
* Applying the first, second, or both first and second RRC-configured fields ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’ is determined from the new DCI field (for dynamic switching) of the activating DCI similar to the case of DG-PUSCH.

**Agreement**

When fallback DCI (DCI format 0\_0) activates a type 2 CG or schedules a retransmission of a type 1 or type 2 CG, and the CG configuration is RRC-configured with 2 sets of power control parameters (two ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’):

* The UE uses the first set of values for power control (first RRC-configured ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’).

**Agreement**

When a DCI that includes the new 2-bits DCI field for dynamic switching activates a type 2 CG or schedules a retransmission of a type 1 or type 2 CG, and the CG configuration is RRC-configured with only one set of power control parameters (one ‘*p0-PUSCH-Alpha*’ and ‘*powerControlLoopToUse*’):

* The UE expects the new DCI field for dynamic switching is set to “00”, and all PUSCH repetitions are associated with the first SRS resource set.

**Agreement**

For the new field in DCI for dynamic switching,

* For Codepoint “11”, the 1st SRI/TPMI field associate with the 1st SRS resource set while the 2nd SRI/TPMI field associate with the 2nd SRS resource set. i.e.,

|  |  |  |
| --- | --- | --- |
| **Codepoint** | **SRS resource set(s)** | **SRI (for both CB and NCB)/TPMI (CB only) field(s)** |
| 11 | m-TRP mode with (TRP2,TRP1 order)  1st SRI/TPMI field: 1st  SRS resource set  2nd SRI/TPMI field: 2nd SRS resource set | Both 1st and 2nd SRI/TPMI fields |

* For Codepoint “11”, the first repetition in time is associated with the second SRS resource set, and the remaining repetitions follow the configured mapping pattern (cyclic or sequential).
* For Codepoint “10”, the first repetition in time is associated with the first SRS resource set, and the remaining repetitions follow the configured mapping pattern (cyclic or sequential).

**Agreement**

For PHR reporting related to M-TRP PUSCH repetition, support Option 4 as UE optional capability for a UE that supports mTRP PUSCH,

* Option 4: Calculate two PHRs (at least corresponding to the CC that applies m-TRP PUSCH repetitions), each associated with a first PUSCH occasion to each TRP, and report two PHRs.

**Agreement**

For SP-CSI report on mTRP PUSCH repetition Type A and B activated by a DCI, support the use of a similar mechanism to A-CSI multiplexing on M-TRP PUSCH without a TB, which includes the following,

* When SP-CSI multiplexed on m-TRP PUSCH, SP-CSI multiplexed on the two repetitions associated with the two TRPs, and the number of repetitions is always assumed to be 2, regardless of the value indicated.
* For mTRP PUSCH repetition Type A, or for the first PUSCH after activation for PUSCH repetition Type B**,** reuse similar conditions to support SP-CSI multiplexing on m-TRP PUSCH as defined in A-CSI multiplexing on M-TRP PUSCH, i.e.,
  + The UE is expected to follow the above operation for transmitting SP-CSI on two PUSCH repetitions only if
    - For the first PUSCH after activation for PUSCH repetition Type B, the first and second nominal repetitions are expected to be the same as the first and second actual repetitions, respectively (no segmentation).
    - For PUSCH repetition Type A and B, UCIs other than the SP-CSI are not multiplexed on any of the two PUSCH repetitions.
  + When the UE does not follow the above operation, UE transmits SP-CSI only on the first PUSCH repetition similar to Rel. 15/16.
* For subsequent PUSCHs after activation (without corresponding PDCCH) for PUSCH repetition Type B, use the following criteria,
  + If the first / second nominal repetition is not the same as the first / second actual repetition, the first / second nominal repetition is dropped
    - If one of the first or second nominal repetitions is not dropped, SP-CSI is multiplexed on that repetition
  + Else (the first and second nominal repetitions are the same as the first and second actual repetitions)
    - If UCIs other than the SP-CSI are not multiplexed on any of the two PUSCH repetitions, SP-CSI is multiplexed on both repetitions.
    - Otherwise, UE transmits SP-CSI only on the first PUSCH repetition similar to Rel. 15/16 (and the second repetition is dropped)

**Agreement**

For indicating per-TRP OLPC set in DCI format 0\_1/0\_2, if no SRI field presents in the DCI,

* Use the existing field (1 or 2 bits) for OLPC set indication and the second p0-PUSCH-SetList-r16.
  + if value of the field equals to ‘0’ or ‘00’, the UE determine two values of P0 for two TRPs (one P0 value for each TRP) from the first and the second default P0 values.
    - Note: per TRP default P0 values to be decided in separate discussion (alt.1, alt.2, alt.3 in default power control parameter sets).
  + if value of the field equals to ‘1’ or ‘01’, the UE determine two values of P0 for two TRPs (one P0 value for each TRP) from the **first value** in the first *P0-PUSCH-Set-r16\_list* and the **first value** in the **second** *P0-PUSCH-Set-r16\_list*.
  + if value of the field equals to ‘10’ or ‘11’, the UE determine two values of P0 for two TRPs (one P0 value for each TRP) from the **second value** in the first *P0-PUSCH-Set-r16\_list* and the **second value** in the **second** *P0-PUSCH-Set-r16\_list.*

**Working assumption**

For non-codebook based multi-TRP PUSCH repetition, select Alt.2.

* Alt. 2: the actual number of PT-RS ports corresponding to the 1st SRS resource set can be different from the actual number of PT-RS ports corresponding to the 2nd SRS resource set.
* FFS: Whether specification change is needed due to this working assumption

**Agreement**

For RV mapping of type 1 or type 2 CG based multi-TRP PUSCH repetition, support,

* the configured RV sequence (via “*repK-RV*”) is applied separately for PUSCH repetitions corresponding to the first TRP and the second TRP with a an RV offset for the starting RV corresponding to the second TRP (similar to the case of dynamic multi-TRP PUSCH repetition).
* if *startingFromRV0* set to ‘on’, support that the initial transmission of a transport block may start at:
  + the first RV0 transmission occasion of any TRP if the configured RV sequence is {0 2 3 1},
  + any of the transmission occasions of the K repetitions that are associated with RV = 0 if the configured RV sequence is {0 3 0 3}, (same as Rel-15/16).
  + any of the transmission occasions of the K repetitions if the configured RV sequence is {0,0,0,0}, except the last transmission occasion when K≥8. (same as Rel-15/16).
* if *startingFromRV0* set to ‘off’, the initial transmission of a transport block may only start at the first transmission occasion of the K repetitions (same as Rel-15/16).

**Agreement**

For option 4, support the following:

When PHR MAC-CE is reported in slot n, for a CC that is configured with mTRP PUSCH repetition, PHR value(s) are determined as,

* The first PHR value is reported same as Rel. 15/16.
* If the first PHR value is actual PHR (based on Rel. 15/16) corresponding to a repetition among mTRP PUSCH repetitions associated with a given TRP, the second PHR value, select Alt. 1A or Alt. 2A
  + Alt.1A: Is always actual. When there are more than one repetitions associated with the other TRP, the second PHR is calculated considering on the following repetition,
    - If there are repetition(s) towards the other TRP which transmit after the repetition used to calculate first PHR, the UE select the earliest repetition among them.
    - Otherwise, the UE select the latest repetition which transmitted before the repetition used to calculate first PHR.
  + Alt.2A: Is actual only when a repetition associated with the other TRP is transmitted in slot n. Otherwise, it is virtual.
    - If there are multiple repetitions associated with the other TRP in slot n, the earliest one in slot n is selected.
* If the first PHR value is actual PHR (based on Rel. 15/16) but not corresponding to a repetition among mTRP PUSCH repetitions (corresponds to sTRP PUSCH), select Alt. 1B or Alt. 2B
  + Alt1B: a second PHR value is reported as virtual PHR.
  + Alt2B: a second PHR is not reported
* If the first PHR value is virtual, select Alt. 1C or Alt. 2C
  + Alt1C: a second PHR value is reported as virtual PHR.
  + Alt2C: a second PHR is not reported
* When second PHR is virtual, it is calculated based on a set of default power control parameters defined for the other TRP (that is not associated with the first PHR)
* Note: the above is applicable to both single entry and multi-entry PHR reports

**Agreement**

For single-DCI based M-TRP PUSCH repetition schemes, when one SRS resource per SRS resource set is configured (i.e., when two SRI fields are absent in DCI formats 0\_1 / 0\_2), per TRP default P0, alpha, PL-RS, and closed loop index is defined by,

* If the UE is provided*enablePL-RS-UpdateForPUSCH-SRS*, the first set of values {the first value in *P0-AlphaSet*, the PL-RS corresponding to the first *sri-PUSCH-PowerControl* associated with the first SRS resource set and closed-loop index *l* = 0} is used for TRP1, and the second set of values {the second value in *P0-AlphaSet*, the PL-RS corresponding to the first *sri-PUSCH-PowerControl* associated with the second SRS resource set and closed-loop index *l* = 1 if  *twoPUSCH-PC-AdjustmentStates* is configured, *l*=0 otherwise} is used for TRP2.
* Otherwise, the first set of values {the first value in *P0-AlphaSet*, the PL-RS with *PUSCH-PathlossReferenceRS-Id=0* and closed-loop index *l* = 0} can be used for TRP1, and the second set of values {the second value in P0-AlphaSet, the PL-RS with *PUSCH-PathlossReferenceRS-Id*= 1 and closed-loop index *l* = 1 if *twoPUSCH-PC-AdjustmentStates* is configured, *l*=0 otherwise } can be used for TRP2.
* Note: How to design the signaling link sri-PUSCH-PowerControl with two SRS resource sets is up to RAN2.

**Agreement**

For option 4, support the following:

* When PHR MAC-CE is reported in slot n, for a CC that is configured with mTRP PUSCH repetition, second PHR value is determined as,
  + If the first PHR value is actual PHR (based on Rel. 15/16) corresponding to a repetition among mTRP PUSCH repetitions associated with a given TRP, the second PHR value, select Alt. 2A
    - Alt.2A: Is actual only when a repetition associated with the other TRP is transmitted in slot n. Otherwise, it is virtual.
      * If there are multiple repetitions associated with the other TRP in slot n, the earliest one in slot n is selected.
  + If the first PHR value is actual PHR (based on Rel. 15/16) but not corresponding to a repetition among mTRP PUSCH repetitions (corresponds to sTRP PUSCH), select Alt. 1B
    - Alt1B: a second PHR value is reported as virtual PHR.
  + If the first PHR value is virtual, select Alt. 1C
    - Alt1C: a second PHR value is reported as virtual PHR.
* Note: It was agreed that when second PHR is virtual, it is calculated based on a set of default power control parameters defined for the other TRP (that is not associated with the first PHR)
* Note: It was agreed that the above is applicable to both single entry and multi-entry PHR reports

**Agreement**

For per-TRP closed-loop power control,

* When the second TPC field is configured and the indicated PUCCH transmission in DCI formats 1\_1/1\_2  (or PUSCH transmission in DCI formats 0\_1/0\_2) is associated with one “*closedLoopIndex*” value for single TRP transmission, the other TPC field associated with the other “*closedLoopIndex*” value is unused.
* Note1: Each TPC field is for each closed-loop index value respectively (i.e., 1st /2nd TPC fields correspond to “*closedLoopIndex*” value = 0 and 1, respectively).
* Note2: When the other TPC field associated with the other “*closedLoopIndex*” value is unused, the unused TPC field is not applied for any legacy procedures of calculating sum of TPC command values.

**Agreement**

For mTRP PUCCH (or PUSCH) repetitions schemes,

* When the second TPC field is configured and the indicated PUCCH transmission in DCI formats 1\_1/1\_2 (or PUSCH transmission in DCI formats 0\_1/0\_2) is associated with the same “*closedLoopIndex*” value for mutli-TRP tranmission, the other TPC field associated with the other “*closedLoopIndex*” value is unused.
* Note: When the other TPC field associated with the other “*closedLoopIndex*” value is unused, the unused TPC field is not applied for any legacy procedures of calculating sum of TPC command values.

**Agreement**

On the number of SRS resource configured in the two SRS resource sets, select one of the following alternatives,

* Alt.1: Support the same number of SRS resources for both CB and NCB based m-TRP PUSCH repetition.
* Alt.2: Support different number of SRS resources for both CB and NCB based m-TRP PUSCH repetition. The first SRS resource set always have the same or larger number of SRS resources than the second SRS resources set.
  + The bit width of the 1st SRI field is determined based on the first SRS resource set
  + FFS: How to interpret “SRI field is present or not present”
* Alt.3: Support different number of SRS resources for both CB and NCB based m-TRP PUSCH repetition. The first SRS resource set always have the smaller, same or larger number of SRS resources than the second SRS resources set.
  + The bit width of the 1st SRI field is determined based on maximum number of SRS resources among two resource sets
  + FFS: How to interpret “SRI field is present or not present”