**3GPP TSG RAN WG1 #101 R1-200xxxx**

**e-Meeting, May 25th – June 5th, 2020**

Source: moderator (vivo)

Title: Feature lead summary on [101-e-NR-eMIMO-ULFPTx-01]

Agenda Item: 7.2.6.4

Document for: Discussion and Decision

1. Introduction

Per guidance from Mr. Chairman, this is to kick-off following email discussion, please provide your views below.

[101-e-NR-eMIMO-ULFPTx-01] Additional entries of full power TPMI grouping indication with Mode 2 operation by 5/29 and corresponding TP (if any) by 6/5 – Rakesh (vivo)

* Issue 1 of the FL summary
* Companies are encouraged to provide simulation results
* Note that having this email thread does not automatically mean that additional entries will be included into the specification

For reference, the agreement from RAN1#99 is copied below.

**Agreement**

For 4 ports, number of bits to indicate TPMI(s) which can deliver UL full power:

* + Non Coherent 2 bits
  + Partial coherent 4 bits
    - Additional entries on top of existing entries may be added to table 1 and table 2
  + Whether is this capability reporting is optional or not will be discussed as part of UE capability discussions

Table 1.

|  |  |
| --- | --- |
| 4Tx, nonCoherent | 4Tx, partial coherent (4bit) |
| G0 | G0 |
| G1 | G1 |
| G2 | G2 |
| G3 | G3 |
|  | G4 |
|  | G5 |
|  | G6 |
|  |  |

Definition of G0~G6 can be found in the table below.

Table 2.



1. Remaining issues
   1. Issue 1: Additional entries of full power TPMI grouping indication with Mode 2 operation

There are diverging views/proposals on this issue in contribution submitted in this RAN1#101-e and following alternatives are listed:

Alt1: Whether to revise the number of bits for partial coherent case? i.e. revise the bit size to 3bits for 4Tx partial coherent

~~Alt2: Whether to revise TPMI groups G0~G6? i.e. revise the existing TPMI groups~~

Alt3: Whether to introduce more TPMI groups for 4Tx partial coherent? If yes, how many groups and TPMI group details

|  |  |
| --- | --- |
| Company | Comment |
| ZTE | For Alt 1, we do not agree to reduce the bit size to 3bits, because it means the number of supported UE PA architectures will be reduced from 16 to 8 for partial-coherent case. From the technical perspective, it make no sense to penalize this functionality by restricting the diversity of UE implementation. Instead, some more types of the partial-coherent 4-Tx UE with different PA architectures should be captured to optimize this functionality.  For Alt 2, we propose to adopt the following modifications, because there are still some obvious logical leaks of the existing G0 to G6.   * Considering the beam-forming gain from partial-coherent ports, precoding matrices , ,  and  should be added in the existing G1 and G2. * In order to optimize TPMI group based full power capability reporting, entries of TPMI groups should be decoupled as much as possible and corresponds to independent PA architectures and coherent capabilities. Based on that, G3 is a redundant entry of G5 and should be removed accordingly.   For Alt 3, we propose to add six new TPMI groups, which are G0+G4, G0+G5, G1+G5, G0+G6, G1+G6 and G2+G6, with the following comments.   * From the perspective of permutation and combination of antenna ports PA architecture, there are 76 out of 81 types of the partial-coherent 4-Tx UE should be captured. However, the size of partial-coherent 4-Tx UE is 4 bits, up to 16 TPMI groups can be adopted. * As a middle ground between signaling overhead and supported UE types, if just consider the PA combination architecture, there are only 12 out of 15 types of the partial-coherent 4-Tx UE need to be captured. Further, the existing G0 to G6 have captured 6 PA combination architectures. Therefore, it is only need to capture the remaining 6 PA combinations. * Based on the first two reasons, it is recommended that the maximum power value of PA for each port obey the following rule: port {1000} ≥ port {1002} ≥ port {1001} ≥ port {1003}. Correspondingly, G0+G4, G0+G5, G1+G5, G0+G6, G1+G6 and G2+G6 should be introduced. |
| Huawei, HiSilicon | 1. The TPMIs for , ,  and are obviously missing in the TPMIs reporting groups G1/2/3 for partial coherent UEs during the discussion stage. Since the TPMI for rank-2 [1 0 0 0; 0 0 1 0] is supported for full power transmission in G1/2/3, the same two antennas (1st and 3rd ) should be also support full power transmissions for the partial coherent UE. So, the missing 4 TPMIs are at least need to be included.   Please note that, in the coverage limited cases, for rank-1 full power transmission, the two antennas transmission with [1 0 1 0], [1 0 -1 0], [1 0 j 0] and [1 0 –j 0] is with beamforming gain compared to only with one antenna transmission, such as [1 0 0 0] and [0 1 0 0]. The simulation results are shown as follows.  Then, use Alt.2 just add the four TPMIs in the exist G1/2/3, or just add new 3 TPMI groups as Alt.3, i.e., the 4 TPMIs+G1/2/3. We are fine both of alternatives.  cid:image001.jpg@01D62FC0.6F7233F0   1. Since there is 4 bits for UE reporting agreed for 4Tx, we are fine to add more TPMI groups to reflect the true PA architectures as ZTE proposed to minimize the UE architecture restrictions for full power transmission. |
| Apple | We think this is the sequence of the questions that we need to answer  Alt3: Whether to introduce more TPMI groups for 4Tx partial coherent? If yes, how many groups and TPMI group details  We think more TPMI is needed. The current 7 TPMI groups cannot provide full flexibility in terms of UE capability reporting to address all the possible UE PA architecture and implementation decision.  Alt2: Whether to revise TPMI groups G0~G6? i.e. revise the existing TPMI groups  We slightly prefer to revise excising TPMI group  Alt1: Whether to revise the number of bits for partial coherent case? i.e. revise the bit size to 3bits for 4Tx partial coherent  Too early to answer, we need to reach a conclusion whether we will add new groups first. |
| Samsung | First, Alt2 is not within the scope this email discussion, and also beaks the previous agreement made in RAN1#99, so, it should be removed. I deleted it from above.  Now, we support Alt1 due the following reasons.   * The current spec is not broken (hence beyond the scope of normative maintenance work) * Additional TPMI groups is a minor optimization of this feature, hence is non-essential * Additional TPMI groups, if any, will bring small performance gain. * There is hardly any quantitative analysis (simulation results) provided by the proponents supporting additional TPMIs. Without any simulation analysis, it is unclear how the need for additional TPMI groups can be discussed.   Re the argument about missing PA combinations or/and missed TPMIs, we would like understand the following:   * Is there any PA combination which can’t be supported (may not be optimized) by the agreed G0-G6? * If yes, are they really important for real UE implementations? * If no, there is at least one TPMI among G0-G6 that supports a given PA combination. Then, why do we need another TPMI group for the given PA combination. The proponents need to provide simulation results in support of their proposals in order to understand the need for any additional TPMI groups. |
| NTT DOCOMO | Regarding Alt1, we do not agree on reducing the bit size from 4 bits to 3 bits at this stage. This is because, as per our understanding, the current TPMI groups cover only a limited number of PA architectures.  Regarding Alt2, one thing we need to clarify is whether there are two TPMI group tables for non-coherent and partial-coherent UEs. It seems, there are different understandings between companies about this fact. In case if there are different TPMI group tables, we are fine with updating existing entries.  Regarding Alt3, we support introducing new TPMI groups to partial-coherent UEs for allowing more PA architectures to benefit from UL full power feature. However, we think companies should first align their understanding on following points:   1. Regarding the coherent port combinations of 4Tx partial-coherent UE, it is generally understood that, port pairs {0, 2} and{1, 3} are coherent. We should respect this port coherency when designing new TPMI groups. Hence, new TPMI groups for partial-coherent UEs need to consider antenna port switching is applicable only between coherent ports 2. While complying to 1 above, the mapping rule between Tx chain and antenna port is up to the UE implementation. In other words, if a given TPMI can provide UL full Tx power for PA architecture , then that TPMI can provide UL full Tx power for PA architecture as well   These are reasonable considerations and with these, all possible PA architectures (as shown in our tdoc R1-2004397) can be covered with 8 new TPMI groups. In particular, we believe TPMI groups, #1, #2, #6, #14, #15, #16, #17 and #19 in the Annex are enough to cover all possible PA architectures |
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# References

[1] R1-2003402, “Feature lead summary on ULFPTx”, vivo, RAN1#101-e

**Annex**

|  |  |  |
| --- | --- | --- |
| **TPMI groups** | **Precoder** | **Source company** |
| 1 |  | DCM |
| 2 | , | DCM |
| 3 | , , | LG |
| 4 |  | NOK |
| 5 |  | HW, LG |
| 6 | ,,,, | QC, ZTE, DCM, CMCC |
| 7 |  | NOK |
| 8 |  | NOK |
| 9 |  | NOK |
| 10 | , ,, | LG, vivo |
| 11 | , , , ,, | LG |
| 12 | , , , ,,, | vivo |
| 13 | , , , ,,,, | vivo |
| 14 | ,, , , , , , , | DCM, ZTE |
| 15 | ,, , , , , , , ,  , , , *,* *,* *,* | DCM, ZTE, CMCC, LG |
| 16 | , , ,, , , , , , , | DCM, ZTE |
| 17 | , , ,, , , , , , , ,  , , , *,* *,* *,* | DCM, OPPO,ZTE |
| 18 | ,  ,,,,,,,  , | QC, CMCC,  LG |
| 19 | , , , ,, , , , , , , ,  , , , *,* *,* *,* | DCM, ZTE, CMCC, LG |
| 20 | ;  ; | CMCC, LG |
| 21 | ;  ; | CMCC, LG |
| 22 |  | HW, LG |
| 23 |  | HW |
| 24 | , ,,, | LG |
| 25 | in partial/noncoherent codebooksubset for 4 Tx UE | QC |