

**Agenda item:**

**Source:** Ericsson

**Title:** CR 25.215-029: Re-definition of timing measurements

**Document for:** Decision

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## Introduction

At the WG3#9 meeting, contribution R3-99I00 was approved. The contribution was also approved at RAN#6. In R3-99I00 the definition of the timing measurements “CFN-SFN observed time difference” and “SFN-SFN observed time

The basic change is that the main equation for “CFN-SFN observed time difference” is changed. The old equation was  $CFN = SFN + \text{Offset}$ , which lead to that  $\text{Offset} = CFN - SFN$  which equals the current definition of “CFN-SFN observed time difference”. The new equation is  $CFN = SFN - \text{Offset}$ , note the difference in order, e.g. the sign is changed for the Offset in the equation. Hence  $\text{Offset} = SFN - CFN$  and therefore the existing measurement “CFN-SFN observed time difference” has to be updated to reflect Offset in the new definition. Analogous to this the SFN-SFN observed time difference type 1 is also changed.

One argument for the change given in R3-99I00 are:

- The old equation makes the initial DPCH ‘advanced’ instead of ‘delayed’, e.g. an increasing offset makes the DPCH to move leftwards in the timing diagram. It could be considered more natural if channels of different kind in the standard are delayed with increasing offset value instead of ‘advanced’.

## CFN-SFN observed time difference

Below in Figure 1 the old and the new definition of CFN-SFN observed time difference is shown. The denotations below refer to the new definition of the measurement.

$T_m = (T_{\text{UETx}} - T_o) - T_{\text{RxSFN}}$ , given in chip units with the range  $[0 \dots 38400 - 1]$  chips

$T_{\text{UETx}}$  is the time when the UE transmits an uplink DPCCH/DPDCH frame.

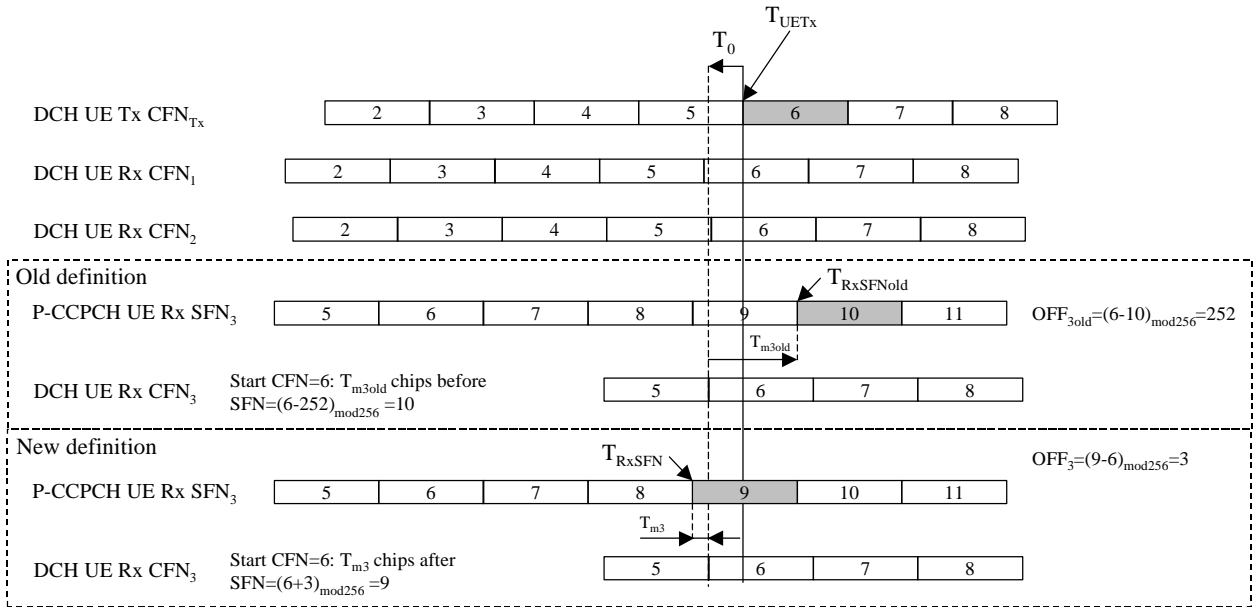
$T_o$  is a constant timing offset of 1024 chips used to set up the timing difference between the first received DPCH frame in the UE and the following uplink DPCCH/DPDCH frame.

$T_{\text{RxSFN}}$  is time at the beginning of the neighbouring P-CCPCH frame received most recent in time before the time instant  $T_{\text{UETx}} - T_o$  in the UE. If the beginning of the neighbouring P-CCPCH frame is received exactly at  $T_{\text{UETx}} - T_o$  then  $T_{\text{RxSFN}} = T_{\text{UETx}} - T_o$  which leads to that  $T_m = 0$ .

$\text{OFF} = (SFN - CFN_{\text{Tx}}) \bmod 256$ , given in number of frames with the range  $[0..255]$  frames.

$CFN_{\text{Tx}}$  is the connection frame number for the UE transmission of an uplink DPCCH/DPDCH frame at the time  $T_{\text{UETx}}$ .

$SFN$  = the system frame number for the downlink P-CCPCH frame from the target cell in the UE that is beginning at the time  $T_{\text{RxSFN}}$ .



**Figure 1 Measuring the CFN-SFN observed time difference**

A simple rule for when Node B<sub>j</sub> shall start the transmission of CFN=X to align the reception of DPDCH frames in the UE can be written as:

- Start transmission of frame X,  $T_{mij}$  chips after  $SFN_j \bmod 256 = (X + OFF_j) \bmod 256$ .

It is also proposed to change the name of the measurement to “SFN-CFN observed time difference” to reflect the change of the definition.

## SFN-SFN observed time difference

Below in Figure 2 the old and the new definition of SFN-SFN observed time difference type 1 is shown. The denotations below refer to the new definition of the measurement.

$T_m = T_{RxSFN1} - T_{RxSFN2}$ , given in chip units with the range  $[0 \dots 38400-1]$  chips

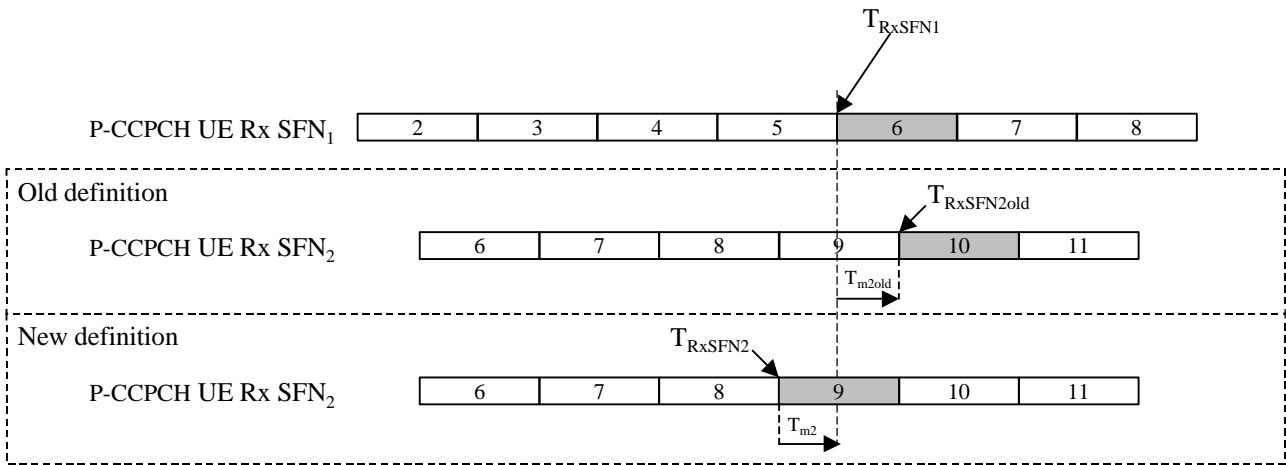
$T_{RxSFN1}$  is the time at the beginning of a received neighbouring P-CCPCH frame from cell 1.

$T_{RxSFN2}$  is time at the beginning of the neighbouring P-CCPCH frame from cell 2 received most recent in time before the time instant  $T_{RxSFN1}$  in the UE. If the beginning of the neighbouring P-CCPCH frame is received exactly at  $T_{RxSFN1}$  then  $T_{RxSFN2} = T_{RxSFN1}$  which leads to that  $T_m = 0$ .

$OFF = (SFN_2 - SFN_1) \bmod 256$ , given in number of frames with the range  $[0 \dots 255]$  frames

$SFN_1$  = the system frame number for downlink P-CCPCH frame from cell j in the UE at the time  $T_{RxSFN1}$ .

$SFN_2$  = the system frame number for the downlink P-CCPCH frame from the target cell i in the UE that is beginning at the time  $T_{RxSFN2}$ .



**Figure 2 Measuring the SFN-SFN observed time difference (type 1)**

Consider a UE in idle mode and is camping in cell  $i$  and measuring neighbouring cell  $j$ . If the UE shall set-up up a call directly in soft handover a simple rule for when Node B $_i$  and Node B $_j$  shall start their downlink transmission to align the the reception of DPDCH's in the UE can be:

- Node B $_i$  shall align the DPDCH with the P-CCPCH frame timing and shall start the transmission at  $SFN_i=X$ .
- Node B $_j$  shall start transmission of DPDCH'frames  $T_{mj}$  chips after  $SFN_j \bmod 256=(X+OFF_j) \bmod 256$ .



### 5.1.11 CSFN-SCFN observed time difference

Definition	<p>The CSFN-SCFN observed time difference to cell is defined as: <math>OFF \times 38400 + T_m</math>, where:</p> <p><math>T_m = T_{RxSFN} - (T_{UETx} - T_0) - T_{RxSFN}</math>, given in chip units with the range [0, 1, ..., 38399] chips</p> <p><math>T_{UETx}</math> is the time when the UE transmits an uplink DPCCH/DPDCH frame.</p> <p><math>T_0</math> is defined in TS 25.211 section 7.1.3.</p> <p><math>T_{RxSFN}</math> is the time at the beginning of the next received-neighbouring P-CCPCH frame received most recent in time before after the time instant <math>T_{UETx} - T_0</math> in the UE. If the next beginning of the neighbouring P-CCPCH frame is received exactly at <math>T_{UETx} - T_0</math> then <math>T_{RxSFN} = T_{UETx} - T_0</math> (which leads to <math>T_m = 0</math>).</p> <p>and</p> <p><math>OFF = (SFN - CFN_{Tx} - SFN) \bmod 256</math>, given in number of frames with the range [0, 1, ..., 255] frames</p> <p><math>CFN_{Tx}</math> is the connection frame number for the UE transmission of an uplink DPCCH/DPDCH frame at the time <math>T_{UETx}</math>.</p> <p><math>SFN</math> is the system frame number for the neighbouring P-CCPCH frame received in the UE at the time <math>T_{RxSFN}</math>.</p> <p>In case the inter-frequency measurement is done with compressed mode, the value for the parameter OFF is always reported to be 0.</p> <p>In case that the SFN measurement indicator indicates that the UE does not need to read cell SFN of the target neighbour cell, the value of the parameter OFF is always be set to 0.</p> <p><i>Note: In Compressed mode it is not required to read cell SFN of the target neighbour cell.</i></p>
Applicable for	Connected Inter, Connected Intra
Range/mapping	Time difference is given with the resolution of one chip with the range [0, ..., 9830399] chips.

### 5.1.12 SFN-SFN observed time difference

Definition	<p><b>Type 1:</b></p> <p>The SFN-SFN observed time difference to cell is defined as: <math>OFF \times 38400 + T_m</math>, where:</p> <p><math>T_m = T_{RxSFNj} - T_{RxSFNi}</math>, given in chip units with the range [0, 1, ..., 38399] chips</p> <p><math>T_{RxSFNj}</math> is the time at the beginning of a received neighbouring P-CCPCH frame from cell j.</p> <p><math>T_{RxSFNi}</math> is time at the beginning of the next received-neighbouring P-CCPCH frame from cell i received most recent in time before after the time instant <math>T_{RxSFNj}</math> in the UE. If the next neighbouring P-CCPCH frame is received exactly at <math>T_{RxSFNj}</math> then <math>T_{RxSFNj} = T_{RxSFNi}</math> (which leads to <math>T_m = 0</math>).</p> <p>and</p> <p><math>OFF = (SFN_j - SFN_i) \bmod 256</math>, given in number of frames with the range [0, 1, ..., 255] frames</p> <p><math>SFN_j</math> is the system frame number for downlink P-CCPCH frame from cell j in the UE at the time <math>T_{RxSFNj}</math>.</p> <p><math>SFN_i</math> is the system frame number for the P-CCPCH frame from cell i received in the UE at the time <math>T_{RxSFNi}</math>.</p> <p><b>Type 2:</b></p> <p>The relative timing difference between cell j and cell i, defined as <math>T_{CPICHRxj} - T_{CPICHRxi}</math>, where:</p> <p><math>T_{CPICHRxj}</math> is the time when the UE receives one Primary CPICH slot from cell j</p> <p><math>T_{CPICHRxi}</math> is the time when the UE receives the Primary CPICH slot from cell i that is closest in time to the Primary CPICH slot received from cell j</p>
Applicable for	<p><b>Type 1:</b> Idle, Connected Intra</p> <p><b>Type 2:</b> Idle, Connected Intra, Connected Inter</p>
Range/mapping	<p><b>Type 1:</b> Time difference is given with a resolution of one chip with the range [0, ..., 9830399] chips.</p> <p><b>Type 2:</b> Time difference is given with a resolution of 0.25 chip with the range [-1279.75, ..., 1280] chips.</p>