#### 3GPP TSG RAN WG1 Meeting #7bis October 4- 5 1999, Kyongju, Korea

Source:	Mitsubishi Electric
Title:	Text proposal for section 4.4 in TS25.212 on compressed mode
Agenda item:	
<b>Document for:</b>	Discussion and decision

# **1. Introduction**

We propose the following changes to the description of compressed mode in 25.212 (section 4.4):

- (1) Update of text, figures, formulas and descriptions to 15 slots per frame with slots numbered from 0...14
- (2) Change of the maximum allowed idle gap length per frame to 7 slots
- (3) Removal of references from specific TGL-values for specific purposes (e.g. power measurements, acquisition of control channels ...)
- (4) Add the possibility of double frame method for the case of TGL=7 in table 14
- (5) Add the Single frame method for the case of TGL=4 and TGL=7 with use of puncturing method A1
- (6) Update of the values of available idle lengths for measurements in table 14
- (7) Removal the last sentence below table 14: This sentence is used for explanation of notation in case of SF=1 and SF=2 case which is not needed any more
- (8) Changing the title of column from Mode to Type in Table 14: Section 4.4.1 uses type A and type B, does not use mode A or mode B.
- (9) Add a note that the use of the SF-reduction method with SF=4 needs further clarification
- (10) Table 11 and Table 12 are changed according to 16slots/frame to 15slots/frame and Table 10 in TS25.211
- (11) Table 14 is revised to clarify the case of fixed gap position or adjustable gap position.
- (12) Table 14 is revised to show the combination of idle slot per frame in double frame method
- (13) Table 14 is revised according to the change of SF range for each Type; A:4-512, B:4-256
- (14) Method A2 is proposed to remove from R'99 and to move technical document for future release
- (15) Add decision criteria whether method A1 (puncturing) can be used or not

# 2. Text proposal for 25.212(Section 4.4)

# 4.4 Coding of compressed mode

In compressed mode, slots  $N_{first}$  to  $N_{last}$  are not used for transmission of data. As illustrated in Figure 16, which shows the example of fixed <u>idle length transmission gap</u> position with single frame method-(see section 0), the instantaneous transmit power is increased in the compressed frame in order to keep the quality (BER, FER, etc.) unaffected by the reduced processing gain. The amount of power increase depends on the transmission time reduction method (see section 4.4.2). What frames are compressed, are decided by the network. When in compressed mode, compressed

frames can occur periodically, as illustrated in Figure 16, or requested on demand. The rate and type of compressed frames is variable and depends on the environment and the measurement requirements.



Figure 16: Compressed mode transmission

# 4.4.1 Frame structure types in <u>the</u> downlink

There are two different types of frame structures defined for downlink compressed transmission. Type A is the basic case, which maximises the transmission gap length. Type B, which is more optimised for power control, can be used if the requirement of the transmission gap length allows that. Slot structure for uplink compressed mode is for further study.

- With frame structure of type A, BTS transmission is off from the beginning of TFCI field in slot  $N_{first}$ , until the end of Data2 field in slot  $N_{last}$  (Figure 17(a)).
- With frame structure of type B, BTS transmission is off from the beginning of Data2 field in slot  $N_{\rm first}$ , until the end of Data2 field in slot  $N_{\rm last}$  (Figure 17(b)) Dummy bits are transmitted in the TFCI and Data1 fields of slot  $N_{\rm first}$ , and BTS and MS do not use the dummy bits. Thus BTS and MS utilize only the TPC field of  $N_{\rm first}$ .



(b) Frame structure type B



# 4.4.2 Transmission time reduction method

When in compressed mode, the information normally transmitted during a 10 ms frame is compressed in time. The mechanism provided for achieving this is either changing the code rate (method A), which means puncturing in practice, or the -reduction of the spreading factor by a factor of two (method B). The maximum idle length -is defined to be <u>7 slots <del>5 ms</del></u> per one 10 ms frame.

In case of flexible positions of Transport Channels, Method B shall be used, when

$$N_{SF}^{cm,o} \ge PL \cdot \max_{j \in TFCS} \left( \sum_{i=1}^{i=I} \frac{RM_i}{\left(\max_{1 \le i' \le I} RM_{i'}\right)} \cdot \frac{N_{i,TF_i(j)}^{TTI}}{F_i} \right)$$

is not fullfilled.

For fixed fixed positions of Transport Channels, the above criteria is still correct if there exists a TFC in which all the Transport Channels transmit simultaneously at maximum bit rate. Otherwise it shall be replaced by the following criteria:

$$N_{SF}^{cm,o} \ge PL \cdot \left( \sum_{i=1}^{i=I} \frac{RM_i}{\left(\max_{1 \le i' \le I} RM_{i'}\right)} \cdot \frac{\max_{l \in TFS(i)} N_{i,l}^{TTI}}{F_i} \right)$$

The following notation is used:

- <u>PL</u> it is a puncturing limit signalled from upper layers for the Downlink
- $N_{SF}^{cm,o}$  this is the number of available bits for the CCTrCH in a DL radio frame compressed mode format o.

<u>Compressed mode format is notably defined by the number of idle slots per frame TGPF, and by whether special slot format are used for the compressed mode. The compressed mode format is defined for for some spreading factor *SF*.</u>

The value of *SF* is the same as the one used in normal mode, and the compressed mode format is selected for that <u>SF</u> in order to get the desired TGPF.

#### 4.4.2.1 Method A1: By puncturing, basic case

During compressed mode, rate matching (puncturing) is applied for making creating short-transmission gap length in one frame. -<u>The Aalgorithm offor</u> rate matching (puncturing) <u>as</u> described in Section 4.2.7 is used. The maximum transmission gap length allowed to be achieved with this method is the case where the code rate is increased from 1/3 to 1/2 by puncturing, depending on the rate matching conditions that would be used in the non-compressed frame case. The explanation of the rate matching conditions are given below:

Example 1: If rate matching conditions in the non-compressed frame case would be such that maximum puncturing =0.2 would be used, then during compressed mode further puncturing of  $1 (2/(3*(1 \ 0.2))) =0.17$  is allowed which corresponds to  $0.17*15=2.55 \Rightarrow 2$  time slots.

Example 2: If rate matching conditions in the non-compressed frame case would be such that no puncturing would be used, then during compressed mode puncturing of 1 (2/3)=0.33 is allowed which corresponds to  $0.33*15=4.95 \Rightarrow 4$  time slots.

DPDCH and DPCCH fields for compressed mode when puncturing 4 slots and 3 slots, respectively, are shown in Table 11and Table12. Because of higher encoding rate, some DPDCH symbols remain unused and shall be indicated as DTX.

<del>Channel</del> <del>Bit Rate</del> (kbps)	<del>Channel</del> <del>Symbol</del> <del>Rate</del> ( <del>ksps)</del>	<del>SF</del>	Bits/Frame		<del>Bits/</del> <del>Slot</del>	<del>DPDCH</del> <del>Bits/Slot</del>		<del>DPCCH</del> <del>Bits/Slot</del>		<del>Extra</del> <del>DPDCH symbols for DTX</del>		
			<del>DPDCH</del>	DPCCH	TOT		N <sub>Data1</sub>	N <sub>Data2</sub>	N <sub>TFCI</sub>	N <sub>TPC</sub>	N <sub>Pilot</sub>	
<del>16</del>	8	<del>512</del>	<del>40</del>	<del>66</del>	<del>110</del>	<del>10</del>	2	2	θ	2	4	4
<del>32</del>	<del>16</del>	<del>256</del>	<del>100</del>	<del>110</del>	<del>220</del>	<del>20</del>	2	8	θ	2	8	<del>10</del>
<del>32</del>	<del>16</del>	<del>256</del>	<del>80</del>	<del>140</del>	<del>220</del>	<del>20</del>	θ	<del>8</del> 1	$2^{+}$	2	<del>8</del>	θ
<del>64</del>	<del>32</del>	<del>128</del>	<del>300</del>	<del>110</del>	44 <del>0</del>	4 <del>0</del>	6 <sup>1</sup>	<del>2</del> 4	θ	2	8	<del>30</del>
<del>64</del>	<del>32</del>	<del>128</del>	<del>280</del>	<del>140</del>	<del>440</del>	<del>40</del>	4 <sup>1</sup>	<del>24</del>	2 <sup>1</sup>	2	8	<del>20</del>
<del>128</del>	<del>64</del>	<del>6</del> 4	<del>600</del>	<del>252</del>	<del>880</del>	<del>80</del>	4 <sup>1</sup>	<del>56</del>	8 <sup>1,2</sup>	4	8	<del>28</del>
<del>256</del>	<del>128</del>	<del>32</del>	1400	<del>252</del>	<del>1760</del>	<del>160</del>	$20^{4}$	<del>120</del>	8 <sup>1,2</sup>	4	8	<del>108</del>
<del>512</del>	<del>256</del>	<del>16</del>	<del>2880</del>	<del>384</del>	<del>3520</del>	<del>320</del>	$48^{1}$	<del>240</del>	8 <sup>1,2</sup>	8	<del>16</del>	<del>256</del>
<del>1024</del>	<del>512</del>	8	<del>6080</del>	<del>384</del>	<del>7040</del>	<del>640</del>	$112^{11}$	4 <del>96</del>	8 <sup>1,2</sup>	8	<del>16</del>	<del>576</del>
<del>2048</del>	<del>1024</del>	4	<del>12480</del>	<del>384</del>	14080	<del>1280</del>	$240^{1}$	<del>1008</del>	8 <sup>1,2</sup>	8	<del>16</del>	<del>1216</del>

### Table 11: DPDCH and DPCCH fields in compressed mode when puncturing 4 slots

Channel Bit Rate	<u>Channel</u> <u>Symbol</u> Rate	<u>SF</u>	<u>Bits/Fram</u>	<u>e</u>		<u>Bits/</u> <u>Slot</u>	<u>DPDCH</u> <u>Bits/Slot</u>		DPCCH Bits/Slot			<u>Extra</u> DPDCH symbols
<u>(KUPS)</u>	<u>(ksps)</u>		DPDCH	DPCCH	TOT		N <sub>Data1</sub>	N <sub>Data2</sub>	N <sub>tfci</sub>	N <sub>TPC</sub>	<u>N<sub>Pilot</sub></u>	<u>symbols</u> for DTX
<u>15</u>	<u>7.5</u>	<u>512</u>	<u>40</u>	<u>66</u>	<u>110</u>	<u>10</u>	<u>2</u>	<u>2</u>	<u>0</u>	<u>2</u>	<u>4</u>	<u>4</u>
<u>30</u>	<u>15</u>	<u>256</u>	160	<u>44</u>	<u>220</u>	<u>20</u>	<u>2</u>	<u>14</u>	<u>0</u>	2	<u>2</u>	<u>16</u>
<u>30</u>	<u>15</u>	<u>256</u>	<u>140</u>	<u>74</u>	<u>220</u>	<u>20</u>	<u>0</u>	$14^{1}$	$2^{1}$	<u>2</u>	<u>2</u>	<u>6</u>
<u>30</u>	<u>15</u>	<u>256</u>	140	<u>66</u>	<u>220</u>	<u>20</u>	<u>2</u>	<u>12</u>	<u>0</u>	2	<u>4</u>	<u>14</u>
<u>30</u>	<u>15</u>	<u>256</u>	<u>120</u>	<u>96</u>	<u>220</u>	<u>20</u>	<u>0</u>	$12^{1}$	$2^{1}$	2	<u>4</u>	<u>4</u>
<u>30</u>	<u>15</u>	<u>256</u>	<u>100</u>	<u>110</u>	<u>220</u>	<u>20</u>	2	<u>8</u>	<u>0</u>	2	<u>8</u>	<u>10</u>
<u>30</u>	<u>15</u>	<u>256</u>	<u>80</u>	<u>140</u>	<u>220</u>	<u>20</u>	<u>0</u>	$\underline{8^1}$	$2^{1}$	2	<u>8</u>	<u>0</u>
<u>60</u>	<u>30</u>	<u>128</u>	<u>340</u>	<u>66</u>	<u>440</u>	<u>40</u>	<u>6</u>	<u>28</u>	<u>0</u>	2	<u>4</u>	<u>34</u>
<u>60</u>	<u>30</u>	<u>128</u>	<u>320</u>	<u>96</u>	<u>440</u>	<u>40</u>	$\underline{4^1}$	<u>28</u>	$2^{1}$	2	<u>4</u>	<u>24</u>
<u>60</u>	<u>30</u>	<u>128</u>	<u>300</u>	<u>110</u>	<u>440</u>	<u>40</u>	<u>6</u>	<u>24</u>	<u>0</u>	2	<u>8</u>	<u>30</u>
<u>60</u>	<u>30</u>	<u>128</u>	<u>280</u>	<u>140</u>	<u>440</u>	<u>40</u>	$\underline{4^1}$	<u>24</u>	$2^{1}$	2	<u>8</u>	<u>20</u>
<u>120</u>	<u>60</u>	<u>64</u>	<u>600</u>	<u>252</u>	<u>880</u>	<u>80</u>	$\underline{4^1}$	<u>56</u>	$8^{1,2}$	<u>4</u>	<u>8</u>	<u>28</u>
<u>240</u>	<u>120</u>	<u>32</u>	<u>1400</u>	<u>252</u>	<u>1760</u>	<u>160</u>	$20^{1}$	<u>120</u>	$8^{1,2}$	<u>4</u>	<u>8</u>	108
<u>480</u>	<u>240</u>	<u>16</u>	<u>2880</u>	<u>384</u>	<u>3520</u>	<u>320</u>	$48^{1}$	<u>240</u>	$8^{1,2}$	<u>8</u>	<u>16</u>	<u>256</u>
<u>960</u>	<u>480</u>	<u>8</u>	<u>6080</u>	<u>384</u>	<u>7040</u>	<u>640</u>	<u>112<sup>1</sup></u>	<u>496</u>	<u>8<sup>1,2</sup></u>	<u>8</u>	<u>16</u>	<u>576</u>
<u>1920</u>	<u>960</u>	<u>4</u>	<u>12480</u>	<u>384</u>	<u>14080</u>	1280	$240^{1}$	1008	<u>8<sup>1,2</sup></u>	<u>8</u>	<u>16</u>	1216

1) This figure does not take into account the extra TFCI bits from deleted slots

2) If no TFCI then the TFCI field is blank<u>If TFCI bits are not used, then DTX shall be used in TFCI field</u> Note: Compressed mode with puncturing cannot be used for SF=512 with TFCI

<del>Channel</del> <del>Bit Rate</del> <del>(kbps)</del>	<del>Channel</del> <del>Symbol</del> <del>Rate</del> ( <del>ksps)</del>	<del>SF</del>	Bits/Frame		<del>Bits/</del> <del>Slot</del>	<del>DPDCH</del> <del>Bits/Slot</del>		<del>DPCCH</del> <del>Bits/Slot</del>		<del>Extra</del> <del>DPDCH symbols</del> for DTX		
			<del>DPDCH</del>	DPCCH	TOT		N <sub>Data1</sub>	N <sub>Data2</sub>	N <sub>TFCI</sub>	N <sub>TPC</sub>	N <sub>Pilot</sub>	
<del>16</del>	8	<del>512</del>	<del>40</del>	<del>72</del>	<del>120</del>	<del>10</del>	2	2	θ	2	4	8
<del>32</del>	<del>16</del>	<del>256</del>	<del>100</del>	<del>120</del>	<del>240</del>	<del>20</del>	2	8	0	2	8	<del>20</del>
<del>32</del>	<del>16</del>	<del>256</del>	<del>80</del>	<del>150</del>	<del>240</del>	<del>20</del>	θ	<del>8</del> 1	$2^{+}$	2	8	<del>10</del>
<del>64</del>	<del>32</del>	<del>128</del>	<del>300</del>	<del>120</del>	4 <del>80</del>	4 <del>0</del>	6	<del>2</del> 4	θ	2	8	<del>60</del>
<del>64</del>	<del>32</del>	<del>128</del>	<del>280</del>	<del>150</del>	<del>480</del>	<del>40</del>	4 <sup>‡</sup>	<del>24</del>	$2^{+}$	2	8	<del>50</del>
<del>128</del>	<del>64</del>	<del>64</del>	<del>600</del>	<del>264</del>	<del>960</del>	<del>80</del>	$4^{1}$	<del>56</del>	8 <sup>1,2</sup>	4	8	<del>96</del>
<del>256</del>	<del>128</del>	<del>32</del>	<del>1400</del>	<del>264</del>	<del>1920</del>	<del>160</del>	$20^{+}$	<del>120</del>	8 <sup>1,2</sup>	4	8	<del>256</del>
<del>512</del>	<del>256</del>	<del>16</del>	<del>2880</del>	<del>408</del>	<del>3840</del>	<del>320</del>	48 <sup>1</sup>	<del>240</del>	8 <sup>1,2</sup>	8	<del>16</del>	<del>552</del>
<del>1024</del>	<del>512</del>	8	<del>6080</del>	4 <del>08</del>	<del>7680</del>	<del>640</del>	$112^{1}$	4 <del>96</del>	8 <sup>1,2</sup>	8	<del>16</del>	<del>1192</del>
<del>2048</del>	<del>1024</del>	4	<del>12480</del>	<del>408</del>	<del>15360</del>	<del>1280</del>	$240^{4}$	<del>1008</del>	<del>8<sup>1,2</sup></del>	8	<del>16</del>	<del>2472</del>

Table 12: DPDCH and DPCCH fields in compressed mode frame when puncturing 3 slots

<u>Channel</u> <u>Bit Rate</u> (kbps)	<u>Channel</u> <u>Symbol</u> Rate	<u>SF</u>	<u>Bits/Frame</u>		Bits/ DPDCH Slot Bits/Slot		DPCCH Bits/Slot			<u>Extra</u> DPDCH symbols		
( <u>Reps</u> /	<u>(ksps)</u>		<u>DPDCH</u>	DPCCH	TOT		N <sub>Data1</sub>	N <sub>Data2</sub>	N <sub>TFCI</sub>	N <sub>TPC</sub>	<u>N<sub>Pilot</sub></u>	for DTX
<u>15</u>	7.5	<u>512</u>	40	<u>72</u>	120	<u>10</u>	2	2	0	2	<u>4</u>	<u>8</u>
<u>30</u>	<u>15</u>	256	160	<u>48</u>	240	<u>20</u>	2	<u>14</u>	<u>0</u>	<u>2</u>	<u>2</u>	<u>32</u>
<u>30</u>	<u>15</u>	<u>256</u>	<u>140</u>	<u>78</u>	240	<u>20</u>	<u>0</u>	$14^{1}$	$2^{1}$	<u>2</u>	<u>2</u>	<u>22</u>
<u>30</u>	<u>15</u>	256	<u>140</u>	<u>72</u>	240	<u>20</u>	<u>2</u>	<u>12</u>	<u>0</u>	<u>2</u>	<u>4</u>	<u>28</u>
<u>30</u>	<u>15</u>	256	<u>120</u>	102	240	<u>20</u>	<u>0</u>	$12^{1}$	$2^{1}$	<u>2</u>	<u>4</u>	<u>18</u>
<u>30</u>	<u>15</u>	256	100	120	240	<u>20</u>	<u>2</u>	<u>8</u>	<u>0</u>	<u>2</u>	<u>8</u>	<u>20</u>
<u>30</u>	<u>15</u>	256	<u>80</u>	<u>150</u>	240	<u>20</u>	<u>0</u>	<u>8<sup>1</sup></u>	$2^{1}$	<u>2</u>	<u>8</u>	<u>10</u>
<u>60</u>	<u>30</u>	128	<u>340</u>	<u>72</u>	<u>480</u>	<u>40</u>	<u>6</u>	<u>28</u>	<u>0</u>	<u>2</u>	<u>4</u>	<u>68</u>
<u>60</u>	<u>30</u>	128	<u>320</u>	102	480	<u>40</u>	$\underline{4^1}$	<u>28</u>	$2^{1}$	<u>2</u>	<u>4</u>	<u>58</u>
<u>60</u>	<u>30</u>	128	<u>300</u>	120	<u>480</u>	<u>40</u>	<u>6</u>	<u>24</u>	<u>0</u>	<u>2</u>	<u>8</u>	<u>60</u>
<u>60</u>	<u>30</u>	128	<u>280</u>	<u>150</u>	<u>480</u>	<u>40</u>	$\underline{4^1}$	<u>24</u>	<u>21</u>	<u>2</u>	<u>8</u>	<u>50</u>
<u>120</u>	<u>60</u>	<u>64</u>	<u>600</u>	<u>264</u>	<u>960</u>	<u>80</u>	$\underline{4}^1$	<u>56</u>	<b>8</b> <sup>1,2</sup>	<u>4</u>	<u>8</u>	<u>96</u>
240	120	<u>32</u>	1400	<u>264</u>	1920	<u>160</u>	$20^{1}$	120	<u>8</u> <sup>1,2</sup>	<u>4</u>	<u>8</u>	256
480	<u>240</u>	<u>16</u>	<u>2880</u>	<u>408</u>	<u>3840</u>	<u>320</u>	$48^{1}$	240	<b>8</b> <sup>1,2</sup>	<u>8</u>	<u>16</u>	<u>552</u>
<u>960</u>	<u>480</u>	<u>8</u>	<u>6080</u>	<u>408</u>	7680	<u>640</u>	$112^{1}$	<u>496</u>	<u>8</u> <sup>1,2</sup>	<u>8</u>	<u>16</u>	1192
1920	<u>960</u>	<u>4</u>	<u>12480</u>	<u>408</u>	15360	1280	$240^{1}$	1008	<b>8</b> <sup>1,2</sup>	<u>8</u>	<u>16</u>	<u>2472</u>

1) This figure does not take into account the extra TFCI bits from deleted slots

2) If no TFCI then the TFCI field is blank If TFCI bits are not used, then DTX shall be used in TFCI field

Note: Compressed mode with puncturing cannot be used for SF=512 with TFCI

 $\leq$ Mitsubishi NOTE: In all cases where the number of idle slots per frame = 1 to 7, the same description as given in table11,12 is necessary. $\geq$ 

Mitsubishi NOTE: The old section 4.4.2.2 (Method A2) is proposed not to be included for R'99. The figure and text is changed to a more realistic exemple with interleaving depth 40ms. This section could be moved to the study items document.

#### 4.4.2.2Method A2: By puncturing, optional case for services that allow larger delay

Other methods of supporting compressed mode may be considered as options. For example, with services that allows for a larger delay, e.g. data services with interleaving over several frames, multiple frames might be compressed together in order to create a short measurement slot. As an example, for a 2 Mbps service, with interleaving of 45 frames (4050 ms), an 5 ms idle gap of 7 slots can be created by puncturing only 1011.66% of 45 frames, as illustrated in Figure 18.



Compressed transmission during one interleaver span

#### Figure 18: Multi-frame compressed mode for long-delay services

### 4.4.2.3<u>4.4.2.2</u> Method B: By reducing the spreading factor by 2

During compressed mode, the spreading factor (SF) can be reduced by 2 to enable the transmission of the information bits in the remaining time slots of a compressed frame. This can accommodate up to 50% idle slots per frame which is the maximum compression factor required. Additional rate matching is required if there are less than 50% idle slots. Reducing the spreading factor will normally be used if rate matching alone is not sufficient to transmit all information bits in compressed mode. Use of this method for uplink compressed mode is for further study.

On the downlink, UTRAN can also order the UE to use a different scrambling code in compressed mode than in normal mode. If the UE is ordered to use a different scrambling code in compressed mode, then there is a one-to-one mapping between the scrambling code used in normal mode and the one used in compressed mode, as described in TS 25.213 section 5.2.1.

# 4.4.3 Transmission gap position

Transmission gaps can be placed at both fixed position and adjustable position for each purpose such as interfrequency power measurement, acquisition of control channel of other system/carrier, and actual handover operation.

### 4.4.3.1 Fixed transmission gap position

The transmission gaps can be placed onto fixed positions. When using single frame method, the fixed transmission gap is located within the compressed frame depending on the transmission gap length (TGL) as shown in Figure 19 (1). When using double frame method, the fixed transmission gap is located on the center of two connected frames as shown in Figure 19 (2). Table 13 shows the parameters for the fixed transmission gap position case.





Figure 19: Fixed\_transmission gap-lengths position

Table 12 Table 13:	Parameters	for fixed	transmission	gap position
				3

	Single-fram	ne method	Double-frame method		
<u>TGL</u> Transmission gap length	$\mathbf{N}_{\mathbf{first}}$	N <sub>last</sub>	$\mathbf{N}_{\mathbf{first}}$	$\mathbf{N}_{\mathbf{last}}$	
3	<u>7</u> 8	<u>9</u> 10	<u>14</u> 15 in first frame	12 in second frame	
4	<u>6</u> 7	<u>9</u> 10	<u>13</u> 14 in first frame	<u>1</u> 2 in second frame	
7	<u>6</u> 7	<u>12</u> 13	<u>12</u> 13 in first frame	<u>3</u> 4 in second frame	
10	N.A.	N.A.	<u>10</u> 11 in first frame	45 in second frame	
14	N.A.	N.A.	<u>8</u> 9 in first frame	<u>6</u> 7 in second frame	

#### 4.4.3.2 Adjustable transmission gap position

Position of transmission gaps can be adjustable/relocatable for some purpose e.g. data acquisition on certain position as shown in Figure 20. When the transmission gap spans two consecutive radio frames,  $N_{first}$  and TGL must be chosen so that at least 8 slots in each radio frame are transmitted Parameters of the adjustable transmission gap lengths positions are calculated as follows:

N<sub>idle</sub>-TGL is the number of consecutive idle slots during compressed mode, as shown in Table 13,

N<sub>idle</sub><u>TGL</u> = 3, 4, 5,6,87, 10,16 14

N<sub>first</sub> specifies the starting slot of the consecutive idle slots,

 $N_{\text{first}} = \frac{1,2,3,\ldots,16}{0,1,2,3,\ldots,14}$ 

N<sub>last</sub> shows the number of the final idle slot and is calculated as follows;

If  $N_{\text{first}} + \frac{N_{\text{idle}}}{TGL} \le \frac{1715}{15}$ , then  $N_{\text{last}} = N_{\text{first}} + \frac{N_{\text{idle}}}{TGL} - 1$  ( in the same frame ),

If  $N_{\text{first}} + \frac{N_{\text{idle}}}{1000} = \frac{1715}{15}$ , then  $N_{\text{last}} = (N_{\text{first}} + \frac{N_{\text{idle}}}{1000} = \frac{1000}{1000} = \frac{1710}{1000} = \frac{1000}{1000} = \frac{1000}{10$ 

When the transmission gap spans two consecutive radio frames, N<sub>first</sub> and TGL must be chosen so that at least 8 slots in each radio frame are transmitted





Figure 20: Concept of a<u>A</u>djustable transmission gap lengths position <<u>Editors note: Adjustment needed</u>>

### 4.4.3.3 Parameters for compressed mode

< Editor's note: WG1 suggestion is that there is need for further clarifications in Table 14 (e.g. rationales between change of coding rate/puncturing/change of spreading factor and idle time size, spreading factor range for different modes, etc.).>

Table 14 shows the detailed parameters for each number of idle slots <u>when transmission time reduction methods A or</u> <u>B are used</u>. This is an example for the 10ms interleaving depth. Application of compressed mode for interleaving depths other than 10ms are for further study. Each number of idle slots are classified for three cases:

Case 1 - Power measurement : Number of idle slots = 3, 4, 5, 6.

Case 2 Acquisition of control channels : Number of idle slots = 3, 4, 5, 6, 8, 10.

Case 3 Actual handover operation : Number of idle slots = 10, 16.

Number of	Mode	Spreading Factor	Idle length [ms]	Transmission time	Idle frame
ittle slots				reduction method	comoning
3	A	<del>512 256</del>	$\frac{1.63}{1.63}$	Puncturing	
	B	<del>128</del> _4	<del>1.63 1.75</del>	Spreading facter	<del>(S)/(D)</del>
				reduction by 2	
4	A	<del>512 256</del>	<del>2.25 2.25</del>		
	₿	<del>128</del> 4	<del>2.25 2.37</del>	Puncturing (I	<del>))</del>
5	A	<del>512 256</del>	<del>2.87 2.87</del>	Spreading facter reduction	n by 2(D)/(S)
	₿	<del>128</del> 4	<del>2.87 2.99</del>		
<del>6</del>	A	<del>512 - 256</del>	<del>3.50 - 3.50</del>	Puncturing (D)	<del>/(S)</del>
	B	<del>128 - 4</del>	<del>3.50 - 3.62</del>	Spreading factor reductio	<del>n by 2 (S)/(D)</del>
7	A	<del>512 256</del>	4.75 4.75	Sunsadin a fastan nadaat	ion her 2 (S)
	₿	<del>128</del> _4	4 <del>.75 4.87</del>	Spreading factor reduct	<del>ion by 2 (3)</del>
<del>10</del>	A	<del>512 256</del>	6.00 6.00	Puncturing	
	B	<del>128 - 4</del>	<del>6.00 - 6.12</del>	Spreading factor	
				reduction by 2	
-14	A	<del>512 256</del>	<del>9.75 9.75</del>	Puncturing	<del>יש)</del>
	B	<del>128 - 4</del>	<del>9.75 - 9.87</del>	Spreading factor	
				reduction by 2	

Table 14: Parameters for	compressed mode
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<u>TGL</u>	<u>Type</u>	<u>Adjustable</u> /fixed gap	<u>Spreading</u> Factor	<u>Idle</u> length[ms]	Transmission time Reduction method	<u>Idle frame</u> Combining
		position	14000	<u>icing in mis</u>	<u>Accurction memore</u>	
<u>3</u>	<u>A</u>	Adjustable	512 - 4	<u>1.73-1.99</u>	Puncturing	$\frac{(S)}{(D) = (1,2),(2,1)}$
	<u>B</u>	<u>Or</u>	<u>256-4</u>	<u>1.60-1.86</u>	Spreading facter	
<u>4</u>	A	Fixed	<u>512 - 4</u>	<u>2.40-2.66</u>	reduction by 2	$\frac{(S)}{(D) = (1 \ 3) \ (2 \ 2) \ (3 \ 1)}$
	<u>B</u>		<u>256-4</u>	<u>2.27-2.53</u>		
<u>5</u>	<u>A</u>		<u>512 - 4</u>	<u>3.07-3.33</u>		$\frac{(S)}{(D) - (1 \ 4) \ (2 \ 3) \ (3 \ 2) \ (4 \ 1)}$
	<u>B</u>		<u>256-4</u>	<u>2.93-3.19</u>		
<u>6</u>	<u>A</u>		<u>512 - 4</u>	<u>3.73-3.99</u>		$\frac{(S)}{(D) - (1, 5)(2, 4)(3, 3)(4, 2)(5)}$
	<u>B</u>		<u>256-4</u>	<u>3.60-3.86</u>		$(\underline{D}) = (1, 3), (2, 4), (3, 3), (4, 2), (4, 3), (4, 2), (4, 3), (4,$
<u>7</u>	<u>A</u>		<u>512 - 4</u>	4.40-4.66		$\frac{(S)}{(D)-(1,6)(2,5)(3,4)(4,3)(5,5)}$
	<u>B</u>		<u>256-4</u>	<u>4.27-4.53</u>		<u>,2),(6,1)</u>
<u>10</u>	<u>A</u>		<u>512 - 4</u>	<u>6.40-6.66</u>		(D)=(3,7),(4,6),(5,5),(6,4),(7)
	<u>B</u>		<u>256- 4</u>	<u>6.27-6.53</u>		<u>11</u>
<u>14</u>	<u>A</u>	Fixed	<u>512 - 4</u>	<u>9.07-9.33</u>		<u>(D) =(7,7)</u>
	B		<u>256-4</u>	8.93-9.19	1	

(S): Single-frame method as shown in Figure 19 (1).

(D): Double-frame method as shown in Figure 19 (2). (x,y) indicates x: the number of idle slots in the first frame, y: the number of idle slots in the second frame.
SF="2/1": "2" is for (S) and "1" is for (D).
Note: Details for the use of the spreading factor reduction method with SF=4 are FFS

# References

[1] 3GPP TSG RAN 25.212 V2.2.0, Source: Editor