**3GPP TSG- Meeting #**

**, 25 January – 5 February 2021**

|  |
| --- |
| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
|  |
|  | **101-3** | **CR** |  | **rev** |  | **Current version:** |  |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network |  | Core Network |  |

|  |
| --- |
|  |
| ***Title:***  |  |
|  |  |
| ***Source to WG:*** |  |
| ***Source to TSG:*** |   |
|  |  |
| ***Work item code:*** | DC\_R17\_1BLTE\_1BNR\_2DL2UL  |  | ***Date:*** |  |
|  |  |  |  |  |
| ***Category:*** | B |  | ***Release:*** | 7 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
|  |  |
| ***Reason for change:*** | Adding CA\_n7B UL configurations |
|  |  |
| ***Summary of change:*** | Adding CA\_n7B UL configurations |
|  |  |
| ***Consequences if not approved:*** | CA\_n7B UL configurations are not added |
|  |  |
| ***Clauses affected:*** |  5.5 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  |  |
| ***affected:*** | **X** |  |  Test specifications | TS 38.521 series |
| ***(show related CRs)*** |  | **X** |  O&M Specifications |  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

---Start of changes---

#### 5.5B.4.1 Inter-band EN-DC configurations within FR1 (two bands)

Table 5.5B.4.1-1: Inter-band EN-DC configurations within FR1 (two bands)

| EN-DCconfiguration | Uplink EN-DCconfiguration(NOTE 1) | Single UL allowed | DL interruption allowed(Note 14) |
| --- | --- | --- | --- |
| DC\_1A\_n3ADC\_1C\_n3A | DC\_1A\_n3ADC\_1C\_n3A | DC\_1\_n3 |  |
| DC\_1A\_n5A | DC\_1A\_n5A | No |  |
| DC\_1A\_n7ADC\_1A\_n7B | DC\_1A\_n7ADC\_1A\_n7B | No |  |
| DC\_1A-1A\_n7ADC\_1A-1A\_n7B | DC\_1A\_n7ADC\_1A\_n7B | No |  |
| DC\_1A\_n8A | DC\_1A\_n8A | No |  |
| DC\_1A\_n20A | DC\_1A\_n20A | No |  |
| DC\_1A\_n28A | DC\_1A\_n28A | No |  |
| DC\_1A-1A\_n28A | DC\_1A\_n28A | No |  |
| DC\_1A\_n38ADC\_1C\_n38A | DC\_1A\_n38A | No |  |
| DC\_1A\_n40ADC\_1A\_n40B | DC\_1A\_n40A | No |  |
| DC\_1A\_n41A | DC\_1A\_n41A | No |  |
| DC\_1A\_n50A | DC\_1A\_n50A | No |  |
| DC\_1A\_n51A | DC\_1A\_n51A | No |  |
| DC\_1A\_n71ADC\_1A\_n71B | DC\_1A\_n71A | No |  |
| DC\_1A\_n77A7DC\_1A\_n77C7 | DC\_1A\_n77A | DC\_1\_n77 | No |
| DC\_1A\_n77(2A) | DC\_1A\_n77A | DC\_1\_n77 | No |
| DC\_1A\_n78A7DC\_1A\_n78C7 | DC\_1A\_n78A | No | No |
| DC\_1A\_n78(2A)7DC\_1A-1A\_n78A | DC\_1A\_n78A | No | No |
| DC\_1A\_n79A7DC\_1A\_n79C7 | DC\_1A\_n79A | No | No |
| DC\_2A\_n5A | DC\_2A\_n5A | No |  |
| DC\_2A-2A\_n5A | DC\_2A\_n5A | No |  |
| DC\_2A\_n7A | DC\_2A\_n7A | No |  |
| DC\_2A\_n7(2A) | DC\_2A\_n7A | No |  |
| DC\_2A\_n12A | DC\_2A\_n12A | No |  |
| DC\_2A\_n28A | DC\_2A\_n28A | No |  |
| DC\_2A\_n38A | DC\_2A\_n38A | No |  |
| DC\_2A-2A\_n38A | DC\_2A\_n38A | No |  |
| DC\_2A\_n41ADC\_2A\_n41CDC\_2C\_n41A | DC\_2A\_n41ADC\_2C\_n41A | No |  |
| DC\_2A-2A\_n41ADC\_2A\_n41(2A) | DC\_2A\_n41A | No |  |
| DC\_2A\_n46A | DC\_2A\_n46A | No |  |
| DC\_2A\_n48ADC\_2A\_n48B | DC\_2A\_n48A | No |  |
| DC\_2A\_n66ADC\_2A\_n66(2A) | DC\_2A\_n66A | DC\_2\_n66 |  |
| DC\_2A-2A\_n66A | DC\_2A\_n66A | DC\_2\_n66 |  |
| DC\_2A\_n71ADC\_2A\_n71BDC\_2C\_n71A | DC\_2A\_n71ADC\_2C\_n71A | No |  |
| DC\_2A-2A\_n71A | DC\_2A\_n71A | No |  |
| DC\_2A\_n77A | DC\_2A\_n77A | DC\_2\_n77 |  |
| DC\_2A-2A\_n77A | DC\_2A\_n77A | DC\_2\_n77 |  |
| DC\_2A\_n78A | DC\_2A\_n78A | DC\_2\_n78 |  |
| DC\_2A\_n78(2A) | DC\_2A\_n78A | DC\_2\_n78 |  |
| DC\_2A-2A\_n78A | DC\_2A\_n78A | DC\_2\_n78 |  |
| DC\_3A\_n1ADC\_3C\_n1A | DC\_3A\_n1ADC\_3C\_n1A | DC\_3\_n1 |  |
| DC\_3A-3A\_n1A | DC\_3A\_n1A | DC\_3\_n1 |  |
| DC\_3A\_n5ADC\_3C\_n5A | DC\_3A\_n5ADC\_3C\_n5A | DC\_3\_n5 |  |
| DC\_3A\_n7ADC\_3A\_n7BDC\_3C\_n7ADC\_3C\_n7B | DC\_3A\_n7ADC\_3A\_n7BDC\_3C\_n7ADC\_3C\_n7B | No |  |
| DC\_3A-3A\_n7ADC\_3A-3A\_n7B | DC\_3A\_n7ADC\_3A\_n7B | No |  |
| DC\_3A\_n8A | DC\_3A\_n8A | No |  |
| DC\_3A\_n20A | DC\_3A\_n20A | No |  |
| DC\_3A\_n28ADC\_3C\_n28A | DC\_3A\_n28ADC\_3C\_n28A | No |  |
| DC\_3A\_n34A | DC\_3A\_n34A | No |  |
| DC\_3A\_n38ADC\_3C\_n38A | DC\_3A\_n38A | No |  |
| DC\_3A\_n40ADC\_3A\_n40B | DC\_3A\_n40A | No |  |
| DC\_3A\_n41ADC\_3C\_n41A | DC\_3A\_n41ADC\_3C\_n41A | DC\_3\_n41 | No |
| DC\_3A\_n50A | DC\_3A\_n50A | No |  |
| DC\_3A\_n51A | DC\_3A\_n51A | No |  |
| DC\_3A\_n77A7DC\_3A\_n77C7DC\_3C\_n77A7 | DC\_3A\_n77ADC\_3C\_n77A | No |  |
| DC\_3A\_n77(2A)7DC\_3C\_n77(2A)7 | DC\_3A\_n77ADC\_3C\_n77A | DC\_3\_n77 | No |
| DC\_3A-3A\_n77A | DC\_3A\_n77A | DC\_3\_n77 | No |
| DC\_3A\_n78A7DC\_3A\_n78C7DC\_3C\_n78A7 | DC\_3A\_n78ADC\_3C\_n78A | DC\_3\_n77 | No |
| DC\_3A\_n77A7DC\_3A\_n77C7DC\_3C\_n77A7 | DC\_3A\_n77ADC\_3C\_n77A | DC\_3\_n78 | No |
| DC\_3A\_n78(2A)7DC\_3C\_n78(2A)7 | DC\_3A\_n78A | DC\_3\_n78 | No |
| DC\_3A-3A\_n78A | DC\_3A\_n78A | DC\_3\_n78 | No |
| DC\_3A\_n79A7DC\_3A\_n79C7DC\_3C\_n79A7 | DC\_3A\_n79ADC\_3C\_n79A | No | No |
| DC\_4A\_n2A | DC\_4A\_n2A | No |  |
| DC\_4A\_n5A | DC\_4A\_n5A | DC\_4\_n5 |  |
| DC\_4A\_n7A | DC\_4A\_n7A | No |  |
| DC\_4A\_n28A | DC\_4A\_n28A | No |  |
| DC\_4A\_n38A | DC\_4A\_n38A | No |  |
| DC\_4A\_n41A | DC\_4A\_n41A | No |  |
| DC\_4A\_n78A | DC\_4A\_n78A | No |  |
| DC\_4A\_n78(2A) | DC\_4A\_n78A | No |  |
| DC\_5A\_n2ADC\_5B\_n2A | DC\_5A\_n2A | No |  |
| DC\_5A-5A\_n2A | DC\_5A\_n2A | No |  |
| DC\_5A\_n7A | DC\_5A\_n7A | DC\_5\_n7 |  |
| DC\_5A\_n7(2A) | DC\_5A\_n7A | DC\_5\_n7 |  |
| DC\_5A\_n12A | DC\_5A\_n12A | No |  |
| DC\_5A\_n38A | DC\_5A\_n38A | DC\_5\_n38 |  |
| DC\_5A\_n40A | DC\_5A\_n40A | No |  |
| DC\_5A\_n48ADC\_5A\_n48B | DC\_5A\_n48A | No |  |
| DC\_5A\_n66ADC\_5B\_n66A | DC\_5A\_n66A | DC\_5\_n66 |  |
| DC\_5A-5A\_n66A | DC\_5A\_n66A | DC\_5\_n66 |  |
| DC\_5A\_n77A | DC\_5A\_n77A | No |  |
| DC\_5A\_n71A | DC\_5A\_n71A | No |  |
| DC\_5A\_n78A7DC\_5A\_n78C7 | DC\_5A\_n78A | No | No |
| DC\_5A\_n78(2A)7 | DC\_5A\_n78A | No | No |
| DC\_5A\_n79A | DC\_5A\_n79A | No | No |
| DC\_7A\_n1ADC\_7C\_n1A | DC\_7A\_n1ADC\_7C\_n1A | No |  |
| DC\_7A-7A\_n1A | DC\_7A\_n1A | No |  |
| DC\_7A\_n2ADC\_7C\_n2A | DC\_7A\_n2A | No |  |
| DC\_7A\_n3ADC\_7C\_n3A | DC\_7A\_n3ADC\_7C\_n3A | No |  |
| DC\_7A\_n5ADC\_7C\_n5A | DC\_7A\_n5ADC\_7C\_n5A | DC\_7\_n5 |  |
| DC\_7A-7A\_n5A | DC\_7A\_n5A | DC\_7\_n5 |  |
| DC\_7A\_n8A | DC\_7A\_n8A | No |  |
| DC\_7A-7A\_n78A7DC\_7A-7A\_n78C7 | DC\_7A\_n78A | No |  |
| DC\_7A-7A\_n78(2A)7 | DC\_7A\_n78A | No |  |
| DC\_7A\_n20A | DC\_7A\_n20A | No |  |
| DC\_7A\_n28ADC\_7C\_n28A | DC\_7A\_n28ADC\_7C\_n28A | No |  |
| DC\_7A\_n40A | DC\_7A\_n40A | Yes |  |
| DC\_7A\_n51A | DC\_7A\_n51A | No |  |
| DC\_7A\_n66ADC\_7C\_n66A | DC\_7A\_n66A | No |  |
| DC\_7A-7A\_n66A | DC\_7A\_n66A | No |  |
| DC\_7A\_n71A | DC\_7A\_n71A | No |  |
| DC\_7A\_n77ADC\_7A\_n77(2A)DC\_7C\_n77ADC\_7C\_n77(2A) | DC\_7A\_n77A | No |  |
| DC\_7A-7A\_n77ADC\_7A-7A\_n77(2A) | DC\_7A\_n77A | No |  |
| DC\_7A\_n78A7DC\_7C\_n78A7DC\_7A\_n78C7 | DC\_7A\_n78ADC\_7C\_n78A | No |  |
| DC\_7A\_n78(2A)7DC\_7C\_n78(2A)7 | DC\_7A\_n78ADC\_7C\_n78A | No |  |
| DC\_8A\_n1A | DC\_8A\_n1A | No |  |
| DC\_8A\_n2A | DC\_8A\_n2A | DC\_8\_n2 |  |
| DC\_8A\_n3A | DC\_8A\_n3A | No |  |
| DC\_8A\_n7A | DC\_8A\_n7A | No |  |
| DC\_8A\_n20A | DC\_8A\_n20A | Yes |  |
| DC\_8A\_n28A | DC\_8A\_n28A | No |  |
| DC\_8A\_n34A | DC\_8A\_n34A | No |  |
| DC\_8A\_n39A | DC\_8A\_n39A | No |  |
| DC\_8A\_n40A7 | DC\_8A\_n40A | No |  |
| DC\_8A\_n41ADC\_8A\_n41C | DC\_8A\_n41A | No | No |
| DC\_8A\_n41(2A) | DC\_8A\_n41A | No | No |
| DC\_8A\_n77A7 | DC\_8A\_n77A | No | No |
| DC\_8A\_n77(2A)7 | DC\_8A\_n77A | No | No |
| DC\_8A\_n78A7 | DC\_8A\_n78A | No | No |
| DC\_8A\_n79A7DC\_8A\_n79C | DC\_8A\_n79ADC\_8A\_n79C | No | No |
| DC\_8A\_n93A | DC\_8A\_n93A\_ULSUP-TDM | N/A |  |
| DC\_8A\_n94A | DC\_8A\_n94A\_ULSUP-TDM | N/A |  |
| DC\_11A\_n3A | DC\_11A\_n3A | No |  |
| DC\_11A\_n28A | DC\_11A\_n28A | No |  |
| DC\_11A\_n77A7 | DC\_11A\_n77A | No | No |
| DC\_11A\_n77(2A)7 | DC\_11A\_n77A | No | No |
| DC\_11A\_n78A7 | DC\_11A\_n78A | No | No |
| DC\_11A\_n79A7 | DC\_11A\_n79A | No |  |
| DC\_12A\_n2A | DC\_12A\_n2A | No |  |
| DC\_12A\_n5A | DC\_12A\_n5A | No |  |
| DC\_12A\_n7ADC\_12A\_n7(2A) | DC\_12A\_n7A | No |  |
| DC\_12A\_n25A | DC\_12A\_n25A | No |  |
| DC\_12A\_n38A | DC\_12A\_n38A | No |  |
| DC\_12A\_n41A | DC\_12A\_n41A | No |  |
| DC\_12A\_n66ADC\_12A\_n66(2A) | DC\_12A\_n66A | No |  |
| DC\_12A\_n78ADC\_12A\_n78(2A) | DC\_12A\_n78A | DC\_12\_n78 |  |
| DC\_13A\_n2A | DC\_13A\_n2A | No |  |
| DC\_13A\_n5A | DC\_13A\_n5A | DC\_13\_n5 |  |
| DC\_13A\_n7ADC\_13A\_n7(2A) | DC\_13A\_n7A | No |  |
| DC\_13A\_n48ADC\_13A\_n48B | DC\_13A\_n48A | No |  |
| DC\_13A\_n66A | DC\_13A\_n66A | No |  |
| DC\_13A\_n71A | DC\_13A\_n71A | No |  |
| DC\_13A\_n77A | DC\_13A\_n77A | No |  |
| DC\_13A\_n78ADC\_13A\_n78(2A) | DC\_13A\_n78A | No |  |
| DC\_14A\_n2A | DC\_14A\_n2A | No |  |
| DC\_14A\_n66A | DC\_14A\_n66A | No |  |
| DC\_18A\_n3A | DC\_18A\_n3A | No |  |
| DC\_18A\_n28A8 | DC\_18A\_n28A | No |  |
| DC\_18A\_n41A16 | DC\_18A\_n41A | No |  |
| DC\_18A\_n77A7DC\_18A\_n77(2A)7 | DC\_18A\_n77A | No | No |
| DC\_18A\_n78A7DC\_18A\_n78(2A)7 | DC\_18A\_n78A | No | No |
| DC\_20A\_n91A | DC\_20A\_n91A\_ULSUP-TDM | N/A |  |
| DC\_20A\_n92A | DC\_20A\_n92A\_ULSUP-TDM | N/A |  |
| DC\_18A\_n79A7 | DC\_18A\_n79A | No |  |
| DC\_19A\_n1A | DC\_19A\_n1A | No |  |
| DC\_19A\_n77A7DC\_19A\_n77C7 | DC\_19A\_n77A | No |  |
| DC\_19A\_n78A7DC\_19A\_n78C7 | DC\_19A\_n78A | No | No |
| DC\_19A\_n79A7DC\_19A\_n79C7 | DC\_19A\_n79A | No | No |
| DC\_20A\_n1A | DC\_20A\_n1A | No |  |
| DC\_20A\_n3A | DC\_20A\_n3A | No |  |
| DC\_20A\_n7A | DC\_20A\_n7A | DC\_20\_n7 |  |
| DC\_20A\_n8A | DC\_20A\_n8A | DC\_20\_n8 |  |
| DC\_20A\_n28A8, 11,13 | DC\_20A\_n28A | No |  |
| DC\_20A\_n38A | DC\_20A\_n38A | No |  |
| DC\_20A\_n41A | DC\_20A\_n41A | DC\_20\_n41 |  |
| DC\_20A\_n50A | DC\_20A\_n50A | No |  |
| DC\_20A\_n51A | DC\_20A\_n51A | No |  |
| DC\_20A\_n77A7 | DC\_20A\_n77A | No |  |
| DC\_20A\_n78A7 | DC\_20A\_n78A | No |  |
| DC\_20A\_n78(2A)7 | DC\_20A\_n78A | No |  |
| DC\_21A\_n1A | DC\_21A\_n1A | No |  |
| DC\_21A\_n77A7DC\_21A\_n77C7 | DC\_21A\_n77A | No |  |
| DC\_21A\_n78A7DC\_21A\_n78C7 | DC\_21A\_n78A | No | No |
| DC\_21A\_n79A7DC\_21A\_n79C7 | DC\_21A\_n79A | No | No |
| DC\_25A\_n41A | DC\_25A\_n41A | No |  |
| DC\_25A-25A\_n41A | DC\_25A\_n41A | No |  |
| DC\_26A\_n25A | DC\_26A\_n25A | No |  |
| DC\_26A\_n41A | DC\_26A\_n41A | No |  |
| DC\_26A\_n77A7 | DC\_26A\_n77A | No |  |
| DC\_26A\_n78A7 | DC\_26A\_n78A | No |  |
| DC\_26A\_n79A7 | DC\_26A\_n79A | No |  |
| DC\_28A\_n1A | DC\_28A\_n1A | No |  |
| DC\_28A\_n2A | DC\_28A\_n2A | No |  |
| DC\_28A\_n3A | DC\_28A\_n3A | No |  |
| DC\_28A\_n5A8 | DC\_28A\_n5A | No |  |
| DC\_28A\_n7ADC\_28A\_n7B | DC\_28A\_n7ADC\_28A\_n7B | No |  |
| DC\_28A\_n51A | DC\_28A\_n51A | No |  |
| DC\_28A\_n8A | DC\_28A\_n8A | No |  |
| DC\_28A\_n40A | DC\_28A\_n40A | No |  |
| DC\_28A\_n41A | DC\_28A\_n41A | No |  |
| DC\_28A\_n50A | DC\_28A\_n50A | No |  |
| DC\_28A\_n66A | DC\_28A\_n66A | No |  |
| DC\_28A\_n77A7DC\_28A\_n77C7 | DC\_28A\_n77A | No | No |
| DC\_28A\_n77(2A)7 | DC\_28A\_n77A | No | No |
| DC\_28A\_n78A7DC\_28A\_n78C7 | DC\_28A\_n78A | No | No |
| DC\_28A\_n78(2A) | DC\_28A\_n78A | No | No |
| DC\_28A\_n79A7DC\_28A\_n79C7 | DC\_28A\_n79A | No |  |
| DC\_30A\_n2A | DC\_30A\_n2A | No |  |
| DC\_30A\_n5A | DC\_30A\_n5A | No |  |
| DC\_30A\_n66A | DC\_30A\_n66A | No |  |
| DC\_38A\_n78A7 | DC\_38A\_n78A | No |  |
| DC\_39A\_n40A3 | DC\_39A\_n40A | No |  |
| DC\_39A\_n41A3DC\_39C\_n41A3 | DC\_39A\_n41ADC\_39C\_n41A | No | No |
| DC\_39A\_n78A5,7 | DC\_39A\_n78A | No |  |
| DC\_39A\_n79A7DC\_39A\_n79C7 | DC\_39A\_n79A | No | No |
| DC\_40A\_n1ADC\_40C\_n1A | DC\_40A\_n1A | No |  |
| DC\_40A\_n41A3DC\_40C\_n41A3 | DC\_40A\_n41A | No |  |
| DC\_40A\_n77A | DC\_40A\_n77A | No |  |
| DC\_40A\_n78ADC\_40C\_n78A | DC\_40A\_n78ADC\_40C\_n78A | No |  |
| DC\_40A\_n79A7,12DC\_40A\_n79C7,12DC\_40C\_n79A7,12 | DC\_40A\_n79A | No | No |
| DC\_41A\_n3ADC\_41C\_n3A | DC\_41A\_n3ADC\_41C\_n3A | No |  |
| DC\_41A\_n28ADC\_41C\_n28A | DC\_41A\_n28ADC\_41C\_n28A | No |  |
| DC\_41A\_n77ADC\_41C\_n77A | DC\_41A\_n77ADC\_41C\_n77A | No |  |
| DC\_41A\_n77(2A)DC\_41C\_n77(2A) | DC\_41A\_n77ADC\_41C\_n77A | No |  |
| DC\_41A\_n78ADC\_41C\_n78ADC\_41D\_n78A | DC\_41A\_n78ADC\_41C\_n78A | No |  |
| DC\_41A\_n78(2A)DC\_41C\_n78(2A) | DC\_41A\_n78ADC\_41C\_n78A | No |  |
| DC\_41A\_n79A6,7DC\_41A\_n79C6,7DC\_41C\_n79A6,7 | DC\_41A\_n79ADC\_41C\_n79A | No | No |
| DC\_42A\_n1ADC\_42C\_n1A | DC\_42A\_n1A | No |  |
| DC\_42A\_n3ADC\_42C\_n3A | DC\_42A\_n3ADC\_42C\_n3A | DC\_42\_n3 |  |
| DC\_42A\_n28ADC\_42C\_n28A | DC\_42A\_n28ADC\_42C\_n28A | No |  |
| DC\_42A\_n51A | DC\_42A\_n51A | No |  |
| DC\_42A\_n77A3,4,9,11DC\_42A\_n77C3,4,9,11DC\_42C\_n77A3,4,9,11DC\_42C\_n77C3,4,9,11DC\_42D\_n77A3,4,9,11DC\_42D\_n77CDC\_42E\_n77A3,4,9,11DC\_42E\_n77C | N/A | N/A |  |
| DC\_42A\_n77(2A)3,4,9,11DC\_42C\_n77(2A)3,4,9,11 | N/A | N/A |  |
| DC\_42A\_n78A3,4,9,11DC\_42A\_n78C3,4,9,11DC\_42C\_n78A3,4,9,11DC\_42C\_n78C3,4,9,11DC\_42D\_n78A3,4,9,11DC\_42D\_n78CDC\_42E\_n78A3,4,9,11DC\_42E\_n78C | N/A | N/A |  |
| DC\_42A\_n79A9,15DC\_42A\_n79C9,15DC\_42C\_n79A9,15DC\_42C\_n79C9,15DC\_42D\_n79A9,15DC\_42D\_n79C9,15DC\_42E\_n79A9,15DC\_42E\_n79C9,15 | N/A | N/A |  |
| DC\_46A\_n78A2DC\_46C\_n78A2DC\_46D\_n78A2DC\_46E\_n78A2 | N/A | N/A |  |
| DC\_48A\_n5A | DC\_48A\_n5A | No |  |
| DC\_48A\_n12A | DC\_48A\_n12A | No |  |
| DC\_48A\_n46ADC\_48B\_n46ADC\_48C\_n46ADC\_48D\_n46ADC\_48E\_n46ADC\_48A\_n46BDC\_48B\_n46BDC\_48C\_n46BDC\_48D\_n46BDC\_48E\_n46BDC\_48A\_n46CDC\_48B\_n46CDC\_48C\_n46CDC\_48D\_n46CDC\_48E\_n46CDC\_48A\_n46DDC\_48B\_n46DDC\_48C\_n46DDC\_48D\_n46DDC\_48E\_n46DDC\_48A\_n46EDC\_48B\_n46EDC\_48C\_n46EDC\_48D\_n46EDC\_48E\_n46E | DC\_48A\_n46ADC\_48B\_n46A | No |  |
| DC\_48A\_n25ADC\_48C\_n25ADC\_48D\_n25A | DC\_48A\_n25A | No |  |
| DC\_48A\_n66ADC\_48C\_n66ADC\_48D\_n66A | DC\_48A\_n66A | No |  |
| DC\_48A\_n71ADC\_48B\_n71ADC\_48C\_n71ADC\_48D\_n71A | DC\_48A\_n71A | No |  |
| DC\_48A-48A\_n71ADC\_48A-48A-48A\_n71A | DC\_48A\_n71A | No |  |
| DC\_66A\_n2A | DC\_66A\_n2A | DC\_66\_n2 |  |
| DC\_66A-66A\_n2A | DC\_66A\_n2A | DC\_66\_n2 |  |
| DC\_66A\_n5ADC\_66B\_n5ADC\_66C\_n5A | DC\_66A\_n5A | DC\_66\_n5 |  |
| DC\_66A-66A\_n5ADC\_66A-66A-66A\_n5A | DC\_66A\_n5A | DC\_66\_n5 |  |
| DC\_66A\_n7ADC\_66A-66A\_n7ADC\_66A\_n7(2A)DC\_66A-66A\_n7(2A) | DC\_66A\_n7A | No |  |
| DC\_66A\_n12A | DC\_66A\_n12A | No |  |
| DC\_66A\_n25A | DC\_66A\_n25A | DC\_66\_n25 |  |
| DC\_66A\_n28A | DC\_66A\_n28A | No |  |
| DC\_66A\_n38A | DC\_66A\_n38A | No |  |
| DC\_66A-66A\_n38A | DC\_66A\_n38A | No |  |
| DC\_66A\_n41ADC\_66A\_n41C | DC\_66A\_n41A | No |  |
| DC\_66A\_n41(2A) | DC\_66A\_n41A | No |  |
| DC\_66A\_n46A | DC\_66A\_n46A | No |  |
| DC\_66A\_n48ADC\_66A\_n48B | DC\_66A\_n48A | No |  |
| DC\_66A-66A\_n48ADC\_66A-66A\_n48B | DC\_66A\_n48A | No |  |
| DC\_66A\_n71ADC\_66C\_n71ADC\_66A\_n71B | DC\_66A\_n71A | No |  |
| DC\_66A-66A\_n71A | DC\_66A\_n71A | No |  |
| DC\_66A\_n77A | DC\_66A\_n77A | DC\_66\_n77 |  |
| DC\_66A-66A\_n77ADC\_66A-66A-66A\_n77A | DC\_66A\_n77A | DC\_66\_n77 |  |
| DC\_66A\_n78A | DC\_66A\_n78A | No |  |
| DC\_66A\_n78(2A) | DC\_66A\_n78A | No |  |
| DC\_66A-66A\_n78A | DC\_66A\_n78A | No |  |
| DC\_66A-66A\_n78(2A) | DC\_66A\_n78A | No |  |
| DC\_71A\_n5A | DC\_71A\_n5A | No |  |
| DC\_71A\_n38A | DC\_71A\_n38A | No |  |
| DC\_71A\_n48A | DC\_71A\_n48A | No |  |
| DC\_71A\_n66A | DC\_71A\_n66A | No |  |
| DC\_71A\_n78A | DC\_71A\_n78A | No |  |
| NOTE 1: Uplink EN-DC configurations are the configurations supported by the present release of specifications.NOTE 2: Restricted to E-UTRA operation when inter-band carrier aggregation is configured. The downlink operating band for Band 46 is paired with the uplink operating band (external E-UTRA band) of the carrier aggregation configuration that is supporting the configured Pcell.NOTE 3: The minimum requirements apply only when there is non-simultaneous Tx/Rx operation between E-UTRA and NR carriers. This restriction applies also for these carriers when applicable EN-DC configuration is part of a higher order EN-DC configuration.NOTE 4: The minimum requirements for intra-band contiguous or non-contiguous EN-DC apply. The intra-band requirements also apply for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.NOTE 5: The frequency range above 3600 MHz for Band n78 is not used in this combination.NOTE 6: The frequency range below 2506 MHz for Band 41 is not used in this combination.NOTE 7: Applicable for UE supporting inter-band EN-DC with mandatory simultaneous Rx/Tx capability.NOTE 8: The frequency range in band n28 is restricted for this band combination to 703 - 733 MHz for the UL and 758-788 MHz for the DL.NOTE 9: The combination is not used alone as fall back mode of other band combinations in which UL in Band 42 is not used.NOTE 10: Void.NOTE 11: The minimum requirements for inter-band EN-DC apply when the maximum power spectral density imbalance between downlink carriers is within 6 dB. The power spectral density imbalance condition also applies for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.NOTE 12: Applicable for frequency range above 4800 MHz for Band n79 in this combination.NOTE 13: The minimum requirements apply for synchronized DL carriers with a maximum receive time difference ≤ 3 usec. The requirements also apply for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.NOTE 14: Applicable when dynamic switching between two uplink carriers is conducted. The DL interruption requirements for NR DL carrier(s) and E-UTRA DL carrier(s) are specified in clause 8.2.1.2.14 of 38.133 [15] and clause 7.32.2.12 of 36.133 [16] respectively.NOTE 15: Simultaneous Rx/Tx capability does not apply for UEs supporting band 42 with a n77 implementation only. Same restrictions are applied to related higher order configurations.NOTE 16: The frequency range in band n41 is restricted for this band combination to 2595 – 2645 MHz. |

---Unchanged sections are removed----------

#### 6.2B.1.3 Inter-band EN-DC within FR1

For inter-band EN-DC of E-UTRA and NR in FR1, the following UE Power Classes define the maximum output power for any transmission bandwidth within the aggregated channel bandwidth. The maximum output power is measured as the sum of the maximum output power at each UE antenna connector. The period of measurement shall be at least one sub frame (1ms). UE maximum output power shall be measured over all component carriers from different bands. If each band has separate antenna connectors, maximum output power is measured as the sum of maximum output power at each UE antenna connector.

Table 6.2B.1.3-1: Maximum output power for inter-band EN-DC (two bands)

| EN-DC configuration | Power class 2(dBm) | Tolerance(dB) | Power class 3(dBm) | Tolerance(dB) |
| --- | --- | --- | --- | --- |
| DC\_1A\_n3A |  |  | 23 | +2/-3 |
| DC\_1A\_n5A |  |  | 23 | +2/-3 |
| DC\_1A\_n7ADC\_1A\_n7B |  |  | 23 | +2/-3 |
| DC\_1A\_n8A |  |  | 23 | +2/-3 |
| DC\_1A\_n20A |  |  | 23 | +2/-3 |
| DC\_1A\_n28A |  |  | 23 | +2/-3 |
| DC\_1A\_n38A |  |  | 23 | +2/-3 |
| DC\_1A\_n40A |  |  | 23 | +2/-3 |
| DC\_1A\_n41A |  |  | 23 | +2/-3 |
| DC\_1A\_n50A |  |  | 23 | +2/-3 |
| DC\_1A\_n51A |  |  | 23 | +2/-3 |
| DC\_1A\_n71A |  |  | 23 | +2/-3 |
| DC\_1A\_n77ADC\_1A\_n84A\_ULSUP-TDM\_n77A |  |  | 23 | +2/-3 |
| DC\_1A\_n78ADC\_1A\_n84A\_ULSUP-TDM\_n78A | 266 | +2/-31 | 23 | +2/-3 |
| DC\_1A\_n79ADC\_1A\_n84A\_ULSUP-TDM\_n79A |  |  | 23 | +2/-3 |
| DC\_1A\_n80A |  |  | 23 | +2/-3 |
| DC\_2A\_n5A |  |  | 23 | +2/-31 |
| DC\_2A\_n7A |  |  | 23 | +2/-3 |
| DC\_2A\_n12A |  |  | 23 | +2/-3 |
| DC\_2A\_n28A |  |  | 23 | +2/-3 |
| DC\_2A\_n38A |  |  | 23 | +2/-3 |
| DC\_2A\_n41A |  |  | 23 | +2/-3 |
| DC\_2A\_n46A |  |  | 23 | +2/-3 |
| DC\_2A\_n48A |  |  | 23 | +2/-3 |
| DC\_2A\_n66A |  |  | 23 | +2/-31 |
| DC\_2A\_n71A |  |  | 23 | +2/-3 |
| DC\_2A\_n77A |  |  | 23 | +2/-3 |
| DC\_2A\_n78A |  |  | 23 | +2/-3 |
| DC\_3A\_n1A |  |  | 23 | +2/-3 |
| DC\_3A\_n5ADC\_3C\_n5A |  |  | 23 | +2/-3 |
| DC\_3A\_n7ADC\_3A\_n7BDC\_3C\_n7B |  |  | 23 | +2/-31 |
| DC\_3A\_n8A |  |  | 23 | +2/-3 |
| DC\_3A\_n20A |  |  | 23 | +2/-3 |
| DC\_3A\_n28A |  |  | 23 | +2/-31 |
| DC\_3A\_n34A |  |  | 23 | +2/-31 |
| DC\_3A\_n38A |  |  | 23 | +2/-3 |
| DC\_3A\_n40A |  |  | 23 | +2/-31 |
| DC\_3A\_n41A,DC\_3C\_n41A,DC\_3C\_n41A, | 266 | +2/-3 | 23 | +2/-3 |
| DC\_3A\_n50A |  |  | 23 | +2/-3 |
| DC\_3A\_n51A |  |  | 23 | +2/-31 |
| DC\_3A\_n71A |  |  | 23 | +2/-3 |
| DC\_3A\_n77ADC\_3C\_n77A |  |  | 23 | +2/-31 |
| DC\_3A\_n78ADC\_3C\_n78A | 266 | +2/-31 | 23 | +2/-31 |
| DC\_3A\_n79ADC\_3C\_n79A |  |  | 23 | +2/-31 |
| DC\_3A\_n80A\_ULSUP-TDM\_n41DC\_3C\_n80A\_ULSUP-TDM\_n41 |  |  | 23 | +2/-3 |
| DC\_3A\_n80A\_ULSUP-TDM\_n77A |  |  | 23 | +2/-31 |
| DC\_3A\_n80A\_ULSUP-TDM\_n78A |  |  | 23 | +2/-31 |
| DC\_3A\_n80A\_ULSUP-TDM\_n79A |  |  | 23 | +2/-31 |
| DC\_3A\_n82A |  |  | 23 | +2/-31 |
| DC\_3A\_n84A |  |  | 23 | +2/-31 |
| DC\_4A\_n2A |  |  | 23 | +2/-3 |
| DC\_4A\_n5A |  |  | 23 | +2/-3 |
| DC\_4A\_n7A |  |  | 23 | +2/-3 |
| DC\_4A\_n28A |  |  | 23 | +2/-3 |
| DC\_4A\_n38A |  |  | 23 | +2/-3 |
| DC\_4A\_n41A |  |  | 23 | +2/-3 |
| DC\_4A\_n78A |  |  | 23 | +2/-3 |
| DC\_5A\_n2A |  |  | 23 | +2/-3 |
| DC\_5A\_n7A |  |  | 23 | +2/-3 |
| DC\_5A\_n12A |  |  | 23 | +2/-3 |
| DC\_5A\_n38A |  |  | 23 | +2/-3 |
| DC\_5A\_n40A |  |  | 23 | +2/-31 |
| DC\_5A\_n48A |  |  | 23 | +2/-3 |
| DC\_5A\_n66A |  |  | 23 | +2/-31 |
| DC\_5A\_n71A |  |  | 23 | +2/-3 |
| DC\_5A\_n77A |  |  | 23 | +2/-3 |
| DC\_5A\_n78A |  |  | 23 | +2/-3 |
| DC\_5A\_n79A |  |  | 23 | +2/-3 |
| DC\_7A\_n1A |  |  | 23 | +2/-3 |
| DC\_7A\_n2A |  |  | 23 | +2/-3 |
| DC\_7A\_n3A |  |  | 23 | +2/-3 |
| DC\_7A\_n5ADC\_7C\_n5A |  |  | 23 | +2/-3 |
| DC\_7A\_n8A |  |  | 23 | +2/-3 |
| DC\_7A\_n20A |  |  | 23 | +2/-3 |
| DC\_7A\_n28A |  |  | 23 | +2/-31 |
| DC\_7A\_n40A |  |  | 23 | +2/-3 |
| DC\_7A\_n51A |  |  | 23 | +2/-31 |
| DC\_7A\_n66A |  |  | 23 | +2/-31 |
| DC\_7A\_n71A |  |  | 23 | +2/-3 |
| DC\_7A\_n77A |  |  | 23 | +2/-3 |
| DC\_7A\_n78ADC\_7C\_n78A |  |  | 23 | +2/-3 |
| DC\_7A\_n80A |  |  | 23 | +2/-3 |
| DC\_8A\_n1A |  |  | 23 | +2/-3 |
| DC\_8A\_n2A |  |  | 23 | +2/-3 |
| DC\_8A\_n3A |  |  | 23 | +2/-3 |
| DC\_8A\_n7A |  |  | 23 | +2/-3 |
| DC\_8A\_n20A |  |  | 23 | +2/-3 |
| DC\_8A\_n28A |  |  | 23 | +2/-3 |
| DC\_8A\_n34A |  |  | 23 | +2/-31 |
| DC\_8A\_n39A |  |  | 23 | +2/-3 |
| DC\_8A\_n40A |  |  | 23 | +2/-31 |
| DC\_8A\_n41A, |  |  | 23 | +2/-3 |
| DC\_8A\_n77A |  |  | 23 | +2/-3 |
| DC\_8A\_n78A | 266 | +2/-31 | 23 | +2/-3 |
| DC\_8A\_n79ADC\_8A\_n79C |  |  | 23 | +2/-3 |
| DC\_8A\_n80A |  |  | 23 | +2/-3 |
| DC\_8A\_n81A\_ULSUP-TDM\_n41 |  |  | 23 | +2/-3 |
| DC\_8A\_n81A\_ULSUP-TDM\_n78A |  |  | 23 | +2/-3 |
| DC\_8A\_n81A\_ULSUP-TDM\_n79A |  |  | 23 | +2/-3 |
| DC\_11A\_n3A |  |  | 23 | +2/-3 |
| DC\_11A\_n28A |  |  | 23 | +2/-3 |
| DC\_11A\_n77A |  |  | 23 | +2/-3 |
| DC\_11A\_n78A |  |  | 23 | +2/-3 |
| DC\_11A\_n79A |  |  | 23 | +2/-3 |
| DC\_12A\_n2A |  |  | 23 | +2/-3 |
| DC\_12A\_n5A |  |  | 23 | +2/-3 |
| DC\_12A\_n7A |  |  | 23 | +2/-3 |
| DC\_12A\_n25A |  |  | 23 | +2/-3 |
| DC\_12A\_n38A |  |  | 23 | +2/-3 |
| DC\_12A\_n41A |  |  | 23 | +2/-3 |
| DC\_12A\_n66A |  |  | 23 | +2/-3 |
| DC\_12A\_n78A |  |  | 23 | +2/-3 |
| DC\_13A\_n2A |  |  | 23 | +2/-3 |
| DC\_13A\_n5A |  |  | 23 | +2/-3 |
| DC\_13A\_n7A |  |  | 23 | +2/-3 |
| DC\_13A\_n48A |  |  | 23 | +2/-3 |
| DC\_13A\_n66A |  |  | 23 | +2/-3 |
| DC\_13A\_n71A |  |  | 23 | +2/-3 |
| DC\_13A\_n77A |  |  | 23 | +2/-3 |
| DC\_13A\_n78A |  |  | 23 | +2/-3 |
| DC\_14A\_n2A |  |  | 23 | +2/-3 |
| DC\_14A\_n66A |  |  | 23 | +2/-3 |
| DC\_18A\_n3A |  |  | 23 | +2/-3 |
| DC\_18A\_n28A |  |  | 23 | +2/-3 |
| DC\_18A\_n41A |  |  | 23 | +2/-3 |
| DC\_18A\_n77A |  |  | 23 | +2/-3 |
| DC\_18A\_n78A |  |  | 23 | +2/-3 |
| DC\_18A\_n79A |  |  | 23 | +2/-3 |
| DC\_19A\_n1A |  |  | 23 | +2/-3 |
| DC\_19A\_n77A |  |  | 23 | +2/-3 |
| DC\_19A\_n78A |  |  | 23 | +2/-3 |
| DC\_19A\_n79A |  |  | 23 | +2/-3 |
| DC\_20A\_n1A |  |  | 23 | +2/-3 |
| DC\_20A\_n3A |  |  | 23 | +2/-3 |
| DC\_20A\_n7A |  |  | 23 | +2/-3 |
| DC\_20A\_n8A |  |  | 23 | +2/-3 |
| DC\_20A\_n38A |  |  | 23 | +2/-3 |
| DC\_20A\_n28A |  |  | 23 | +2/-3 |
| DC\_20A\_n41A |  |  | 23 | +2/-3 |
| DC\_20A\_n50A |  |  | 23 | +2/-3 |
| DC\_20A\_n51A |  |  | 23 | +2/-3 |
| DC\_20A\_n77A |  |  | 23 | +2/-3 |
| DC\_20A\_n80A |  |  | 23 | +2/-3 |
| DC\_20A\_n78A |  |  | 23 | +2/-3 |
| DC\_20A\_n82A\_ULSUP-TDM\_n78A |  |  | 23 | +2/-3 |
| DC\_20A\_n83A |  |  | 23 | +2/-3 |
| DC\_21A\_n1A |  |  | 23 | +2/-3 |
| DC\_21A\_n77A |  |  | 23 | +2/-3 |
| DC\_21A\_n78A |  |  | 23 | +2/-3 |
| DC\_21A\_n79A |  |  | 23 | +2/-3 |
| DC\_25A\_n41A |  |  | 23 | +2/-3 |
| DC\_26A\_n25A |  |  | 23 | +2/-3 |
| DC\_26A\_n41A |  |  | 23 | +2/-3 |
| DC\_26A\_n77A |  |  | 23 | +2/-3 |
| DC\_26A\_n78A |  |  | 23 | +2/-3 |
| DC\_26A\_n79A |  |  | 23 | +2/-3 |
| DC\_28A\_n1A |  |  | 23 | +2/-3 |
| DC\_28A\_n2A |  |  | 23 | +2/-3 |
| DC\_28A\_n3A |  |  | 23 | +2/-3 |
| DC\_28A\_n5A |  |  | 23 | +2/-3 |
| DC\_28A\_n7ADC\_28A\_n7B |  |  | 23 | +2/-3 |
| DC\_28A\_n8A |  |  | 23 | +2/-3 |
| DC\_28A\_n40A |  |  | 23 | +2/-3 |
| DC\_28A\_n41A |  |  | 23 | +2/-3 |
| DC\_28A\_n50A |  |  | 23 | +2/-3 |
| DC\_28A\_n51A |  |  | 23 | +2/-3 |
| DC\_28A\_n66A |  |  | 23 | +2/-3 |
| DC\_28A\_n77A |  |  | 23 | +2/-3 |
| DC\_28A\_n78A |  |  | 23 | +2/-3 |
| DC\_28A\_n79A |  |  | 23 | +2/-3 |
| DC\_28A\_n83A\_ULSUP-TDM\_n41A |  |  | 23 | +2/-3 |
| DC\_28A\_n83A\_ULSUP-TDM\_n78A |  |  | 23 | +2/-3 |
| DC\_30A\_n2A |  |  | 23 | +2/-3 |
| DC\_30A\_n5A |  |  | 23 | +2/-3 |
| DC\_30A\_n66A |  |  | 23 | +2/-3 |
| DC\_38A\_n78A |  |  | N/A | N/A |
| DC\_39A\_n40A |  |  | 23 | +2/-3 |
| DC\_39A\_n41ADC\_39C\_n41A | 265 | +2/-31 | 23 | +2/-3 |
| DC\_39A\_n78A |  |  | 23 | +2/-31 |
| DC\_39A\_n79A | 265 | +2/-31 | 23 | +2/-31 |
| DC\_40A\_n1A |  |  | 23 | +2/-3 |
| DC\_40A\_n41ADC\_40C\_n41A |  |  | 23 | +2/-3 |
| DC\_40A\_n77A |  |  | N/A | N/A |
| DC\_40A\_n78A |  |  | 23 | +2/-3 |
| DC\_40C\_n78A |  |  | 23 | +2/-3 |
| DC\_40A\_n79A |  |  | 23 | +2/-3 |
| DC\_41A\_n3ADC\_41C\_n3A |  |  | 23 | +2/-3 |
| DC\_41A\_n28ADC\_41C\_n28A |  |  | 23 | +2/-3 |
| DC\_41A\_n77ADC\_41C\_n77A |  |  | 23 | +2/-31 |
| DC\_41A\_n78ADC\_41C\_n78A |  |  | 23 | +2/-31 |
| DC\_41A\_n79ADC\_41C\_n79A | 265 | +2/-31 | 23 | +2/-31 |
| DC\_42A\_n1A |  |  | 23 | +2/-3 |
| DC\_42A\_n3ADC\_42C\_n3A |  |  | 23 | +2/-3 |
| DC\_42A\_n28ADC\_42C\_n28A |  |  | 23 | +2/-3 |
| DC\_42A\_n51A |  |  | 23 | +2/-3 |
| DC\_42A\_n77A |  |  | N/A | N/A |
| DC\_42A\_n78A |  |  | N/A | N/A |
| DC\_42A\_n79A |  |  | N/A | N/A |
| DC\_48A\_n5A |  |  | 23 | +2/-3 |
| DC\_48A\_n12A |  |  | 23 | +2/-3 |
| DC\_48A\_n25A |  |  | 23 | +2/-3 |
| DC\_2A\_n46A |  |  | 23 | +2/-3 |
| DC\_48A\_n66A |  |  | 23 | +2/-3 |
| DC\_48A\_n71A |  |  | 23 | +2/-3 |
| DC\_66A\_n2A |  |  | 23 | +2/-3 |
| DC\_66A\_n5A |  |  | 23 | +2/-31 |
| DC\_66A\_n7A |  |  | 23 | +2/-3 |
| DC\_66A\_n12A |  |  | 23 | +2/-3 |
| DC\_66A\_n25A |  |  | 23 | +2/-3 |
| DC\_66A\_n28A |  |  | 23 | +2/-3 |
| DC\_66A\_n38A |  |  | 23 | +2/-3 |
| DC\_66A\_n41A |  |  | 23 | +2/-3 |
| DC\_66A\_n46A |  |  | 23 | +2/-3 |
| DC\_66A\_n48A |  |  | 23 | +2/-3 |
| DC\_66A\_n71A |  |  | 23 | +2/-3 |
| DC\_66A\_n77A |  |  | 23 | +2/-3 |
| DC\_66A\_n78ADC\_66A-66A\_n78A |  |  | 23 | +2/-3 |
| DC\_66A\_n86A\_ULSUP-TDM\_n78A |  |  | 23 | +2/-3 |
| DC\_71A\_n5A |  |  | 23 | +2/-3 |
| DC\_71A\_n38A |  |  | 23 | +2/-3 |
| DC\_71A\_n48A |  |  | 23 | +2/-3 |
| DC\_71A\_n66A |  |  | 23 | +2/-3 |
| DC\_71A\_n78A |  |  | 23 | +2/-3 |
| NOTE 1: For the transmission bandwidths confined within FUL\_low and FUL\_low + 4 MHz or FUL\_high – 4 MHz and FUL\_high, the maximum output power requirement is relaxed by reducing the lower tolerance limit by 1.5 dBNOTE 2: PPowerClass, EN-DC is the maximum UE power specified without taking into account the toleranceNOTE 3: For inter-band EN-DC the maximum power requirement should apply to the total transmitted power over all component carriers (per UE).NOTE 4: Power Class 3 is the default power class unless otherwise stated.NOTE 5: The UE is not required to support PC2 within each individual cell group. Power class support within each individual cell group is signaled separately by the UE.NOTE 6: The UE supports PC3 within E-UTRA cell group, and supports either PC3 or PC2 within NR cell group. Power class support within each individual cell group is signaled separately by the UE. |

If a UE supports a different power class than the default UE power class for an E-UTRA TDD and NR TDD Inter-band EN-DC band combination and the supported power class enables higher maximum output power than that of the default power class:

– if the field of UE capability *maxUplinkDutyCycle-interBandENDC-TDD-PC2-r16* is absent and the percentage of NR uplink symbols transmitted in a certain evaluation period is larger than 30% (The exact evaluation period is no less than one radio frame); or

– if the field of UE capability *maxUplinkDutyCycle-interBandENDC-TDD-PC2-r16* is not absent and the percentage of NR uplink symbols transmitted in a certain evaluation period is larger than *maxUplinkDutyCycle-interBandENDC-TDD-PC2-r16* as defined in TS38.331 (The exact evaluation period is no less than one radio frame); or

– if the IE *p-maxUE-FR1* as defined in TS 38.331 is provided and set to the maximum output power of the default power class or lower;

– shall apply all requirements for the default power class to the supported power class and set the configured transmitted power as specified sub-clause 6.2B.4;

– Else if the IE *p-maxUE-FR1* as defined in TS 38.331 is not provided or set to the higher value than the maximum output power of the default power class and the percentage of NR uplink symbols transmitted in a certain evaluation period is less than or equal t*o maxUplinkDutyCycle-interBandENDC-TDD-PC2-r16* as defined in TS 38.331; or

– if the IE *p-maxUE-FR1* as defined in TS 38.331 is not provided or set to the higher value than the maximum output power of the default power class and the percentage of NR uplink symbols transmitted in a certain evaluation period is less than or equal to 30% when *maxUplinkDutyCycle-interBandENDC-TDD-PC2-r16* is absent. (The exact evaluation period is no less than one radio frame):

– shall apply all requirements for the supported power class and set the configured transmitted power class as specified in sub-clause 6.2B.4.

If a UE supports a different power class than the default UE power class for an E-UTRA FDD and NR TDD EN-DC band combination and the supported power class enables higher maximum output power than that of the default power class:

If UE indicating the two capabilities maxUplinkDutyCycle-EN-DC\_FDDTDD\_1 and maxUplinkDutyCycle-EN-DC\_FDDTDD\_2:

– if the IE *p-maxUE-FR1* as defined in TS 38.331 is not provided or set to the higher value than the maximum output power of the default power class, and the percentage of EUTRA uplink symbols transmitted in a certain evaluation period is between 40% and 70%, and the percentage of NR uplink symbols transmitted in a certain evaluation period is less than or equal t*o maxUplinkDutyCycle-EN-DC\_FDDTDD\_1* as defined in TS 38.331 (The exact evaluation period is no less than one radio frame); or

– if the IE *p-maxUE-FR1* as defined in TS 38.331 is not provided or set to the higher value than the maximum output power of the default power class, and the percentage of EUTRA uplink symbols transmitted in a certain evaluation period is no larger than 40%, and the percentage of NR uplink symbols transmitted in a certain evaluation period is less than or equal t*o maxUplinkDutyCycle-EN-DC\_FDDTDD\_2* as defined in TS 38.331 (The exact evaluation period is no less than one radio frame)

– shall apply all requirements for the supported power class and set the configured transmitted power class as specified in sub-clause 6.2B.4.

– else

– shall apply all requirements for the default power class and set the configured transmitted power as specified sub-clause 6.2B.4;

else

– shall apply all requirements for the supported power class and set the configured transmitted power as specified sub-clause 6.2B.4;

#### 6.2B.1.3a Inter-band NE-DC within FR1

For inter-band NE-DC of E-UTRA and NR in FR1, the following UE power classes define the maximum output power for any transmission bandwidth within the aggregated channel bandwidth. The maximum output power is measured as the sum of the maximum output power at each UE antenna connector. The period of measurement shall be at least one sub frame (1 ms). UE maximum output power shall be measured over all component carriers from different bands. If each band has separate antenna connectors, maximum output power is measured as the sum of maximum output power at each UE antenna connector.

Table 6.2B.1.3a-1: Maximum output power for inter-band NE-DC (two bands)

| NE-DC configuration | Power class 3(dBm) | Tolerance(dB) |
| --- | --- | --- |
| DC\_n1A\_28A | 23 | +2/-3 |
| DC\_n78A\_1A | 23 | +2/-3 |
| DC\_n78A\_3A | 23 | +2/-3 |
| DC\_n78A\_5A | 23 | +2/-3 |
| DC\_n78A\_7A | 23 | +2/-3 |
| DC\_n78A\_8A | 23 | +2/-3 |
| DC\_n78A\_26A | 23 | +2/-3 |

-------End of changes---