**3GPP TSG-RAN WG4 Meeting #97-eR4-2016819**

**Electronic Meeting, 2-13 Nov., 2020**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| *CR-Form-v12.1* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.101-3** | **CR** | **0386** | **rev** | **1** | **Current version:** | **16.5.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | |
| ***Title:*** | CR to 38.101-3: Add requirement on the inter-band EN-DC with no DL interruption | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | China Telecom | | | | | | | | | |
| ***Source to TSG:*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_RF\_FR1-Core | | | | |  | ***Date:*** | | | 2020-11-10 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-16 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-15 (Release 15) Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | In RAN4 #96e, it was agreed in WF R4-2011731 that DL interruption is not allowed for some inter-band EN-DC and UL CA configurations. The exact EN-DC and UL CA configurations for which DL interruptions are not allowed will be captured in TS 38.101-1 and TS 38.101-3 respectively | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | Indicate that for some EN-DC configurations, DL interruption is not allowed. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | The specification is not clear. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | 5.5B.4.1 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | | TS/TR ... CR ... | | |
| ***affected:*** | | **X** |  | Test specifications | | | | TS 38.521-3 | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | | TS/TR ... CR ... | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

## <Start of Changes>

# 2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non‑specific.

- For a specific reference, subsequent revisions do not apply.

- For a non-specific reference, the latest version applies. In the case of a reference to a 3GPP document (including a GSM document), a non-specific reference implicitly refers to the latest version of that document *in the same Release as the present document*.

[1] 3GPP TR 21.905: "Vocabulary for 3GPP Specifications".

[2] 3GPP TS 38.101-1: "NR; User Equipment (UE) radio transmission and reception; Part 1: Range 1 Standalone"

[3] 3GPP TS 38.101-2: "NR; User Equipment (UE) radio transmission and reception; Part 2: Range 2 Standalone"

[4] 3GPP TS 36.101: "Evolved Universal Terrestrial Radio Access (E-UTRA); User Equipment (UE) radio transmission and reception"

[5] 3GPP TS 38.521-3: "NR; User Equipment (UE) conformance specification; Radio transmission and reception; Part 3: Range 1 and Range 2 Interworking operation with other radios"

[6] Recommendation ITU-R M.1545: "Measurement uncertainty as it applies to test limits for the terrestrial component of International Mobile Telecommunications-2000"

[7] 3GPP TS 36.211: "E-UTRA; Physical channels and modulation"

[8] 3GPP TS 36.331: " Evolved Universal Terrestrial Radio Access (E-UTRA); Radio Resource Control (RRC); Protocol specification"

[9] 3GPP TS 38.331: "NR; Radio Resource Control (RRC) protocol specification"

[10] 3GPP TS 38.213: "NR; Physical layer procedures for control"

[11] 3GPP TS 38.306: "NR; User Equipment (UE) radio access capabilities"

[12] 3GPP TS 38.133: "NR; Requirements for support of radio resource management"

[13] 3GPP TS 38.211: "NR; Physical channels and modulation".

[14] 3GPP TS 38.214: "NR; Physical layer procedures for data"

[15] 3GPP TS 38.133: "NR; Requirements for support of radio resource management"

[16] 3GPP TS 36.133: "Evolved Universal Terrestrial Radio Access (E-UTRA); Requirements for support of radio resource management"

## <Next Change>

#### 5.5B.4.1 Inter-band EN-DC configurations within FR1 (two bands)

Table 5.5B.4.1-1: Inter-band EN-DC configurations within FR1 (two bands)

| EN-DC  configuration | Uplink EN-DC  configuration  (NOTE 1) | Single UL allowed | DL interruption allowed  (Note 14) |
| --- | --- | --- | --- |
| DC\_1A\_n3A  DC\_1C\_n3A | DC\_1A\_n3A  DC\_1C\_n3A | DC\_1\_n3 | N/A |
| DC\_1A\_n5A | DC\_1A\_n5A | No | N/A |
| DC\_1A\_n7A  DC\_1A\_n7B | DC\_1A\_n7A | No | N/A |
| DC\_1A-1A\_n7A  DC\_1A-1A\_n7B | DC\_1A\_n7A | No | N/A |
| DC\_1A\_n8A | DC\_1A\_n8A | No | N/A |
| DC\_1A\_n20A | DC\_1A\_n20A | No | N/A |
| DC\_1A\_n28A | DC\_1A\_n28A | No | N/A |
| DC\_1A\_n38A  DC\_1C\_n38A | DC\_1A\_n38A | No | N/A |
| DC\_1A\_n40A | DC\_1A\_n40A | No | N/A |
| DC\_1A\_n41A | DC\_1A\_n41A | No | N/A |
| DC\_1A\_n50A | DC\_1A\_n50A | No | N/A |
| DC\_1A\_n51A | DC\_1A\_n51A | No | N/A |
| DC\_1A\_n71A  DC\_1A\_n71B | DC\_1A\_n71A | No | N/A |
| DC\_1A\_n77A7  DC\_1A\_n77C7 | DC\_1A\_n77A | DC\_1\_n77 | No |
| DC\_1A\_n77(2A) | DC\_1A\_n77A | DC\_1\_n77 | No |
| DC\_1A\_n78A7  DC\_1A\_n78C7 | DC\_1A\_n78A | No | No |
| DC\_1A\_n78(2A)7 | DC\_1A\_n78A | No | No |
| DC\_1A\_n79A7  DC\_1A\_n79C7 | DC\_1A\_n79A | No | No |
| DC\_2A\_n5A | DC\_2A\_n5A | No | N/A |
| DC\_2A-2A\_n5A | DC\_2A\_n5A | No | N/A |
| DC\_2A\_n7A | DC\_2A\_n7A | No | N/A |
| DC\_2A\_n7(2A) | DC\_2A\_n7A | No | N/A |
| DC\_2A\_n12A | DC\_2A\_n12A | No | N/A |
| DC\_2A\_n38A | DC\_2A\_n38A | No | N/A |
| DC\_2A-2A\_n38A | DC\_2A\_n38A | No | N/A |
| DC\_2A\_n41A  DC\_2A\_n41C  DC\_2C\_n41A | DC\_2A\_n41A  DC\_2C\_n41A | No | N/A |
| DC\_2A-2A\_n41A  DC\_2A\_n41(2A) | DC\_2A\_n41A | No | N/A |
| DC\_2A\_n48A  DC\_2A\_n48B | DC\_2A\_n48A | No | N/A |
| DC\_2A\_n66A | DC\_2A\_n66A | DC\_2\_n66 | N/A |
| DC\_2A-2A\_n66A | DC\_2A\_n66A | DC\_2\_n66 | N/A |
| DC\_2A\_n71A  DC\_2A\_n71B  DC\_2C\_n71A | DC\_2A\_n71A  DC\_2C\_n71A | No | N/A |
| DC\_2A-2A\_n71A | DC\_2A\_n71A | No | N/A |
| DC\_2A\_n78A | DC\_2A\_n78A | DC\_2\_n78 | N/A |
| DC\_2A\_n78(2A) | DC\_2A\_n78A | DC\_2\_n78 | N/A |
| DC\_2A-2A\_n78A | DC\_2A\_n78A | DC\_2\_n78 | N/A |
| DC\_3A\_n1A  DC\_3C\_n1A | DC\_3A\_n1A  DC\_3C\_n1A | DC\_3\_n1 | N/A |
| DC\_3A-3A\_n1A | DC\_3A\_n1A | DC\_3\_n1 | N/A |
| DC\_3A\_n5A  DC\_3C\_n5A | DC\_3A\_n5A  DC\_3C\_n5A | DC\_3\_n5 | N/A |
| DC\_3A\_n7A  DC\_3A\_n7B  DC\_3C\_n7A  DC\_3C\_n7B | DC\_3A\_n7A  DC\_3A\_n7B  DC\_3C\_n7A | No | N/A |
| DC\_3A-3A\_n7A  DC\_3A-3A\_n7B | DC\_3A\_n7A | No | N/A |
| DC\_3A\_n8A | DC\_3A\_n8A | No | N/A |
| DC\_3A\_n20A | DC\_3A\_n20A | No | N/A |
| DC\_3A\_n28A  DC\_3C\_n28A | DC\_3A\_n28A  DC\_3C\_n28A | No | N/A |
| DC\_3A\_n34A | DC\_3A\_n34A | No | N/A |
| DC\_3A\_n38A  DC\_3C\_n38A | DC\_3A\_n38A | No | N/A |
| DC\_3A\_n40A | DC\_3A\_n40A | No | N/A |
| DC\_3A\_n41A  DC\_3C\_n41A | DC\_3A\_n41A  DC\_3C\_n41A | DC\_3\_n41 | No |
| DC\_3A\_n50A | DC\_3A\_n50A | No | N/A |
| DC\_3A\_n51A | DC\_3A\_n51A | No | N/A |
| DC\_3A\_n71A  DC\_3A\_n71B | DC\_3A\_n71A | No | N/A |
| DC\_3A\_n77A7  DC\_3A\_n77C7 | DC\_3A\_n77A | DC\_3\_n77 | No |
| DC\_3A\_n77(2A)7 | DC\_3A\_n77A | DC\_3\_n77 | No |
| DC\_3A-3A\_n77A | DC\_3A\_n77A | DC\_3\_n77 | No |
| DC\_3A\_n78A7  DC\_3A\_n78C7  DC\_3C\_n78A7 | DC\_3A\_n78A | DC\_3\_n78 | No |
| DC\_3A\_n78(2A)7  DC\_3C\_n78(2A)7 | DC\_3A\_n78A | DC\_3\_n78 | No |
| DC\_3A-3A\_n78A | DC\_3A\_n78A | DC\_3\_n78 | No |
| DC\_3A\_n79A7  DC\_3A\_n79C7  DC\_3C\_n79A7 | DC\_3A\_n79A  DC\_3C\_n79A | No | No |
| DC\_4A\_n38A | DC\_4A\_n38A | No | N/A |
| DC\_4A\_n41A | DC\_4A\_n41A | No | N/A |
| DC\_4A\_n78A | DC\_4A\_n78A | No | N/A |
| DC\_4A\_n78(2A) | DC\_4A\_n78A | No | N/A |
| DC\_5A\_n2A  DC\_5B\_n2A | DC\_5A\_n2A | No | N/A |
| DC\_5A-5A\_n2A | DC\_5A\_n2A | No | N/A |
| DC\_5A\_n7A | DC\_5A\_n7A | DC\_5\_n7 | N/A |
| DC\_5A\_n7(2A) | DC\_5A\_n7A | DC\_5\_n7 | N/A |
| DC\_5A\_n12A | DC\_5A\_n12A | No | N/A |
| DC\_5A\_n38A | DC\_5A\_n38A | DC\_5\_n38 | N/A |
| DC\_5A\_n40A | DC\_5A\_n40A | No | N/A |
| DC\_5A\_n48A  DC\_5A\_n48B | DC\_5A\_n48A | No | N/A |
| DC\_5A\_n66A  DC\_5B\_n66A | DC\_5A\_n66A | DC\_5\_n66 | N/A |
| DC\_5A-5A\_n66A | DC\_5A\_n66A | DC\_5\_n66 | N/A |
| DC\_5A\_n71A | DC\_5A\_n71A | No | N/A |
| DC\_5A\_n78A7 | DC\_5A\_n78A | No | No |
| DC\_5A\_n78(2A)7 | DC\_5A\_n78A | No | No |
| DC\_5A\_n79A | DC\_5A\_n79A | No | No |
| DC\_7A\_n1A  DC\_7C\_n1A | DC\_7A\_n1A  DC\_7C\_n1A | No | N/A |
| DC\_7A-7A\_n1A | DC\_7A\_n1A | No | N/A |
| DC\_7A\_n3A  DC\_7C\_n3A | DC\_7A\_n3A  DC\_7C\_n3A | No | N/A |
| DC\_7A\_n5A  DC\_7C\_n5A | DC\_7A\_n5A  DC\_7C\_n5A | DC\_7\_n5 | N/A |
| DC\_7A-7A\_n5A | DC\_7A\_n5A | DC\_7\_n5 | N/A |
| DC\_7A\_n8A | DC\_7A\_n8A | No | N/A |
| DC\_7A-7A\_n78A7 | DC\_7A\_n78A | No | N/A |
| DC\_7A-7A\_n78(2A)7 | DC\_7A\_n78A | No | N/A |
| DC\_7A\_n20A | DC\_7A\_n20A | No | N/A |
| DC\_7A\_n28A  DC\_7C\_n28A | DC\_7A\_n28A  DC\_7C\_n28A | No | N/A |
| DC\_7A\_n40A | DC\_7A\_n40A | Yes | N/A |
| DC\_7A\_n51A | DC\_7A\_n51A | No | N/A |
| DC\_7A\_n66A  DC\_7C\_n66A | DC\_7A\_n66A | No | N/A |
| DC\_7A-7A\_n66A | DC\_7A\_n66A | No | N/A |
| DC\_7A\_n71A | DC\_7A\_n71A | No | N/A |
| DC\_7A\_n77A | DC\_7A\_n77A | No | N/A |
| DC\_7A-7A\_n77A | DC\_7A\_n77A | No | N/A |
| DC\_7A\_n78A7  DC\_7C\_n78A7 | DC\_7A\_n78A  DC\_7C\_n78A | No | N/A |
| DC\_7A\_n78(2A)7  DC\_7C\_n78(2A)7 | DC\_7A\_n78A  DC\_7C\_n78A | No | N/A |
| DC\_8A\_n1A | DC\_8A\_n1A | No | N/A |
| DC\_8A\_n3A | DC\_8A\_n3A | No | N/A |
| DC\_8A\_n20A | DC\_8A\_n20A | Yes | N/A |
| DC\_8A\_n28A | DC\_8A\_n28A | No | N/A |
| DC\_8A\_n34A | DC\_8A\_n34A | No | N/A |
| DC\_8A\_n39A | DC\_8A\_n39A | No | N/A |
| DC\_8A\_n40A7 | DC\_8A\_n40A | No | N/A |
| DC\_8A\_n41A  DC\_8A\_n41C | DC\_8A\_n41A | No | No |
| DC\_8A\_n41(2A) | DC\_8A\_n41A | No | No |
| DC\_8A\_n77A7 | DC\_8A\_n77A | No | No |
| DC\_8A\_n77(2A)7 | DC\_8A\_n77A | No | No |
| DC\_8A\_n78A7 | DC\_8A\_n78A | No | No |
| DC\_8A\_n79A7  DC\_8A\_n79C | DC\_8A\_n79A  DC\_8A\_n79C | No | No |
| DC\_8A\_n93A | DC\_8A\_n93A\_ULSUP-TDM | N/A | N/A |
| DC\_8A\_n94A | DC\_8A\_n94A\_ULSUP-TDM | N/A | N/A |
| DC\_11A\_n3A | DC\_11A\_n3A | No | N/A |
| DC\_11A\_n28A | DC\_11A\_n28A | No | N/A |
| DC\_11A\_n77A7 | DC\_11A\_n77A | No | No |
| DC\_11A\_n77(2A)7 | DC\_11A\_n77A | No | No |
| DC\_11A\_n78A7 | DC\_11A\_n78A | No | No |
| DC\_11A\_n79A7 | DC\_11A\_n79A | No | N/A |
| DC\_12A\_n2A | DC\_12A\_n2A | No | N/A |
| DC\_12A\_n5A | DC\_12A\_n5A | No | N/A |
| DC\_12A\_n7A  DC\_12A\_n7(2A) | DC\_12A\_n7A | No | N/A |
| DC\_12A\_n25A | DC\_12A\_n25A | No | N/A |
| DC\_12A\_n38A | DC\_12A\_n38A | No | N/A |
| DC\_12A\_n41A | DC\_12A\_n41A | No | N/A |
| DC\_12A\_n66A | DC\_12A\_n66A | No | N/A |
| DC\_12A\_n78A  DC\_12A\_n78(2A) | DC\_12A\_n78A | DC\_12\_n78 | N/A |
| **DC\_13A\_n2A** | DC\_13A\_n2A | No | N/A |
| DC\_13A\_n5A | DC\_13A\_n5A | DC\_13\_n5 | N/A |
| DC\_13A\_n7A  DC\_13A\_n7(2A) | DC\_13A\_n7A | No | N/A |
| DC\_13A\_n48A  DC\_13A\_n48B | DC\_13A\_n48A | No | N/A |
| DC\_13A\_n66A | DC\_13A\_n66A | No | N/A |
| DC\_13A\_n71A | DC\_13A\_n71A | No | N/A |
| DC\_13A\_n78A  DC\_13A\_n78(2A) | DC\_13A\_n78A | No | N/A |
| DC\_14A\_n2A | DC\_14A\_n2A | No | N/A |
| DC\_14A\_n66A | DC\_14A\_n66A | No | N/A |
| DC\_18A\_n3A | DC\_18A\_n3A | No | N/A |
| DC\_18A\_n77A7 | DC\_18A\_n77A | No | No |
| DC\_18A\_n78A7 | DC\_18A\_n78A | No | No |
| DC\_20A\_n91A | DC\_20A\_n91A\_ULSUP-TDM | N/A | N/A |
| DC\_20A\_n92A | DC\_20A\_n92A\_ULSUP-TDM | N/A | N/A |
| DC\_18A\_n79A7 | DC\_18A\_n79A | No | N/A |
| DC\_19A\_n77A7  DC\_19A\_n77C7 | DC\_19A\_n77A | No | N/A |
| DC\_19A\_n78A7  DC\_19A\_n78C7 | DC\_19A\_n78A | No | No |
| DC\_19A\_n79A7  DC\_19A\_n79C7 | DC\_19A\_n79A | No | No |
| DC\_20A\_n1A | DC\_20A\_n1A | No | N/A |
| DC\_20A\_n3A | DC\_20A\_n3A | No | N/A |
| DC\_20A\_n7A | DC\_20A\_n7A | DC\_20\_n7 | N/A |
| DC\_20A\_n8A | DC\_20A\_n8A | DC\_20\_n8 | N/A |
| DC\_20A\_n28A8,10,11,13 | DC\_20A\_n28A | No | N/A |
| DC\_20A\_n38A | DC\_20A\_n38A | No | N/A |
| DC\_20A\_n41A | DC\_20A\_n41A | DC\_20\_n41 | N/A |
| DC\_20A\_n50A | DC\_20A\_n50A | No | N/A |
| DC\_20A\_n51A | DC\_20A\_n51A | No | N/A |
| DC\_20A\_n77A7 | DC\_20A\_n77A | No | N/A |
| DC\_20A\_n78A7 | DC\_20A\_n78A | No | N/A |
| DC\_20A\_n78(2A)7 | DC\_20A\_n78A | No | N/A |
| DC\_21A\_n77A7  DC\_21A\_n77C7 | DC\_21A\_n77A | No | N/A |
| DC\_21A\_n78A7  DC\_21A\_n78C7 | DC\_21A\_n78A | No | No |
| DC\_21A\_n79A7  DC\_21A\_n79C7 | DC\_21A\_n79A | No | No |
| DC\_25A\_n41A | DC\_25A\_n41A | No | N/A |
| DC\_25A-25A\_n41A | DC\_25A\_n41A | No | N/A |
| DC\_26A\_n25A | DC\_26A\_n25A | No | N/A |
| DC\_26A\_n41A | DC\_26A\_n41A | No | N/A |
| DC\_26A\_n77A7 | DC\_26A\_n77A | No | N/A |
| DC\_26A\_n78A7 | DC\_26A\_n78A | No | N/A |
| DC\_26A\_n79A7 | DC\_26A\_n79A | No | N/A |
| DC\_28A\_n3A | DC\_28A\_n3A | No | N/A |
| DC\_28A\_n5A8 | DC\_28A\_n5A | No | N/A |
| DC\_28A\_n7A  DC\_28A\_n7B | DC\_28A\_n7A  DC\_28A\_n7B | No | N/A |
| DC\_28A\_n51A | DC\_28A\_n51A | No | N/A |
| DC\_28A\_n8A | DC\_28A\_n8A | No | N/A |
| DC\_28A\_n40A | DC\_28A\_n40A | No | N/A |
| DC\_28A\_n41A | DC\_28A\_n41A | No | N/A |
| DC\_28A\_n50A | DC\_28A\_n50A | No | N/A |
| DC\_28A\_n77A7  DC\_28A\_n77C7 | DC\_28A\_n77A | No | No |
| DC\_28A\_n77(2A)7 | DC\_28A\_n77A | No | No |
| DC\_28A\_n78A7  DC\_28A\_n78C7 | DC\_28A\_n78A | No | No |
| DC\_28A\_n78(2A) | DC\_28A\_n78A | No | No |
| DC\_28A\_n79A7  DC\_28A\_n79C7 | DC\_28A\_n79A | No | N/A |
| DC\_30A\_n2A | DC\_30A\_n2A | No | N/A |
| DC\_30A\_n5A | DC\_30A\_n5A | No | N/A |
| DC\_30A\_n66A | DC\_30A\_n66A | No | N/A |
| DC\_38A\_n78A7 | DC\_38A\_n78A | No | N/A |
| DC\_39A\_n40A3 | DC\_39A\_n40A | No | N/A |
| DC\_39A\_n41A3  DC\_39C\_n41A3 | DC\_39A\_n41A  DC\_39C\_n41A | No | No |
| DC\_39A\_n78A5,7 | DC\_39A\_n78A | No | N/A |
| DC\_39A\_n79A7  DC\_39A\_n79C7 | DC\_39A\_n79A | No | No |
| DC\_40A\_n1A | DC\_40A\_n1A | No | N/A |
| DC\_40A\_n41A3  DC\_40C\_n41A3 | DC\_40A\_n41A | No | N/A |
| DC\_40A\_n77A | DC\_40A\_n77A | No | N/A |
| DC\_40A\_n78A  DC\_40C\_n78A | DC\_40A\_n78A  DC\_40C\_n78A | No | N/A |
| DC\_40A\_n79A7,12  DC\_40C\_n79A7,12 | DC\_40A\_n79A | No | No |
| DC\_41A\_n3A  DC\_41C\_n3A | DC\_41A\_n3A  DC\_41C\_n3A | No | N/A |
| DC\_41A\_n28A  DC\_41C\_n28A | DC\_41A\_n28A  DC\_41C\_n28A | No | N/A |
| DC\_41A\_n77A  DC\_41C\_n77A | DC\_41A\_n77A  DC\_41C\_n77A | No | N/A |
| DC\_41A\_n77(2A)  DC\_41C\_n77(2A) | DC\_41A\_n77A  DC\_41C\_n77A | No | N/A |
| DC\_41A\_n78A  DC\_41C\_n78A  DC\_41D\_n78A | DC\_41A\_n78A  DC\_41C\_n78A | No | N/A |
| DC\_41A\_n78(2A)  DC\_41C\_n78(2A) | DC\_41A\_n78A  DC\_41C\_n78A | No | N/A |
| DC\_41A\_n79A6,7  DC\_41A\_n79C6,7  DC\_41C\_n79A6,7 | DC\_41A\_n79A  DC\_41C\_n79A | No | No |
| DC\_42A\_n28A  DC\_42C\_n28A | DC\_42A\_n28A  DC\_42C\_n28A | No | N/A |
| DC\_42A\_n51A | DC\_42A\_n51A | No | N/A |
| DC\_42A\_n77A3,4,9,11  DC\_42A\_n77C3,4,9,11  DC\_42C\_n77A3,4,9,11  DC\_42C\_n77C3,4,9,11  DC\_42D\_n77A3,4,9,11  DC\_42D\_n77C  DC\_42E\_n77A3,4,9,11  DC\_42E\_n77C | N/A | N/A | N/A |
| DC\_42A\_n77(2A)3,4,9,11  DC\_42C\_n77(2A)3,4,9,11 | N/A | N/A | N/A |
| DC\_42A\_n78A3,4,9,11  DC\_42A\_n78C3,4,9,11  DC\_42C\_n78A3,4,9,11  DC\_42C\_n78C3,4,9,11  DC\_42D\_n78A3,4,9,11  DC\_42D\_n78C  DC\_42E\_n78A3,4,9,11  DC\_42E\_n78C | N/A | N/A | N/A |
| DC\_42A\_n79A9  DC\_42A\_n79C9  DC\_42C\_n79A9  DC\_42C\_n79C9  DC\_42D\_n79A9  DC\_42D\_n79C  DC\_42E\_n79A9  DC\_42E\_n79C | N/A | N/A | N/A |
| DC\_46A\_n78A2  DC\_46C\_n78A2  DC\_46D\_n78A2  DC\_46E\_n78A2 | N/A | N/A | N/A |
| DC\_48A\_n5A | DC\_48A\_n5A | No | N/A |
| DC\_48A\_n12A | DC\_48A\_n12A | No | N/A |
| DC\_48A\_n66A | DC\_48A\_n66A | No | N/A |
| DC\_48A\_n71A  DC\_48B\_n71A  DC\_48C\_n71A  DC\_48D\_n71A | DC\_48A\_n71A | No | N/A |
| DC\_48A-48A\_n71A  DC\_48A-48A-48A\_n71A | DC\_48A\_n71A | No | N/A |
| DC\_66A\_n2A | DC\_66A\_n2A | DC\_66\_n2 | N/A |
| DC\_66A-66A\_n2A | DC\_66A\_n2A | DC\_66\_n2 | N/A |
| DC\_66A\_n5A  DC\_66B\_n5A  DC\_66C\_n5A | DC\_66A\_n5A | DC\_66\_n5 | N/A |
| DC\_66A-66A\_n5A  DC\_66A-66A-66A\_n5A | DC\_66A\_n5A | DC\_66\_n5 | N/A |
| DC\_66A\_n7A  DC\_66A-66A\_n7A  DC\_66A\_n7(2A)  DC\_66A-66A\_n7(2A) | DC\_66A\_n7A | No | N/A |
| DC\_66A\_n12A | DC\_66A\_n12A | No | N/A |
| DC\_66A\_n25A | DC\_66A\_n25A | DC\_66\_n25 | N/A |
| **DC\_66A\_n38A** | DC\_66A\_n38A | No | N/A |
| DC\_66A-66A\_n38A | DC\_66A\_n38A | No | N/A |
| DC\_66A\_n41A  DC\_66A\_n41C | DC\_66A\_n41A | No | N/A |
| DC\_66A\_n41(2A) | DC\_66A\_n41A | No | N/A |
| DC\_66A\_n48A  DC\_66A\_n48B | DC\_66A\_n48A | No | N/A |
| DC\_66A-66A\_n48A  DC\_66A-66A\_n48B | DC\_66A\_n48A | No | N/A |
| DC\_66A\_n71A  DC\_66C\_n71A  DC\_66A\_n71B | DC\_66A\_n71A | No | N/A |
| DC\_66A-66A\_n71A | DC\_66A\_n71A | No | N/A |
| DC\_66A\_n78A | DC\_66A\_n78A | No | N/A |
| DC\_66A\_n78(2A) | DC\_66A\_n78A | No | N/A |
| DC\_66A-66A\_n78A | DC\_66A\_n78A | No | N/A |
| DC\_66A-66A\_n78(2A) | DC\_66A\_n78A | No | N/A |
| DC\_71A\_n5A | DC\_71A\_n5A | No | N/A |
| DC\_71A\_n38A | DC\_71A\_n38A | No | N/A |
| DC\_71A\_n48A | DC\_71A\_n48A | No | N/A |
| DC\_71A\_n66A | DC\_71A\_n66A | No | N/A |
| DC\_71A\_n78A | DC\_71A\_n78A | No | N/A |
| NOTE 1: Uplink EN-DC configurations are the configurations supported by the present release of specifications.  NOTE 2: Restricted to E-UTRA operation when inter-band carrier aggregation is configured. The downlink operating band for Band 46 is paired with the uplink operating band (external E-UTRA band) of the carrier aggregation configuration that is supporting the configured Pcell.  NOTE 3: The minimum requirements apply only when there is non-simultaneous Tx/Rx operation between E-UTRA and NR carriers. This restriction applies also for these carriers when applicable EN-DC configuration is part of a higher order EN-DC configuration.  NOTE 4: The minimum requirements for intra-band contiguous or non-contiguous EN-DC apply. The intra-band requirements also apply for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 5: The frequency range above 3600 MHz for Band n78 is not used in this combination.  NOTE 6: The frequency range below 2506 MHz for Band 41 is not used in this combination.  NOTE 7: Applicable for UE supporting inter-band EN-DC with mandatory simultaneous Rx/Tx capability.  NOTE 8: The frequency range in band n28 is restricted for this band combination to 703 - 733 MHz for the UL and 758-788 MHz for the DL.  NOTE 9: The combination is not used alone as fall back mode of other band combinations in which UL in Band 42 is not used.  NOTE 10: The maximum power spectral density imbalance between downlink carriers is within 6 dB. The power spectral density imbalance condition also applies for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 11: The minimum requirements for inter-band EN-DC apply when the maximum power spectral density imbalance between downlink carriers is within 6 dB. The power spectral density imbalance condition also applies for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 12: Applicable for frequency range above 4800 MHz for Band n79 in this combination.  NOTE 13: The minimum requirements apply for synchronized DL carriers with a maximum receive time difference ≤ 3 usec. The requirements also apply for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 14: The DL interruption requirements for NR DL carrier(s) and E-UTRA DL carrier(s) are specified in clause 8.2.1.2.14 of 38.133 [15] and clause 7.32.2.12 of 36.133 [16] respectively. | | | |

## <End of Changes>