**3GPP TSG-RAN WG4 Meeting #96-e R4-2012233**

**Electronic Meeting, 17 Aug. – 28 Aug., 2020**

**Title:** [DRAFT] LS on DCI-based multiple BWP switch simultaneously

**Response to:**

**Release:** Rel-16

**Work Item:** NR\_RRM\_Enh\_Core, LTE\_NR\_DC\_CA\_enh-Core

**Source:** RAN WG4

**To:** RAN WG1

**Cc:**

**Contact Person:**

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**Attachments:**

**1. Overall Description:**

RAN4 has investigated DCI-based multiple BWP switch, and has some conclusions on the BWP switch delay in each FR as follow:

Where,

is the single BWP switch delay;

N is the number of CCs undergoing simultaneous BWP switch. Note 1

D is incremental delay for BWP switch processing on additional CCs based on UE’s capabilities.

* + Type 1 UE: D = 100us, 200us
  + Type 2 UE: D = 200us, 400us, 800us, 1000us



Note 1: The discussion of N’s definition for undergoing simultaneous BWP switch in both FR1+FR2 is on-going.

Note 2: BWP switch delay is the baseline for dormancy SCell switch.

RAN4 therefore would like to ask RAN1 on the following issues:

1. Whether DCI-based multiple BWP switch delay defined in RAN4 is aligned with DCI scheduling for UE’s PDSCH reception and PUSCH transmission?
2. Whether DCI-based multiple BWP switch delay defined in RAN4 will be applied for HARQ processing timeline in dormancy SCell’s design?

**2. Actions:**

**To RAN WG1 group.**

**ACTION:**

1. RAN4 respectfully asks RAN1 whether DCI-based multiple BWP switch delay defined in RAN4 is aligned with DCI scheduling for UE’s PDSCH reception and PUSCH transmission.
2. RAN4 respectfully asks RAN1 whether DCI-based multiple BWP switch delay defined in RAN4 will be applied for HARQ processing timeline in dormancy SCell’s design.

**3. Date of Next TSG-RAN WG4 Meetings:**

TSG RAN WG4 Meeting #97-e Oc.t 26 – Nov. 13, 2020 E-meeting