3GPP TSG-RAN WG4 Meeting # 96-e R4-2012153

Electronic Meeting, 17 August – 28 August, 2020

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| *CR-Form-v12.0* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
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|  | **38.133** | **CR** | **1041** | **rev** | **1** | **Current version:** | **16.4.0** |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network |  | Core Network |  |

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| ***Title:*** | CR on BWP switching delay on mulitple CCs | | | | | | | | | |
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| ***Source to WG:*** | Huawei, HiSilicon | | | | | | | | | |
| ***Source to TSG:*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_RRM\_enh-Core | | | | |  | ***Date:*** | | | 2020-07-30 |
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| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-16 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) Rel-12 (Release 12)* *Rel-13 (Release 13) Rel-14 (Release 14) Rel-15 (Release 15) Rel-16 (Release 16)* | |
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| ***Reason for change:*** | | * The equation in 8.6.2A.1 is wrong. * TBWPswitchDelay shall also be based on the smallest SCS among all SCS values of all involved CCs regardless of SCS changes. * Delay requirements for non-simultaneous timer-based BWP switching on multiple CCs is missing. * Delay requirements for RRC-based BWP switching on multiple CCs is missing. * The cross carrier DCI scheduling for simultaneous BWP switch on multiple CC is missing. | | | | | | | | |
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| ***Summary of change:*** | | 1. Correct the equlation in 8.6.2A.1 2. Clarify that TBWPswitchDelay shall also be based on the smallest SCS among all SCS values of all involved CCs regardless of SCS changes. 3. Clarify the defination of N which is defined based on the gap capabiltiy. 4. Add Delay requirements for non-simultaneous timer-based BWP switching on multiple CCs is missing. 5. Add Delay requirements for RRC-based BWP switching on multiple CCs is missing. | | | | | | | | |
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| ***Consequences if not approved:*** | | Delay requirements for BWP switching on multiple CCs is incomplete. | | | | | | | | |
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| ***Clauses affected:*** | | 8.6 | | | | | | | | |
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|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **x** | Other core specifications | | | |  | | |
| ***affected:*** | |  | **X** | Test specifications | | | |  | | |
| ***(show related CRs)*** | |  | **x** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

### <Start of Change 1>

#### 8.6.2A.1 Simultaneous DCI based BWP switch delay on multiple CCs

The delay requirements for simultaneous DCI based BWP switch on multiple CCs in this clause apply only if the timing difference among the first symbol of slot carrying DCI for all CCs is received within the MRTD for inter-band CA as defined in clause 7.6.4.

The requirements in the clause apply only if the BWP switch on each involved CC is triggered by individual DCI received on the same CC where BWP switch occurs.

For DCI-based BWP switch on multiple CCs, after the UE receives BWP switching request, UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWPs on the serving cells on which BWP switch on the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelay which starts from the beginning of DL slot n, where slot n is slot which UE receives the earliest BWP switching request among CCs on which UE is performing simultaneous DCI-based BWP switching.

The UE is not required to transmit UL signals or receive DL signals until the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelay which starts from the beginning of DL slot n except DCI triggering BWP switch on the cell where DCI-based BWP switch occurs. The UE is not required to follow the requirements defined in this clause when performing a DCI-based BWP switch between the BWPs in disjoint channel bandwidths or in partially overlapping channel bandwidths on any serving cell.

UE shall finish BWP switch within the time duration TMultipleBWPswitchDelay, which is defined as:

TMultipleBWPswitchDelay = TBWPswitchDelay + D\*(N -1)

Where:

- TBWPswitchDelay is the BWP switching delay on single CC defined in Table 8.6.2-1 depending on UE capability *bwp-SwitchingDelay* [2]. TBWPswitchDelay should be based on the smallest SCS among all SCS values of all involved CCs regardless of SCS changes.

- D is the incremental delay for each additional CC involved in simultaneous BWP switch and depends on UE capability [13].

- For UE which is capable of [per-FR gap], and no BWP switch involves SCS change, N is FFS; For UE which is not capable of [per-FR gap], or the BWP switches on multiple CCs involves SCS changing, N isFFS

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

### <End of Change 1>

### <Start of Change 2>

### 8.6.2B Timer based BWP switch delay on multiple CCs

The requirements in this clause only apply to the case when the same type of BWP switch (timer based BWP switch) is performed on multiple CCs simultaneously or over partially overlapping time period.

#### 8.6.2B.1 Simultaneous timer based BWP switch delay on multiple CCs

The delay requirements for simultaneous timer based BWP switch on multiple CCs in this clause apply only if the timing difference among the beginning of the slot where timer based BWP switching starts for all CCs is within the MRTD for inter-band CA as defined in clause 7.6.4.

For timer-based BWP switch on multiple CCs, UE shall start BWP switch at DL slot n, where slot n is the first slot of a DL subframe (in FR1) or DL half-subframe (in FR2) immediately after the earliest BWP-inactivity timer *bwp-InactivityTimer* [2] expiration occurs on multiple serving cells, and the UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWPs on the serving cells on which BWP switch on the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelay which starts from the beginning of DL slot n, where TMultipleBWPswitchDelay is defined in 8.6.2A.1.

The UE is not required to transmit UL signals or receive DL signals during time duration TMultipleBWPswitchDelay after *bwp-InactivityTimer* [2] expires on the cell where timer-based BWP switch occurs.

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

#### 8.6.2B.2 Non-simultaneous timer based BWP switch delay on multiple CCs

In non-simultaneous case, the timer-based BWP switch on multiple CCs is triggered over partially overlapping time period.

The delay requirements for simultaneous timer based BWP switch on multiple CCs in this clause apply if the timing difference among the beginning of the slot where timer based BWP switching starts for all CCs is exceeds the MRTD for inter-band CA as defined in clause 7.6.4, and the BWP switch does not involve SCS change. The UE performs the non-simultaneous timer-based BWP switch on the CCs sequentially.

For non-simultaneous timer-based BWP switch, the UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell of CC *j* on which BWP switch on the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelayTotal which starts from the beginning of DL slot n, where slot n is the first slot of a DL subframe (in FR1) or DL half-subframe (in FR2) immediately after the earliest BWP-inactivity timer *bwp-InactivityTimer* [2] expires.

TMultipleBWPswitchDelayTotal = TDelay + TMultipleBWPswitchDelay

Where:

TDelay is the time required to complete the ongoing timer-based BWP switching on another single CC or simultaneously triggered timer-based BWP switching delay on other multiple CCs.

TMultipleBWPswitchDelay is the timer-based BWP switch delay on current single CC or simultaneously triggered on multiple CCs defined in clause 8.6.2 and clause 8.6.2B.1.

The UE is not required to transmit UL signals or receive DL signals during time duration TMultipleBWPswitchDelayTotal after *bwp-InactivityTimer* [2] expires on the cell where timer-based BWP switch occurs.

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

### 8.6.3 RRC based BWP switch delay on a single CC

The requirements in this clause only apply to the case that the BWP switch is performed on a single CC.

For RRC-based BWP switch, after the UE receives RRC reconfiguration involving active BWP switching or parameter change of its active BWP, UE shall be able to receive PDSCH/PDCCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch occurs on the first DL or UL slot right after a time duration of slots which begins from the beginning of DL slot n, where

DL slot n is the last slot containing the RRC command, and

is the length of the RRC procedure delay in ms as defined in clause 12 in TS 38.331 [2], and

is the time used by the UE to perform BWP switch.

The UE is not required to transmit UL signals or receive DL signals during the time defined by on the cell where RRC-based BWP switch occurs.

### 8.6.3A RRC based BWP switch delay on multiple CCs

The requirements in this clause only apply to the case when the same type of BWP switch (RRC based BWP switch) is performed on multiple CCs simultaneously or over partially overlapping time period.

#### 8.6.3A.1 Simultaneous RRC based BWP switch delay on multiple CCs

Requirements in this clause apply only if RRC based BWP switching on multiple CCs for NR-CA is triggered by a single RRC command.

For RRC-based BWP switch, after the UE receives RRC reconfiguration involving active BWP switching or parameter change of its active BWPs, UE shall be able to receive PDSCH/PDCCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWPs on the serving cells on which BWP switch occurs on the first DL or UL slot right after a time duration of slots which begins from the beginning of DL slot n, where

DL slot n is the last slot containing the RRC command, and

is the length of the RRC procedure delay in millisecond as defined in clause 12 in TS 38.331 [2], and

is the time used by the UE to perform BWP switch.

for UE which is capable of type 1 BWP switching delay depending on UE capability *bwp-SwitchingDelay* [2]. for UE which is capable of type 2 BWP switching delay depending on UE capability *bwp-SwitchingDelay* [2], where D is the incremental delay for each additional CC involved in simultaneous BWP switch and depends on UE capability [13].

N is the number of CCs within the NR-CA configured for performing simultaneous BWP switch.

The UE is not required to transmit UL signals or receive DL signals during the time defined by on the cells where RRC-based BWP switch occurs.

#### 8.6.3A.2 Non-simultaneous RRC based BWP switch delay on multiple CCs

In non-simultaneous case, the RRC-based BWP switch on multiple CCs is triggered over partially overlapping time period n different Cell groups. The delay requirements in this clause apply only if:

* BWP switching on multiple CCs in different cell groups are triggered by separate RRC commands, and
* UE is operating in NR-DC (FR1+FR2), and
* UE is capable of per-FR gap, and
* BWP switch does not involve SCS change.

For non-simultaneous RRC-based BWP switch, after the UE receives RRC reconfiguration involving active BWP switching or parameter change of its active BWPs, UE shall be able to receive PDSCH/PDCCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWPs on the serving cells on which BWP switch occurs on the first DL or UL slot right after a time duration of slots which begins from the beginning of DL slot n, where

DL slot n is the last slot containing the RRC command, and

is the length of the RRC procedure delay in millisecond as defined in clause 12 in TS 38.331 [2], and

is FFS, and

is the time used by the UE to perform BWP switch.

and N are defined in clause 8.6.3A.1.

The UE is not required to transmit UL signals or receive DL signals during the time defined by on the cells where RRC-based BWP switch occurs.

### <End of Change 2>