**3GPP TSG-RAN4 Meeting #95-e *R4-2008629***

**Electronic meeting, 25th May – 5th Jun, 2020**

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| *CR-Form-v11.2* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.133** | **CR** | **0641** | **rev** | **1** | **Current version:** | **16.3.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  | | | | | | | | | | |
| ***Title:*** | Draft CR on cell identification requirements for NR HST | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | CATT | | | | | | | | | |
| ***Source to TSG:*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_HST-Core | | | | |  | | ***Date:*** | | 2020-5-8 |
|  |  | | | |  | | |  | |  |
| ***Category:*** | **B** |  | | | | | | ***Release:*** | | Rel-16 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) Rel-12 (Release 12)* *Rel-13 (Release 13) Rel-14 (Release 14) Rel-15 (Release 15) Rel-16 (Release 16)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | The cell identification requirements should be enhanced under NR HST scenarios | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | Introduce cell identification requirements for NR HST scenarios | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | Cell identification requirements for NR HST will be missed | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | Section 9.2.5 and 9.2.6 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | |  | | | |
| ***Other specs*** | |  | **X** | Other core specifications | | |  | | | |
| ***affected:*** | | **X** |  | Test specifications | | | TS 38.521-3 | | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | |  | | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |

## 

<< Start of Change #1>>

### 9.2.5 Intrafrequency measurements without measurement gaps

9.2.5.1 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index(*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE is indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2.

Tidentify\_intra\_without\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra) ms

Tidentify\_intra\_with\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra) ms

Where:

TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.5.1-1, 9.2.5.1-2, 9.2.5.1-4 (deactivated Scell) or 9.2.5.1-5 (deactivated SCell)

TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.5.1-3 or 9.2.5.1-6 (deactivated SCell)

T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.5.2-1, table 9.2.5.2-2 table 9.2.5.2-3 (deactivated Scell) or 9.2.5.2-4(deactivated SCell)

CSSFintra: it is a carrier specific scaling factor and is determined

- according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside measurement gaps, i.e. when intrafrequency SMTC is fully non overlapping or partially overlapping with measurement gaps, or according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps, i.e. when intrafrequency SMTC is fully overlapping with measurement gaps.

- if the high layer in TS 38.331 [2] signaling of *smtc2* is configured, the assumed periodicity of intrafrequency SMTC occasions corresponds to the value of higher layer parameter *smtc2*; Otherwise the assumed periodicity of intrafrequency SMTC occasions corresponds to the value of higher layer parameter *smtc1*.

Mpss/sss\_sync\_w/o\_gaps : For a UE supporting FR2 power class 1, Mpss/sss\_sync=40. For a UE supporting power class 2, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2 power class 3, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2 power class 4, Mpss/sss\_sync\_w/o\_gaps =24

Mmeas\_period\_w/o\_gaps : For a UE supporting power class 1, Mmeas\_period\_w/o\_gaps =40. For a UE supporting FR2 power class 2, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 3, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 4, Mmeas\_period\_w/o\_gaps =24.

When intrafrequency SMTC is fully non overlapping with measurement gaps or intrafrequency SMTC is fully overlapping with MGs, Kp=1

When intrafrequency SMTC is partially overlapping with measurent gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP

If the higher layer signaling in TS38.331 [2] signaling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index

For FR2,

Klayer1\_measurement=1,

* + if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting outside measurement gap are not fully overlapped by intra-frequency SMTC occasions, or
  + if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting outside measurement gap and fully-overlapped by intra-frequency SMTC occasions are not overlapped by with the SSB symbols indicated by *SSB-ToMeasure* and 1 symbol before each consecutive SSB symbols indicated by *SSB-ToMeasure* and 1 symbol after each consecutive SSB symbols indicated by *SSB-ToMeasure*, given that *SSB-ToMeasure* is configured;

Klayer1\_measurement=1.5, otherwise.

If SCG DRX is in use, intrafrequency cell identification requirements specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5 and Table 9.2.5.1-6 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

**Table 9.2.5.1-1: Time period for PSS/SSS detection, (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max( 600ms, ceil( 5 x Kp) x SMTC period )Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max( 600ms, ceil(M2 Note 2x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(5] x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: When UE is not configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5; When UE is configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. | |

**Table 9.2.5.1-2: Time period for PSS/SSS detection, (Frequency range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600ms, ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified | |

**Table 9.2.5.1-3: Time period for time index detection (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(120ms, ceil( 3 x Kp )x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil (M2 Note 2 x 3 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(3 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: When UE is not configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5; When UE is configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. | |

**Table 9.2.5.1-4: Time period for PSS/SSS detection, deactivated SCell (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync**\_**intra** |
| No DRX | 5 x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | 5 x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | 5 x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-5: Time period for PSS/SSS detection, deactivated SCell (Frequency range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | Mpss/sss\_sync\_w/o\_gaps x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Mpss/sss\_sync\_w/o\_gaps x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Mpss/sss\_sync\_w/o\_gaps x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-6: Time period for time index detection, deactivated SCell (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | 3 x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | 3 x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | 3 x max(measCycleSCell, DRX cycle) x CSSFintra |

Table 9.2.5.1-7: Void

Table 9.2.5.1-8: Void

9.2.5.2 Measurement period

The measurement period for intrafrequency measurements without gaps is as shown in table 9.2.5.2-1, 9.2.5.2-2, 9.2.5.2-3 (deactivated SCell) or 9.2.5.2-4(deactivated SCell). For UE configured with [*highSpeedEnhancedMeasFlagNR*], T SSB\_measurement\_period\_intra is specified in Table 9.2.5.2-5.

If the higher layer signaling in TS38.331 [2] signaling of *smtc2* is present and smtc1 is fully overlapping with measurement and smtc2 is partially overlapping with measurement gaps, requirements are not specified for TSSB\_measurement\_period\_intra

If SCG DRX is in use, intrafrequency measurement period requirements specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3 and Table 9.2.5.2-4 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

**Table 9.2.5.2-1: Measurement period for intrafrequency measurements without gaps(Frequency FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil( 5 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified | |

**Table 9.2.5.2-2: Measurement period for intrafrequency measurements without gaps(Frequency FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | max(400ms, ceil(Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5x Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified | |

**Table 9.2.5.2-3: Measurement period for intrafrequency measurements without gaps (deactivated SCell) (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | 5 x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | 5 x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | 5 x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.2-4: Measurement period for intrafrequency measurements without gaps (deactivated SCell) (Frequency range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | Mmeas\_period\_w/o\_gaps x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Mmeas\_period\_w/o\_gaps x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Mmeas\_period\_w/o\_gaps x max(measCycleSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.2-5: T SSB\_measurement\_period\_intra for UE configured with [highSpeedEnhancedMeasFlagNR] (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX Note 2 | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 |
| DRX cycle≤ 160ms | max(200ms, ceil(5 x M2 Note 2 x Kp) x max(SMTC period,DRX cycle)) |
| 160ms < DRX cycle≤ 320ms | ceil(4 x M2 Note 2 x Kp) x max(SMTC period,DRX cycle) |
| DRX cycle>320ms | ceil( 3 x Kp ) x DRX cycle |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 | |

<< End of Change #1>>

<< Start of Change #2>>

### 9.2.6 Intrafrequency measurements with measurement gaps

#### 9.2.6.1 Void

#### 9.2.6.2 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE has been indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2.

Tidentify\_intra\_without\_index = TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra ms

Tidentify\_intra\_with\_index = TPSS/SSS\_sync\_ntra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra

Where:

TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.6.2-1 or 9.2.6.2-2.

TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.6.2-3.

T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.6.2-1 or 9.2.6.2-2.

CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps.

Mpss/sss\_sync\_with\_gaps : For a UE supporting FR2 power class 1, Mpss/sss\_sync with\_gaps=40. For a UE supporting FR2 power class 2, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2 power class 3, Mpss/sss\_sync with\_gaps =24. For a UE supporting power class 4, Mpss/sss\_sync with\_gaps =24

Mmeas\_period\_ with\_gaps: For a UE supporting power class 1, Mmeas\_period\_ with\_gaps =40. For a UE supporting power class 2, Mmeas\_period\_ with\_gaps =24. For a UE supporting power class 3, Mmeas\_period\_ with\_gaps =24. For a UE supporting power class 4, Mmeas\_period with\_gaps =24.

If the higher layer signaling in TS 38.331 [2] signaling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

If SCG DRX is in use, intrafrequency cell identification requirements specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

**Table 9.2.6.2-1: Time period for PSS/SSS detection (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600ms, 5 x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(M2Note 1x 5) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | 5 x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When UE is not configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5; When UE is configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. | |

**Table 9.2.6.2-2: Time period for PSS/SSS detection (Frequency range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600ms, Mpss/sss\_sync\_with\_gaps x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5x Mpss/sss\_sync\_with\_gaps) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Mpss/sss\_sync\_with\_gaps x max(MGRP, DRX cycle) x CSSFintra |

**Table 9.2.6.2-3: Time period for time index detection (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(120ms, 3 x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil(M2Note 1x 3) x max(MGRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320ms | 3 x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When UE is not configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5; When UE is configured with [highSpeedEnhancedMeasFlagNR], M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. | |

Table 9.2.6.2-7: Void

Table 9.2.6.2-8: Void

#### 9.2.6.3 Intrafrequency Measurement Period

The measurement period for FR1 intrafrequency measurements with gaps is as shown in table 9.2.6.3-1.

The measurement period for FR2 intrafrequency measurements with gaps is as shown in table 9.2.6.3-2.

For UE configured with [*highSpeedEnhancedMeasFlagNR*], T SSB\_measurement\_period\_intra is specified in Table 9.2.6.3-3.

If SCG DRX is in use, intrafrequency measurement period requirements specified in Table 9.2.6.3-1and Table 9.2.6.3-2, shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

**Table 9.2.6.3-1: Measurement period for intrafrequency measurements with gaps(Frequency Range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | max(200ms, 5 x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5) x max(MGRP, SMTC period,DRX cycle))x CSSFintra |
| DRX cycle>320ms | 5 x max(MGRP, DRX cycle) x CSSFintra |

**Table 9.2.6.3-2: Measurement period for intrafrequency measurements with gaps(Frequency Range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | max(400ms, Mmeas\_period with\_gaps x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5 x Mmeas\_period with\_gaps) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| DRX cycle>320ms | Mmeas\_period with\_gaps x max(MGRP, DRX cycle) x CSSFintra |

**Table 9.2.6.3-3: Measurement period for UE configured with [highSpeedEnhancedMeasFlagNR] (Frequency Range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **T SSB\_measurement\_period\_intra** |
| No DRX | max(200ms, 5 x max(MGRP, SMTC period)) Note 1 |
| DRX cycle≤ 160ms | max(200ms, ceil(M2Note 2 x 5) x max(MGRP, SMTC period,DRX cycle)) |
| 160ms < DRX cycle≤ 320ms | max(200ms, ceil(M2Note 2 x 4) x max(MGRP, DRX cycle)) |
| DRX cycle>320ms | 3 x max(MGRP, DRX cycle) |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified  NOTE 2: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 | |

<< End of Change #2>>