**3GPP TSG-RAN WG4 Meeting #110 R4-2403905**

**Athens, Greece, February 26 – March 1, 2024**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| *CR-Form-v12.2* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.101-3** | **CR** | **1181** | **rev** | **-** | **Current version:** | **18.4.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
|  | | | | | | | | |

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | |
| ***Title:*** | Big CR to 38.101-3 on simultaneous Rx-Tx basket | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Huawei, HiSilicon | | | | | | | | | |
| ***Source to TSG:*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | LTE\_NR\_Simult\_RxTx\_R18 | | | | |  | ***Date:*** | | | 2023-03-06 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **B** |  | | | | | ***Release:*** | | | Rel-18 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18) Rel-19 (Release 19)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | This big CR covers the following draft CRs endorsed in RAN4#110. The reason for change is copied below.  R4-2403604 draftCR to 38.101-3 Correction on the simultaneous Rx-Tx for DC\_40\_n41 and DC\_39\_n41  It was agreed that CA\_n39-n41 and CA\_n40-n41 can support simultaneous Rx-Tx in R4-2310507. According to the principle in TR 38.839: Same simultaneous Rx/Tx capability can be applied for the corresponding band combination among NR CA, NR DC and NR EN-DC. Therefore, DC\_40\_n41 and DC\_39\_n41 should also support simultaneous Rx-Tx.  The cross band isolation from band 40 UL to band n7 DL is too high for the test point compared to that from band n40 UL to band 7 DL. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | The summary of change in the endorsed draft CR is copied below.  R4-2403604   1. Remove Note 3 for DC\_39\_n41, DC\_40\_n41 in Table 5.5B.4.1-1. 2. Remove Note 5 for DC\_40\_n41 in Table 6.2B.4.2.3.1-1. 3. Add the cross band isolation MSD in Table 7.3B.2.3.4-1. Remove the bracket for the cross band isolation of DC\_7\_n40 to align with CA\_n7-n40. 4. Put the cross band isolation from band 40 UL to band n7 DL into bracket. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | The consequences if not approved for the endorsed draft CR are coppied below  R4-2403604   1. The EN-DC band combinations DC\_40\_n41 and DC\_39\_n41 cannot be supported in the specification, which is not aligned with the CA band combination counterparts in TS 38.101-1. And unable to meet the principle in TR 38.839. 2. The cross band isolation from band 40 UL to band n7 DL is inaccurate. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | 5.5B.4.1, 6.2B.4.2.3.1, 7.3B.2.3.4 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | | TS/TR ... CR ... | | |
| ***affected:*** | | **X** |  | Test specifications | | | | TS 38.521-3 | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | | TS/TR ... CR ... | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

## **<<Start of Change>>**

### 5.5B.4 Inter-band EN-DC within FR1

#### 5.5B.4.1 Inter-band EN-DC configurations within FR1 (two bands)

Table 5.5B.4.1-1: Inter-band EN-DC configurations within FR1 (two bands)

| **EN-DC**  **configuration** | **Uplink EN-DC**  **configuration**  **(NOTE 1)** | **Single UL allowed** | **DL interruption allowed**  **(Note 14)** |
| --- | --- | --- | --- |
| DC\_1A\_n3A  DC\_1C\_n3A | DC\_1A\_n3A  DC\_1C\_n3A | DC\_1\_n3 |  |
| DC\_1A\_n5A | DC\_1A\_n5A | No |  |
| DC\_1A\_n7A  DC\_1A\_n7B | DC\_1A\_n7A  DC\_1A\_n7B | No |  |
| DC\_1A-1A\_n7A  DC\_1A-1A\_n7B | DC\_1A\_n7A | No |  |
| DC\_1A\_n8A | DC\_1A\_n8A | No |  |
| DC\_1A\_n20A | DC\_1A\_n20A | No |  |
| DC\_1A\_n28A | DC\_1A\_n28A | No |  |
| DC\_1A\_n26A | DC\_1A\_n26A | No |  |
| DC\_1A-1A\_n28A | DC\_1A\_n28A | No |  |
| DC\_1A\_n38A  DC\_1C\_n38A | DC\_1A\_n38A | No |  |
| DC\_1A\_n40A  DC\_1A\_n40B | DC\_1A\_n40A | No |  |
| DC\_1A\_n41A7 | DC\_1A\_n41A | No |  |
| DC\_1A\_n50A | DC\_1A\_n50A | No |  |
| DC\_1A\_n51A | DC\_1A\_n51A | No |  |
| DC\_1A\_n71A  DC\_1A\_n71B | DC\_1A\_n71A | No |  |
| DC\_1A\_n77A7  DC\_1A\_n77C7 | DC\_1A\_n77A | DC\_1\_n77 | No |
| DC\_1A\_n77(2A)7,21  DC\_1A\_n77(3A)7 | DC\_1A\_n77A21 | DC\_1\_n77 | No |
| DC\_1A\_n78A7  DC\_1A\_n78C7, 21 | DC\_1A\_n78A 21 | No | No |
| DC\_1A\_n78(2A)7,21  DC\_1A\_n78(A-C)7 | DC\_1A\_n78A21 | No | No |
| DC\_1A-1A\_n78A | DC\_1A\_n78A | No | No |
| DC\_1A\_n79A7  DC\_1A\_n79C7 | DC\_1A\_n79A | No | No |
| DC\_1A\_n105A | DC\_1A\_n105A | No |  |
| DC\_2A\_n5A | DC\_2A\_n5A | No |  |
| DC\_2A-2A\_n5A | DC\_2A\_n5A | No |  |
| DC\_2A\_n7A  DC\_2C\_n7A | DC\_2A\_n7A | No |  |
| DC\_2A\_n7(2A) | DC\_2A\_n7A | No |  |
| DC\_2A-2A\_n7A | DC\_2A\_n7A | No |  |
| DC\_2A\_n12A | DC\_2A\_n12A | No |  |
| DC\_2A\_n25A11, 13, 20 | N/A | N/A |  |
| DC\_2A\_n28A  DC\_2C\_n28A | DC\_2A\_n28A | No |  |
| DC\_2A\_n30A | DC\_2A\_n30A | No |  |
| DC\_2A-2A\_n30A | DC\_2A\_n30A | No |  |
| DC\_2A\_n38A | DC\_2A\_n38A | No |  |
| DC\_2A-2A\_n38A | DC\_2A\_n38A | No |  |
| DC\_2A\_n41A  DC\_2A\_n41C  DC\_2C\_n41A | DC\_2A\_n41A  DC\_2C\_n41A | No |  |
| DC\_2A\_n41(2A) | DC\_2A\_n41A | No |  |
| DC\_2A-2A\_n41A | DC\_2A\_n41A | No |  |
| DC\_2A\_n46A | DC\_2A\_n46A | No |  |
| DC\_2A\_n48A  DC\_2A\_n48B | DC\_2A\_n48A | No |  |
| DC\_2A\_n66A | DC\_2A\_n66A | DC\_2\_n66 |  |
| DC\_2A\_n66(2A) | DC\_2A\_n66A | DC\_2\_n66 |  |
| DC\_2A-2A\_n66A | DC\_2A\_n66A | DC\_2\_n66 |  |
| DC\_2A\_n71A  DC\_2A\_n71B  DC\_2C\_n71A | DC\_2A\_n71A | No |  |
| DC\_2A-2A\_n71A | DC\_2A\_n71A | No |  |
| DC\_2A\_n77A  DC\_2A\_n77C21 | DC\_2A\_n77A21 | DC\_2\_n77 |  |
| DC\_2A\_n77(2A) 21 | DC\_2A\_n77A21 | DC\_2\_n77 |  |
| DC\_2A-2A\_n77A21  DC\_2A-2A\_n77C21 | DC\_2A\_n77A21 | DC\_2\_n77 |  |
| DC\_2A-2A\_n77(2A) 21 | DC\_2A\_n77A21 | DC\_2\_n77 |  |
| DC\_2A\_n78A | DC\_2A\_n78A | DC\_2\_n78 |  |
| DC\_2A-2A\_n78(2A) | DC\_2A\_n78A | DC\_2\_n78 |  |
| DC\_2A\_n78(2A) 21 | DC\_2A\_n78A21 | DC\_2\_n78 |  |
| DC\_2A-2A\_n78A | DC\_2A\_n78A | DC\_2\_n78 |  |
| DC\_3A\_n1A  DC\_3C\_n1A | DC\_3A\_n1A  DC\_3C\_n1A | DC\_3\_n1 |  |
| DC\_3A-3A\_n1A | DC\_3A\_n1A | DC\_3\_n1 |  |
| DC\_3A\_n5A  DC\_3C\_n5A | DC\_3A\_n5A | DC\_3\_n5 |  |
| DC\_3A\_n7A  DC\_3A\_n7B  DC\_3C\_n7A  DC\_3C\_n7B | DC\_3A\_n7A  DC\_3A\_n7B  DC\_3C\_n7A | No |  |
| DC\_3A-3A\_n7A  DC\_3A-3A\_n7B | DC\_3A\_n7A | No |  |
| DC\_3A\_n8A | DC\_3A\_n8A | No |  |
| DC\_3A-3A\_n8A | DC\_3A\_n8A | No |  |
| DC\_3A\_n20A  DC\_3C\_n20A | DC\_3A\_n20A | No |  |
| DC\_3A\_n26A  DC\_3C\_n26A | DC\_3A\_n26A  DC\_3C\_n26A | Yes |  |
| DC\_3A\_n28A  DC\_3C\_n28A | DC\_3A\_n28A  DC\_3C\_n28A | No |  |
| DC\_3A\_n34A | DC\_3A\_n34A | No |  |
| DC\_3A\_n38A  DC\_3C\_n38A | DC\_3A\_n38A | No |  |
| DC\_3A\_n40A  DC\_3A\_n40B | DC\_3A\_n40A | No |  |
| DC\_3A\_n41A7  DC\_3A\_n41C  DC\_3C\_n41A7 | DC\_3A\_n41A  DC\_3C\_n41A | DC\_3\_n41 | No |
| DC\_3A\_n50A | DC\_3A\_n50A | No |  |
| DC\_3A\_n51A | DC\_3A\_n51A | No |  |
| DC\_3A\_n71A  DC\_3A\_n71B | DC\_3A\_n71A | No |  |
| DC\_3A\_n77A7  DC\_3A\_n77C7  DC\_3C\_n77A7,21 | DC\_3A\_n77A21  DC\_3C\_n77A | DC\_3\_n77 | No |
| DC\_3A\_n77(2A)7,21  DC\_3A\_n77(3A)7  DC\_3C\_n77(2A)7,21 | DC\_3A\_n77A,21  DC\_3C\_n77A | DC\_3\_n77 | No |
| DC\_3A-3A\_n77A7 | DC\_3A\_n77A | DC\_3\_n77 | No |
| DC\_3A\_n78A7,21  DC\_3A\_n78C7  DC\_3C\_n78A7,21 | DC\_3A\_n78A,21,23  DC\_3C\_n78A | DC\_3\_n78 | No |
| DC\_3A\_n78(2A)7,21  DC\_3A\_n78(A-C)7  DC\_3C\_n78(2A)7,21 | DC\_3A\_n78A,21  DC\_3C\_n78A | DC\_3\_n78 | No |
| DC\_3A-3A\_n78A7, 21 | DC\_3A\_n78A21 | DC\_3\_n78 | No |
| DC\_3A\_n79A7  DC\_3A\_n79C7  DC\_3C\_n79A7 | DC\_3A\_n79A  DC\_3C\_n79A | No | No |
| DC\_3A-3A\_n79A7 | DC\_3A\_n79A | No |  |
| DC\_3A\_n105A | DC\_3A\_n105A | No |  |
| DC\_4A\_n2A | DC\_4A\_n2A | No |  |
| DC\_4A\_n5A | DC\_4A\_n5A | DC\_4\_n5 |  |
| DC\_4A\_n7A | DC\_4A\_n7A | No |  |
| DC\_4A\_n28A | DC\_4A\_n28A | No |  |
| DC\_4A\_n38A | DC\_4A\_n38A | No |  |
| DC\_4A\_n41A | DC\_4A\_n41A | No |  |
| DC\_4A\_n78A | DC\_4A\_n78A | No |  |
| DC\_4A\_n78(2A) | DC\_4A\_n78A | No |  |
| DC\_5A\_n1A | DC\_5A\_n1A | No |  |
| DC\_5A\_n2A  DC\_5B\_n2A | DC\_5A\_n2A | No |  |
| DC\_5A\_n2(2A) | DC\_5A\_n2A | No |  |
| DC\_5A-5A\_n2A | DC\_5A\_n2A | No |  |
| DC\_5A\_n3A | DC\_5A\_n3A | DC\_5\_n3 |  |
| DC\_5A\_n7A | DC\_5A\_n7A | DC\_5\_n7 |  |
| DC\_5A\_n7(2A) | DC\_5A\_n7A | DC\_5\_n7 |  |
| DC\_5A\_n12A | DC\_5A\_n12A | No |  |
| DC\_5A\_n25A | DC\_5A\_n25A | No |  |
| DC\_5A\_n28A | DC\_5A\_n28A | No |  |
| DC\_5A\_n30A | DC\_5A\_n30A | No |  |
| DC\_5A\_n38A | DC\_5A\_n38A | DC\_5\_n38 |  |
| DC\_5A\_n40A | DC\_5A\_n40A | No |  |
| DC\_5A\_n41A | DC\_5A\_n41A | No |  |
| DC\_5A\_n48A  DC\_5A\_n48B | DC\_5A\_n48A | No |  |
| DC\_5A\_n66A  DC\_5B\_n66A | DC\_5A\_n66A | DC\_5\_n66 |  |
| DC\_5A-5A\_n66A | DC\_5A\_n66A | DC\_5\_n66 |  |
| DC\_5A\_n77A  DC\_5A\_n77C21 | DC\_5A\_n77A21 | No |  |
| DC\_5A\_n77(2A) 21  DC\_5A\_n77(3A) | DC\_5A\_n77A21 | No |  |
| DC\_5A\_n71A | DC\_5A\_n71A | No |  |
| DC\_5A\_n78A7  DC\_5A\_n78C7 | DC\_5A\_n78A | No | No |
| DC\_5A\_n78(2A)7,21  DC\_5A\_n78(A-C)7 | DC\_5A\_n78A21 | No | No |
| DC\_5A\_n79A | DC\_5A\_n79A | No | No |
| DC\_7A\_n1A  DC\_7C\_n1A | DC\_7A\_n1A  DC\_7C\_n1A | No |  |
| DC\_7A-7A\_n1A | DC\_7A\_n1A | No |  |
| DC\_7A\_n2A  DC\_7C\_n2A | DC\_7A\_n2A | No |  |
| DC\_7A\_n2(2A) | DC\_7A\_n2A | No |  |
| DC\_7A\_n3A  DC\_7C\_n3A | DC\_7A\_n3A  DC\_7C\_n3A | No |  |
| DC\_7A\_n5A  DC\_7C\_n5A | DC\_7A\_n5A  DC\_7C\_n5A | DC\_7\_n5 |  |
| DC\_7A-7A\_n5A | DC\_7A\_n5A | DC\_7\_n5 |  |
| DC\_7A\_n8A | DC\_7A\_n8A | No |  |
| DC\_7A-7A\_n8A | DC\_7A\_n8A | No |  |
| DC\_7A\_n12A | DC\_7A\_n12A | No |  |
| DC\_7A-7A\_n78(2A)7,21 | DC\_7A\_n78A21 | No |  |
| DC\_7A\_n20A | DC\_7A\_n20A | No |  |
| DC\_7A\_n25A  DC\_7C\_n25A | DC\_7A\_n25A | No |  |
| DC\_7A\_n26A  DC\_7C\_n26A | DC\_7A\_n26A  DC\_7C\_n26A | Yes |  |
| DC\_7A-7A\_n25A | DC\_7A\_n25A | No |  |
| DC\_7A\_n28A  DC\_7C\_n28A | DC\_7A\_n28A  DC\_7C\_n28A | No |  |
| DC\_7A\_n40A | DC\_7A\_n40A | Yes |  |
| DC\_7A-7A\_n40A | DC\_7A\_n40A | Yes |  |
| DC\_7A-7A\_n28A | DC\_7A\_n28A | No |  |
| DC\_7A\_n51A | DC\_7A\_n51A | No |  |
| DC\_7A\_n66A  DC\_7C\_n66A | DC\_7A\_n66A | No |  |
| DC\_7A-7A\_n66A | DC\_7A\_n66A | No |  |
| DC\_7A\_n71A | DC\_7A\_n71A | No |  |
| DC\_7A\_n77A7  DC\_7C\_n77A | DC\_7A\_n77A | No |  |
| DC\_7A\_n77(2A)  DC\_7A\_n77(3A)  DC\_7C\_n77(2A) | DC\_7A\_n77A | No |  |
| DC\_7A-7A\_n77A7 | DC\_7A\_n77A | No |  |
| DC\_7A-7A\_n77(2A)  DC\_7A-7A\_n77(3A) | DC\_7A\_n77A | No |  |
| DC\_7A\_n78A7,24  DC\_7C\_n78A7,21  DC\_7A\_n78C7 | DC\_7A\_n78A21,24  DC\_7C\_n78A | No |  |
| DC\_7A\_n78(2A)7,21  DC\_7A\_n78(A-C)7  DC\_7C\_n78(2A)7, 21 | DC\_7A\_n78A21  DC\_7C\_n78A | No |  |
| DC\_7A-7A\_n78A7, 21  DC\_7A-7A\_n78C7 | DC\_7A\_n78A21 | No |  |
| DC\_7A-7A\_n78(A-C)7 | DC\_7A\_n78A | No |  |
| DC\_7A\_n79A  DC\_7A\_n79C | DC\_7A\_n79A | No |  |
| DC\_7A-7A\_n79A | DC\_7A\_n79A | No |  |
| DC\_7A\_n105A | DC\_7A\_n105A | No |  |
| DC\_8A\_n1A  DC\_8B\_n1A | DC\_8A\_n1A | No |  |
| DC\_8A\_n2A | DC\_8A\_n2A | DC\_8\_n2 |  |
| DC\_8A\_n3A  DC\_8B\_n3A | DC\_8A\_n3A | No |  |
| DC\_8A\_n7A | DC\_8A\_n7A | No |  |
| DC\_8A\_n20A | DC\_8A\_n20A | Yes |  |
| DC\_8A\_n28A | DC\_8A\_n28A | No |  |
| DC\_8A\_n34A | DC\_8A\_n34A | No |  |
| DC\_8A\_n38A | DC\_8A\_n38A | No |  |
| DC\_8A\_n39A | DC\_8A\_n39A | No |  |
| DC\_8A\_n40A7 | DC\_8A\_n40A | No |  |
| DC\_8A\_n41A7  DC\_8A\_n41C | DC\_8A\_n41A | No | No |
| DC\_8A\_n41(2A) | DC\_8A\_n41A | No | No |
| DC\_8A\_n77A7  DC\_8B\_n77A7 | DC\_8A\_n77A | No | No |
| DC\_8A\_n77(2A)7,21  DC\_8A\_n77(3A)7 | DC\_8A\_n77A21 | No | No |
| DC\_8A\_n78A7,24  DC\_8B\_n78A7 | DC\_8A\_n78A24  DC\_8B\_n78A | No | No |
| DC\_8A\_n78(2A)7, 21 | DC\_8A\_n78A21 | No | No |
| DC\_8A\_n79A7  DC\_8A\_n79C | DC\_8A\_n79A  DC\_8A\_n79C | No | No |
| DC\_8A\_n93A | DC\_8A\_n93A\_ULSUP-TDM | N/A |  |
| DC\_8A\_n94A | DC\_8A\_n94A\_ULSUP-TDM | N/A |  |
| DC\_11A\_n1A | DC\_11A\_n1A | No |  |
| DC\_11A\_n3A | DC\_11A\_n3A | No |  |
| DC\_11A\_n28A | DC\_11A\_n28A | No |  |
| DC\_11A\_n41A7 | DC\_11A\_n41A | No |  |
| DC\_11A\_n77A7 | DC\_11A\_n77A | No | No |
| DC\_11A\_n77(2A)7  DC\_11A\_n77(3A)7 | DC\_11A\_n77A | No | No |
| DC\_11A\_n78A7 | DC\_11A\_n78A | No | No |
| DC\_11A\_n78(2A) | DC\_11A\_n78A | No | No |
| DC\_11A\_n79A7 | DC\_11A\_n79A | No |  |
| DC\_12A\_n2A | DC\_12A\_n2A | No |  |
| DC\_12A\_n2(2A) | DC\_12A\_n2A | No |  |
| DC\_12A\_n5A | DC\_12A\_n5A | No |  |
| DC\_12A\_n7A | DC\_12A\_n7A | No |  |
| DC\_12A\_n7(2A) | DC\_12A\_n7A | No |  |
| DC\_12A\_n25A | DC\_12A\_n25A | No |  |
| DC\_12A\_n30A | DC\_12A\_n30A | No |  |
| DC\_12A\_n38A | DC\_12A\_n38A | No |  |
| DC\_12A\_n41A | DC\_12A\_n41A | No |  |
| DC\_12A\_n66A | DC\_12A\_n66A | No |  |
| DC\_12A\_n66(2A) | DC\_12A\_n66A | No |  |
| DC\_12A\_n71A | DC\_12A\_n71A18,19 | DC\_12\_n71 |  |
| DC\_12A\_n77A | DC\_12A\_n77A | DC\_12\_n77 |  |
| DC\_12A\_n77(2A) 21 | DC\_12A\_n77A21 | DC\_12\_n77 |  |
| DC\_12A\_n78A | DC\_12A\_n78A | DC\_12\_n78 |  |
| DC\_12A\_n78(2A) | DC\_12A\_n78A | DC\_12\_n78 |  |
| DC\_13A\_n2A | DC\_13A\_n2A | No |  |
| DC\_13A\_n5A | DC\_13A\_n5A | DC\_13\_n5 |  |
| DC\_13A\_n7A | DC\_13A\_n7A | No |  |
| DC\_13A\_n7(2A) | DC\_13A\_n7A | No |  |
| DC\_13A\_n25A | DC\_13A\_n25A | No |  |
| DC\_13A\_n48A  DC\_13A\_n48B | DC\_13A\_n48A | No |  |
| DC\_13A\_n66A | DC\_13A\_n66A | No |  |
| DC\_13A\_n71A | DC\_13A\_n71A | No |  |
| DC\_13A\_n77A  DC\_13A\_n77C21 | DC\_13A\_n77A21 | No |  |
| DC\_13A\_n78A | DC\_13A\_n78A | No |  |
| DC\_13A\_n78(2A) 21 | DC\_13A\_n78A21 | No |  |
| DC\_14A\_n2A | DC\_14A\_n2A | No |  |
| DC\_14A\_n5A | DC\_14A\_n5A | DC\_14\_n5 |  |
| DC\_14A\_n30A | DC\_14A\_n30A | No |  |
| DC\_14A\_n41A | DC\_14A\_n41A | No |  |
| DC\_14A\_n66A | DC\_14A\_n66A | No |  |
| DC\_14A\_n77A | DC\_14A\_n77A | No |  |
| DC\_14A\_n77(2A) 21 | DC\_14A\_n77A21 | No |  |
| DC\_18A\_n3A | DC\_18A\_n3A | No |  |
| DC\_18A\_n28A | DC\_18A\_n28A | No |  |
| DC\_18A\_n41A16 | DC\_18A\_n41A | No |  |
| DC\_18A\_n77A7  DC\_18A\_n77(2A)7 | DC\_18A\_n77A | No | No |
| DC\_18A\_n78A7 | DC\_18A\_n78A | No | No |
| DC\_18A\_n78(2A)7 | DC\_18A\_n78A | No | No |
| DC\_20A\_n91A | DC\_20A\_n91A\_ULSUP-TDM | N/A |  |
| DC\_20A\_n92A | DC\_20A\_n92A\_ULSUP-TDM | N/A |  |
| DC\_18A\_n79A7 | DC\_18A\_n79A | No |  |
| DC\_19A\_n1A | DC\_19A\_n1A | No |  |
| DC\_19A\_n77A7  DC\_19A\_n77C7 | DC\_19A\_n77A | No |  |
| DC\_19A\_n77(2A)7, 21 | DC\_19A\_n77A, 21 | No |  |
| DC\_19A\_n78A7  DC\_19A\_n78C7 | DC\_19A\_n78A | No | No |
| DC\_19A\_n78(2A)7, 21 | DC\_19A\_n78A, 21 | No | No |
| DC\_19A\_n79A7  DC\_19A\_n79C7 | DC\_19A\_n79A | No | No |
| DC\_20A\_n1A | DC\_20A\_n1A | No |  |
| DC\_20A\_n3A | DC\_20A\_n3A | No |  |
| DC\_20A\_n7A | DC\_20A\_n7A | DC\_20\_n7 |  |
| DC\_20A\_n8A | DC\_20A\_n8A | DC\_20\_n8 |  |
| DC\_20A\_n28A8,11,13 | DC\_20A\_n28A | No |  |
| DC\_20A\_n38A | DC\_20A\_n38A | No |  |
| DC\_20A\_n40A | DC\_20A\_n40A | No |  |
| DC\_20A\_n41A | DC\_20A\_n41A | DC\_20\_n41 |  |
| DC\_20A\_n50A | DC\_20A\_n50A | No |  |
| DC\_20A\_n51A | DC\_20A\_n51A | No |  |
| DC\_20A\_n77A7 | DC\_20A\_n77A | No |  |
| DC\_20A\_n78A7,24  DC\_20A\_n78C7 | DC\_20A\_n78A24 | No |  |
| DC\_20A\_n78(2A)7 | DC\_20A\_n78A | No |  |
| DC\_21A\_n1A | DC\_21A\_n1A | No |  |
| DC\_21A\_n28A17 | DC\_21A\_n28A | DC\_21\_n28 |  |
| DC\_21A\_n77A7  DC\_21A\_n77C7 | DC\_21A\_n77A | No |  |
| DC\_21A\_n77(2A)7,21 | DC\_21A\_n77A,21 | No |  |
| DC\_21A\_n78A7  DC\_21A\_n78C7 | DC\_21A\_n78A | No | No |
| DC\_21A\_n78(2A)7,21 | DC\_21A\_n78A,21 | No | No |
| DC\_21A\_n79A7  DC\_21A\_n79C7 | DC\_21A\_n79A | No | No |
| DC\_25A\_n41A | DC\_25A\_n41A | No |  |
| DC\_25A-25A\_n41A | DC\_25A\_n41A | No |  |
| DC\_25A\_n77A | DC\_25A\_n77A | DC\_25\_n77 |  |
| DC\_25A-25A\_n77A | DC\_25A\_n77A | DC\_25\_n77 |  |
| DC\_25A\_n78A | DC\_25A\_n78A | DC\_25\_n78 |  |
| DC\_25A-25A\_n78A | DC\_25A\_n78A | DC\_25\_n78 |  |
| DC\_26A\_n25A | DC\_26A\_n25A | No |  |
| DC\_26A\_n41A | DC\_26A\_n41A | No |  |
| DC\_26A\_n77A7 | DC\_26A\_n77A | No |  |
| DC\_26A\_n78A7 | DC\_26A\_n78A | No |  |
| DC\_26A\_n78(2A) | DC\_26A\_n78A | No |  |
| DC\_26A\_n79A7 | DC\_26A\_n79A | No |  |
| DC\_28A\_n1A | DC\_28A\_n1A | No |  |
| DC\_28A\_n2A | DC\_28A\_n2A | No |  |
| DC\_28A\_n3A | DC\_28A\_n3A | No |  |
| DC\_28A\_n5A | DC\_28A\_n5A | No |  |
| DC\_28A\_n7A  DC\_28A\_n7B | DC\_28A\_n7A  DC\_28A\_n7B | No |  |
| DC\_28A\_n51A | DC\_28A\_n51A | No |  |
| DC\_28A\_n8A | DC\_28A\_n8A | No |  |
| DC\_28A\_n20A8,11,13 | DC\_28A\_n20A | No |  |
| DC\_28A\_n38A | DC\_28A\_n38A | No |  |
| DC\_28A\_n40A | DC\_28A\_n40A | No |  |
| DC\_28A\_n41A7 | DC\_28A\_n41A | No |  |
| DC\_28A\_n50A | DC\_28A\_n50A | No |  |
| DC\_28A\_n66A | DC\_28A\_n66A | No |  |
| DC\_28A\_n77A7  DC\_28A\_n77C7 | DC\_28A\_n77A | No | No |
| DC\_28A\_n77(2A)7 | DC\_28A\_n77A | No | No |
| DC\_28A\_n78A7,24  DC\_28A\_n78C7 | DC\_28A\_n78A24 | No | No |
| DC\_28A\_n78(2A)7 | DC\_28A\_n78A | No | No |
| DC\_28A\_n79A7  DC\_28A\_n79C7 | DC\_28A\_n79A | No |  |
| DC\_30A\_n2A | DC\_30A\_n2A | No |  |
| DC\_30A\_n5A | DC\_30A\_n5A | No |  |
| DC\_30A\_n66A | DC\_30A\_n66A | No |  |
| DC\_30A\_n77A | DC\_30A\_n77A | No |  |
| DC\_30A\_n77(2A) 21 | DC\_30A\_n77A21 | No |  |
| DC\_38A\_n1A | DC\_38A\_n1A | No |  |
| DC\_38A\_n3A | DC\_38A\_n3A | No |  |
| DC\_38A\_n8A | DC\_38A\_n8A | No |  |
| DC\_38A\_n28A | DC\_38A\_n28A | No |  |
| DC\_38A\_n78A7 | DC\_38A\_n78A | No |  |
| DC\_38A\_n79A  DC\_38A\_n79C | DC\_38A\_n79A | No |  |
| DC\_39A\_n40A3 | DC\_39A\_n40A | No |  |
| DC\_39A\_n41A  DC\_39C\_n41A  DC\_39A\_n41C | DC\_39A\_n41A  DC\_39C\_n41A | No | No |
| DC\_39A\_n78A5,7 | DC\_39A\_n78A | No |  |
| DC\_39A\_n79A7  DC\_39A\_n79C7 | DC\_39A\_n79A | No | No |
| DC\_40A\_n1A  DC\_40C\_n1A | DC\_40A\_n1A | No |  |
| DC\_40A\_n3A | DC\_40A\_n3A | No |  |
| DC\_40A\_n7A | DC\_40A\_n7A | No |  |
| DC\_40A\_n41A  DC\_40A\_n41C  DC\_40C\_n41A | DC\_40A\_n41A | No |  |
| DC\_40A\_n41(2A) | DC\_40A\_n41A | No |  |
| DC\_40A\_n77A  DC\_40A\_n77C  DC\_40C\_n77A  DC\_40C\_n77C | DC\_40A\_n77A | No |  |
| DC\_40A\_n78A  DC\_40C\_n78A  DC\_40A\_n78C  DC\_40C\_n78C | DC\_40A\_n78A23  DC\_40C\_n78A | No |  |
| DC\_40A\_n78(2A)  DC\_40C\_n78(2A) | DC\_40A\_n78A  DC\_40C\_n78A | No |  |
| DC\_40A\_n79A7,12  DC\_40A\_n79C7,12  DC\_40C\_n79A7,12 | DC\_40A\_n79A | No | No |
| DC\_41A\_n1A  DC\_41C\_n1A | DC\_41A\_n1A  DC\_41C\_n1A | No | DC\_41A\_n1A  DC\_41C\_n1A |
| DC\_41A\_n3A7  DC\_41C\_n3A7 | DC\_41A\_n3A  DC\_41C\_n3A | No |  |
| DC\_41A\_n28A7  DC\_41C\_n28A7 | DC\_41A\_n28A  DC\_41C\_n28A | No |  |
| DC\_41A\_n77A  DC\_41C\_n77A | DC\_41A\_n77A  DC\_41C\_n77A | No |  |
| DC\_41A\_n77(2A)  DC\_41C\_n77(2A) | DC\_41A\_n77A  DC\_41C\_n77A | No |  |
| DC\_41A\_n78A24  DC\_41C\_n78A  DC\_41D\_n78A | DC\_41A\_n78A24  DC\_41C\_n78A | No |  |
| DC\_41A\_n78(2A)  DC\_41C\_n78(2A) | DC\_41A\_n78A  DC\_41C\_n78A | No |  |
| DC\_41A\_n79A6,7  DC\_41A\_n79C6,7  DC\_41C\_n79A6,7 | DC\_41A\_n79A  DC\_41C\_n79A | No | No |
| DC\_42A\_n1A7  DC\_42C\_n1A7 | DC\_42A\_n1A  DC\_42C\_n1A | No |  |
| DC\_42A\_n3A**7**  DC\_42C\_n3A7 | DC\_42A\_n3A  DC\_42C\_n3A | DC\_42\_n3 |  |
| DC\_42A\_n28A7  DC\_42C\_n28A7 | DC\_42A\_n28A  DC\_42C\_n28A | No |  |
| DC\_42A\_n51A | DC\_42A\_n51A | No |  |
| DC\_42A\_n77A3,4,9,11  DC\_42A\_n77C3,4,9,11  DC\_42C\_n77A3,4,9,11  DC\_42C\_n77C3,4,9,11  DC\_42D\_n77A3,4,9,11  DC\_42D\_n77C  DC\_42E\_n77A3,4,9,11  DC\_42E\_n77C | N/A | N/A |  |
| DC\_42A\_n77(2A)3,4,9,11  DC\_42C\_n77(2A)3,4,9,11 | N/A | N/A |  |
| DC\_42A\_n78A3,4,9,11  DC\_42A\_n78C3,4,9,11  DC\_42C\_n78A3,4,9,11  DC\_42C\_n78C3,4,9,11  DC\_42D\_n78A3,4,9,11  DC\_42D\_n78C3,4,9,11  DC\_42E\_n78A3,4,9,11  DC\_42E\_n78C3,4,9,11 | N/A | N/A |  |
| DC\_42A\_n79A9,15  DC\_42A\_n79C9,15  DC\_42C\_n79A9,15  DC\_42C\_n79C9,15  DC\_42D\_n79A9,15  DC\_42D\_n79C9,15  DC\_42E\_n79A9,15  DC\_42E\_n79C9,15 | N/A | N/A |  |
| DC\_46A\_n77A2 | N/A | N/A |  |
| DC\_46A\_n78A2  DC\_46C\_n78A2  DC\_46D\_n78A2  DC\_46E\_n78A2 | N/A | N/A |  |
| DC\_48A\_n2A  DC\_48C\_n2A  DC\_48D\_n2A  DC\_48E\_n2A | DC\_48A\_n2A | No |  |
| DC\_48A\_n5A  DC\_48C\_n5A  DC\_48D\_n5A  DC\_48E\_n5A | DC\_48A\_n5A | No |  |
| DC\_48A\_n12A | DC\_48A\_n12A | No |  |
| DC\_48A\_n25A  DC\_48C\_n25A  DC\_48D\_n25A | DC\_48A\_n25A | No |  |
| DC\_48A\_n46A  DC\_48B\_n46A  DC\_48C\_n46A  DC\_48D\_n46A  DC\_48E\_n46A  DC\_48A\_n46B  DC\_48B\_n46B  DC\_48C\_n46B  DC\_48D\_n46B  DC\_48E\_n46B  DC\_48A\_n46C  DC\_48B\_n46C  DC\_48C\_n46C  DC\_48D\_n46C  DC\_48E\_n46C  DC\_48A\_n46D  DC\_48B\_n46D  DC\_48C\_n46D  DC\_48D\_n46D  DC\_48E\_n46D | DC\_48A\_n46A  DC\_48B\_n46A | No |  |
| DC\_48A\_n66A  DC\_48C\_n66A  DC\_48D\_n66A  DC\_48E\_n66A | DC\_48A\_n66A | No |  |
| DC\_48A\_n71A  DC\_48B\_n71A  DC\_48C\_n71A  DC\_48D\_n71A | DC\_48A\_n71A | No |  |
| DC\_48A-48A\_n71A  DC\_48A-48A-48A\_n71A | DC\_48A\_n71A | No |  |
| DC\_48A\_n77A3. 4. 9, 11  DC\_48C\_n77A3. 4. 9, 11  DC\_48A\_n77C3. 4. 9, 11  DC\_48C\_n77C3. 4. 9, 11  DC\_48D\_n77A3. 4. 9, 11  DC\_48D\_n77C3. 4. 9, 11  DC\_48E\_n77A3. 4. 9, 11 | N/A | N/A |  |
| DC\_48A-48A\_n77A | N/A | N/A |  |
| DC\_48A-48A-48A\_n77A | N/A | N/A |  |
| DC\_66A\_n2A  DC\_66B\_n2A  DC\_66C\_n2A | DC\_66A\_n2A | DC\_66\_n2 |  |
| DC\_66A\_n2(2A) | DC\_66A\_n2A | DC\_66\_n2 |  |
| DC\_66A-66A\_n2A | DC\_66A\_n2A | DC\_66\_n2 |  |
| DC\_66A-66A-66A\_n2A | DC\_66A\_n2A | DC\_66\_n2 |  |
| DC\_66A\_n5A  DC\_66B\_n5A  DC\_66C\_n5A | DC\_66A\_n5A | DC\_66\_n5 |  |
| DC\_66A-66A\_n5A | DC\_66A\_n5A | DC\_66\_n5 |  |
| DC\_66A-66A-66A\_n5A | DC\_66A\_n5A | DC\_66\_n5 |  |
| DC\_66A\_n7A | DC\_66A\_n7A | No |  |
| DC\_66A\_n7(2A) | DC\_66A\_n7A | No |  |
| DC\_66A-66A\_n7A | DC\_66A\_n7A | No |  |
| DC\_66A-66A\_n7(2A) | DC\_66A\_n7A | No |  |
| DC\_66A\_n12A | DC\_66A\_n12A | No |  |
| DC\_66A\_n25A | DC\_66A\_n25A | DC\_66\_n25 |  |
| DC\_66A\_n28A | DC\_66A\_n28A | No |  |
| DC\_66A\_n30A | DC\_66A\_n30A | No |  |
| DC\_66A-66A\_n30A | DC\_66A\_n30A | No |  |
| DC\_66A\_n38A | DC\_66A\_n38A | No |  |
| DC\_66A-66A\_n38A | DC\_66A\_n38A | No |  |
| DC\_66A\_n41A  DC\_66A\_n41C | DC\_66A\_n41A | No |  |
| DC\_66A\_n41(2A) | DC\_66A\_n41A | No |  |
| DC\_66A\_n46A | DC\_66A\_n46A | No |  |
| DC\_66A\_n48A  DC\_66A\_n48B | DC\_66A\_n48A | No |  |
| DC\_66A-66A\_n48A  DC\_66A-66A\_n48B | DC\_66A\_n48A | No |  |
| DC\_66A\_n71A  DC\_66C\_n71A  DC\_66A\_n71B | DC\_66A\_n71A | No |  |
| DC\_66A-66A\_n71A | DC\_66A\_n71A | No |  |
| DC\_66A\_n77A  DC\_66A\_n77C21 | DC\_66A\_n77A21 | DC\_66\_n77 |  |
| DC\_66A\_n77(2A) 21 | DC\_66A\_n77A21 | DC\_66\_n77 |  |
| DC\_66A-66A\_n77A21  DC\_66A-66A\_n77C21 | DC\_66A\_n77A21 | DC\_66\_n77 |  |
| DC\_66A-66A\_n77(2A) 21 | DC\_66A\_n77A21 | DC\_66\_n77 |  |
| DC\_66A-66A-66A\_n77A21  DC\_66A-66A-66A\_n77C21 | DC\_66A\_n77A21 | DC\_66\_n77 |  |
| DC\_66A-66A-66A\_n77(2A)21 | DC\_66A\_n77A21 | DC\_66\_n77 |  |
| DC\_66A\_n78A | DC\_66A\_n78A | No |  |
| DC\_66A\_n78(2A) 21 | DC\_66A\_n78A21 | No |  |
| DC\_66A-66A\_n78A21 | DC\_66A\_n78A21 | No |  |
| DC\_66A-66A\_n78(2A21) | DC\_66A\_n78A21 | No |  |
| DC\_71A\_n2A | DC\_71A\_n2A | No |  |
| DC\_71A\_n2(2A) | DC\_71A\_n2A | No |  |
| DC\_71A\_n5A | DC\_71A\_n5A | No |  |
| DC\_71A\_n12A | DC\_71A\_n12A18,19 | Yes |  |
| DC\_71A\_n38A | DC\_71A\_n38A | No |  |
| DC\_71A\_n7A | DC\_71A\_n7A | No |  |
| DC\_71A\_n25A | DC\_71A\_n7A | No |  |
| DC\_71A\_n41A | DC\_71A\_n41A | No |  |
| DC\_71A\_n48A | DC\_71A\_n48A | No |  |
| DC\_71A\_n66A | DC\_71A\_n66A | No |  |
| DC\_71A\_n77A  DC\_71A\_n77C | DC\_71A\_n77A | No |  |
| DC\_71A\_n77(2A) | DC\_71A\_n77A | No |  |
| DC\_71A\_n78A | DC\_71A\_n78A | No |  |
| DC\_71A\_n78(2A) 21 | DC\_71A\_n78A21 | No |  |
| NOTE 1: Uplink EN-DC configurations are the configurations supported by the present release of specifications.  NOTE 2: Restricted to E-UTRA operation when inter-band carrier aggregation is configured. The downlink operating band for Band 46 is paired with the uplink operating band (external E-UTRA band) of the carrier aggregation configuration that is supporting the configured Pcell.  NOTE 3: The minimum requirements apply only when there is non-simultaneous Tx/Rx operation between E-UTRA and NR carriers. This restriction applies also for these carriers when applicable EN-DC configuration is part of a higher order EN-DC configuration.  NOTE 4: For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, the minimum requirements for intra-band non-contiguous EN-DC apply for the Band 42/48 and Band n77/n78 combination. For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, when UE capability *interBandContiguousMRDC* is indicated, the minimum requirements for intra-band-contiguous EN-DC also should be met in addtion to intra-band non-contiguous EN-DC*.* The intra-band requirements also apply for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 5: The frequency range above 3600 MHz for Band n78 is not used in this combination.  NOTE 6: The frequency range below 2506 MHz for Band 41 is not used in this combination.  NOTE 7: Applicable for UE supporting inter-band EN-DC with mandatory simultaneous Rx/Tx capability.  NOTE 8: The frequency range in band n28 /28 is restricted for this band combination to 703 - 733 MHz for the UL and 758-788 MHz for the DL. This restriction also apply for any band combinations when DC\_20\_n28/DC\_28\_n20/CA\_20-28/CA\_n20-n28 is a subset of a higher order band combination.  NOTE 9: The combination is not used alone as fall back mode of other band combinations in which UL in Band 42 or Band 48 is not used.  NOTE 10: Void.  NOTE 11: For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, the minimum requirements apply when the maximum power spectral density imbalance between downlink carriers is within 6 dB. For UEs indicating interBandMRDC-WithOverlapDL-Bands-r16 and if [*nonCollocatedTypeMRDC-r18]* is not provided and UE is configured with *maxMIMO-Layers* with value less than or equal to 2, the power imbalance requirement defined in clause 7. 10B.3 apply. For UEs indicating [*interBandMRDC-WithOverlapDL-Bands-r16]* and [*requirementTypeIndication-r18]* and when [*nonCollocatedTypeMRDC-r18]* is provided, the minimum requirements apply when the maximum power spectral density imbalance between downlink carriers is within 6 dB. For these UEs, the power spectral density imbalance condition also applies for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 12: Applicable for frequency range above 4800 MHz for Band n79 in this combination.  NOTE 13: For UEs not indicating *interBandMRDC-WithOverlapDL-Bands-r16*, the minimum requirements apply for synchronized DL carriers with a maximum receive time difference ≤ 3 usec. The requirements also apply for these carriers when applicable EN-DC configuration is a subset of a higher order EN-DC configuration.  NOTE 14: Applicable when dynamic switching between two uplink carriers is conducted. The DL interruption requirements for NR DL carrier(s) and E-UTRA DL carrier(s) are specified in clause 8.2.1.2.14 of 38.133 [15] and clause 7.32.2.12 of 36.133 [16] respectively.  NOTE 15: Simultaneous Rx/Tx capability does not apply for UEs supporting band 42 with a n77 implementation only. Same restrictions are applied to related higher order configurations.  NOTE 16: The frequency range in band n41 is restricted for this band combination to 2595 – 2645 MHz.  NOTE 17: The frequency range in band n28 is restricted for this band combination to 728 - 738 MHz for the UL and 783 - 793 MHz for the DL. This restriction applies also for these band combinations when applicable EN-DC configuration is part of a higher order EN-DC configuration.  NOTE 18: Only single switched UL is supported.  NOTE 19: The implementation with 4 antennas is targeted for FWA form factor for this band combination.  NOTE 20: The combination is not used alone as fallback mode of other band combinations in which UL in Band 2 is not used.  NOTE 21: Minimum requirements for PC2 are applicable for this uplink EN-DC configuration in this downlink/uplink EN-DC configuration with 1Tx antenna connector in each band.  NOTE 22: The PC2 Uplink EN-DC configuration supported in Table 6.2B.1.3-1 is applicable to the same EN-DC configuration without additional indication of NOTE 21.  NOTE 23: Minimum requirements for Power Class 2 are applicable for this EN-DC configuration with 1Tx antenna connector in one band and 2Tx antenna connectors in the other band.  NOTE 24: Minimum requirements for Power Class 2 are applicable for this EN-DC configuration with 1Tx antenna connector in one band and 2Tx antenna connectors in the other band. | | | |

## **<<Next Change>>**

##### 6.2B.4.2.3 Inter-band EN-DC within FR1

###### 6.2B.4.2.3.1 ΔTIB,c for EN-DC two bands

Table 6.2B.4.2.3.1-1: ΔTIB,c due to EN-DC(two bands)

| **Inter-band EN-DC configuration** | | **ΔTIB,c for E-UTRA band / NR band (dB)7** | | | |
| --- | --- | --- | --- | --- | --- |
| **Component band in order of bands in configuration8** | | | |
| DC\_1\_n3 | | 0.3 | | 0.3 | |
| DC\_1\_n5 | | 0.3 | | 0.3 | |
| DC\_1\_n7  DC\_1-1\_n7 | | 0.5 | | 0.6 | |
| DC\_1\_n8 | | 0.3 | | 0.3 | |
| DC\_1\_n20 | | 0.3 | | 0.3 | |
| DC\_1\_n26 | | 0.3 | | 0.3 | |
| DC\_1\_n28 | | 0.3 | | 0.6 | |
| DC\_1\_n38 | | 0.5 | | 0.5 | |
| DC\_1\_n40 | | 0.5 | | 0.5 | |
| DC\_1\_n41 | | 0.5 | | 0.5 | |
| DC\_1\_n50 | | 0.5 | | 0.5 | |
| DC\_1\_n51 | | 0.6 | | 0.6 | |
| DC\_1\_n71 | | 0.3 | | 0.3 | |
| DC\_1\_n77 | | 0.6 | | 0.8 | |
| DC\_1\_n78 | | 0.3 | | 0.8 | |
| DC\_1\_n105 | | 0.3 | | 0.6 | |
| DC\_2\_n5  DC\_2-2\_n5 | | 0.3 | | 0.3 | |
| DC\_2\_n7  DC\_2-2\_n7 | | 0.5 | | 0.5 | |
| DC\_2\_n12 | | 0.3 | | 0.3 | |
| DC\_2\_n28 | | 0.3 | | 0.3 | |
| DC\_2\_n30 DC\_2-2\_n30 | | 0.5 | | 0.3 | |
| DC\_2\_n38  DC\_2-2\_n38 | | 0.5 | | 0.9 | |
| DC\_2\_n41  DC\_2-2\_n41 | | 0.5 | | 0.41 / 0.92 | |
| DC\_2\_n48 | | 0.6 | | 0.8 | |
| DC\_2\_n66  DC\_2-2\_n66 | | 0.5 | | 0.5 | |
| DC\_2\_n71  DC\_2-2\_n71 | | 0.3 | | 0.3 | |
| DC\_2\_n77  DC\_2-2\_n77 | | 0.6 | | 0.8 | |
| DC\_2\_n78  DC\_2-2\_n78 | | 0.6 | | 0.8 | |
| DC\_3\_n1 | | 0.3 | | 0.3 | |
| DC\_3\_n5 | | 0.3 | | 0.3 | |
| DC\_3\_n8  DC\_3-3\_n8 | | 0.3 | | 0.3 | |
| DC\_3\_n7  DC\_3-3\_n7 | | 0.5 | | 0.5 | |
| DC\_3\_n105 | | 0.3 | | 0.6 | |
| DC\_3\_n20 | | 0.3 | | 0.3 | |
| DC\_3\_n26 | | 0.3 | | 0.3 | |
| DC\_3\_n28 | | 0.3 | | 0.3 | |
| DC\_3\_n34 | | 0.5 | | 0.5 | |
| DC\_3\_n38 | | 0.5 | | 0.5 | |
| DC\_3\_n40 | | 0.5 | | 0.5 | |
| DC\_3-n41 | | 0.5 | | 0.33 / 0.84 | |
| DC\_3\_n50 | | 0.5 | | 0.5 | |
| DC\_3\_n51 | | 0.3 | | 0.3 | |
| DC\_3\_n71 | | 0.3 | | 0.3 | |
| DC\_3\_n77  DC\_3-3\_n77 | | 0.6 | | 0.8 | |
| DC\_3\_n78  DC\_3-3\_n78 | | 0.6 | | 0.8 | |
| DC\_4\_n2 | | 0.5 | | 0.5 | |
| DC\_4\_n5 | | 0.3 | | 0.3 | |
| DC\_4\_n7 | | 0.5 | | 0.5 | |
| DC\_4\_n28 | | 0.3 | | 0.6 | |
| DC\_4\_n38 | | 0.5 | | 0.8 | |
| DC\_4\_n41 | | 0.5 | | 0.81 / 1.32 | |
| DC\_4\_n78 | | 0.6 | | 0.8 | |
| DC\_5\_n1 | | 0.3 | | 0.3 | |
| DC\_5\_n2  DC\_5-5\_n2 | | 0.3 | | 0.3 | |
| DC\_5\_n3 | | 0.3 | | 0.3 | |
| DC\_5\_n7 | | 0.3 | | 0.3 | |
| DC\_5\_n12 | | 0.8 | | 0.4 | |
| DC\_5\_n25 | | 0.3 | | 0.3 | |
| DC\_5\_n28 | | 0.5 | | 0.5 | |
| DC\_5\_n30 | | 0.3 | | 0.3 | |
| DC\_5\_n38 | | 0.3 | | 0.3 | |
| DC\_5\_n40 | | 0.3 | | 0.3 | |
| DC\_5\_n41 | | 0.6 | | 0.3 | |
| DC\_5\_n48 | | 0.3 | | 0.3 | |
| DC\_5\_n66  DC\_5-5\_n66 | | 0.3 | | 0.3 | |
| DC\_5\_n71 | | 0.5 | | 0.5 | |
| DC\_5\_n77 | | 0.6 | | 0.8 | |
| DC\_5\_n78 | | 0.6 | | 0.8 | |
| DC\_7\_n1  DC\_7-7\_n1 | | 0.6 | | 0.5 | |
| DC\_7\_n2 | | 0.5 | | 0.5 | |
| DC\_7\_n3 | | 0.5 | | 0.5 | |
| DC\_7\_n5  DC\_7-7\_n5 | | 0.3 | | 0.3 | |
| DC\_7\_n8  DC\_7-7\_n8 | | 0.3 | | 0.6 | |
| DC\_7\_n12 | | 0.3 | | 0.3 | |
| DC\_7\_n20 | | 0.3 | | 0.3 | |
| DC\_7\_n25 | | 0.5 | | 0.5 | |
| DC\_7\_n26 | | 0.3 | | 0.3 | |
| DC\_7\_n28,  DC\_7-7\_n28 | | 0.3 | | 0.3 | |
| DC\_7\_n40  DC\_7-7\_n40 | | 0.5 | | 0.6 | |
| DC\_7\_n51 | | 0.3 | | 0.3 | |
| DC\_7\_n71 | | 0.3 | | 0.6 | |
| DC\_7\_n66  DC\_7-7\_n66 | | 0.5 | | 0.5 | |
| DC\_7\_n77  DC\_7-7\_n77 | | 0.5 | | 0.8 | |
| DC\_7\_n78  DC\_7-7\_n78 | | 0.5 | | 0.8 | |
| DC\_7\_n79  DC\_7-7\_n79 | | 0.5 | | 0.8 | |
| DC\_7\_n105 | | 0.3 | | 0.6 | |
| DC\_8\_n1 | | 0.3 | | 0.3 | |
| DC\_8\_n2 | | 0.3 | | 0.3 | |
| DC\_8\_n3 | | 0.3 | | 0.3 | |
| DC\_8\_n7 | | 0.6 | | 0.3 | |
| DC\_8\_n20 | | 0.4 | | 0.4 | |
| DC\_8\_n28 | | 0.6 | | 0.5 | |
| DC\_8\_n34 | | 0.3 | | 0.3 | |
| DC\_8\_n38 | | 0.6 | | 0.3 | |
| DC\_8\_n39 | | 0.3 | | 0.3 | |
| DC\_8\_n40 | | 0.3 | | 0.3 | |
| DC\_8\_n41 | | 0.3 | | 0.3 | |
| DC\_8\_n77 | | 0.6 | | 0.8 | |
| DC\_8\_n78 | | 0.6 | | 0.8 | |
| DC\_11\_n1 | | 0.3 | | 0.3 | |
| DC\_11\_n3 | | 0.8 | | 0.9 | |
| DC\_11\_n28 | | 0.4 | | 0.6 | |
| DC\_11\_n41 | | 0.3 | | 0.3 | |
| DC\_11\_n77 | | 0.4 | | 0.8 | |
| DC\_11\_n78 | | 0.4 | | 0.8 | |
| DC\_12\_n2 | | 0.3 | | 0.3 | |
| DC\_12\_n5 | | 0.4 | | 0.8 | |
| DC\_12\_n7 | | 0.3 | | 0.3 | |
| DC\_12\_n25 | | 0.3 | | 0.3 | |
| DC\_12\_n30 | | 0.3 | | 0.3 | |
| DC\_12\_n38 | | 0.3 | | 0.3 | |
| DC\_12\_n41 | | 0.3 | | 0.3 | |
| DC\_12\_n66 | | 0.8 | | 0.3 | |
| DC\_12\_n71 | | 1 | | 1 | |
| DC\_12\_n77 | | 0.5 | | 0.8 | |
| DC\_12\_n78 | | 0.5 | | 0.8 | |
| DC\_13\_n2 | | 0.3 | | 0.3 | |
| DC\_13\_n5 | | 0.5 | | 0.5 | |
| DC\_13\_n7 | | 0.5 | | 0.5 | |
| DC\_13\_n25 | | 0.3 | | 0.3 | |
| DC\_13\_n48 | | 0.3 | | 0.3 | |
| DC\_13\_n66 | | 0.3 | | 0.3 | |
| DC\_13\_n71 | | 0.5 | | 0.5 | |
| DC\_13\_n77 | | 0.5 | | 0.8 | |
| DC\_13\_n78 | | 0.5 | | 0.8 | |
| DC\_14\_n2 | | 0.3 | | 0.3 | |
| DC\_14\_n5 | | 0.5 | | 0.5 | |
| DC\_14\_n30 | | 0.3 | | 0.3 | |
| DC\_14\_n41 | | 0.3 | | 0.3 | |
| DC\_14\_n66 | | 0.3 | | 0.3 | |
| DC\_14\_n77 | | 0.5 | | 0.8 | |
| DC\_18\_n3 | | 0.3 | | 0.3 | |
| DC\_18\_n28 | | 0.5 | | 0.5 | |
| DC\_18\_n41 | | 0.3 | | 0.33 | |
| DC\_18\_n77 | | 0.3 | | 0.8 | |
| DC\_18\_n78 | | 0.3 | | 0.8 | |
| DC\_19\_n1 | | 0.3 | | 0.3 | |
| DC\_19\_n77 | | 0.3 | | 0.8 | |
| DC\_19\_n78 | | 0.3 | | 0.8 | |
| DC\_20\_n1 | | 0.3 | | 0.3 | |
| DC\_20\_n3 | | 0.3 | | 0.3 | |
| DC\_20\_n7 | | 0.3 | | 0.3 | |
| DC\_20\_n8 | | 0.4 | | 0.4 | |
| DC\_20\_n28 | | 0.5 | | 0.5 | |
| DC\_20\_n38 | | 0.5 | | 0.3 | |
| DC\_20\_n40 | | 0.3 | | 0.3 | |
| DC\_20\_n41 | | 0.3 | | 0.3 | |
| DC\_20\_n50 | | 0.3 | | 0.4 | |
| DC\_20\_n51 | | 0.5 | | 0.5 | |
| DC\_20\_n77 | | 0.6 | | 0.8 | |
| DC\_20\_n78 | | 0.6 | | 0.8 | |
| DC\_21\_n1 | | 0.3 | | 0.3 | |
| DC\_21\_n28 | | 0.4 | | 0.3 | |
| DC\_21\_n77 | | 0.4 | | 0.8 | |
| DC\_21\_n78 | | 0.4 | | 0.8 | |
| DC\_25\_n41,  DC\_25-25\_n41 | | 0.5 | | 0.41 / 0.92 | |
| DC\_25\_n77  DC\_25-25\_n77 | | 0.6 | | 0.8 | |
| DC\_25\_n78  DC\_25-25\_n78 | | 0.6 | | 0.8 | |
| DC\_26\_n25 | | 0.3 | | 0.3 | |
| DC\_26\_n41 | | 0.3 | | 0.3 | |
| DC\_26\_n77 | | 0.3 | | 0.8 | |
| DC\_26\_n78 | | 0.3 | | 0.8 | |
| DC\_28\_n1 | | 0.6 | | 0.3 | |
| DC\_28\_n2 | | 0.3 | | 0.3 | |
| DC\_28\_n3 | | 0.3 | | 0.3 | |
| DC\_28\_n5 | | 0.5 | | 0.5 | |
| DC\_28\_n7 | | 0.3 | | 0.3 | |
| DC\_28\_n8 | | 0.5 | | 0.6 | |
| DC\_28\_n20 | | 0.5 | | 0.5 | |
| DC\_28\_n38 | | 0.3 | | 0.3 | |
| DC\_28\_n40 | | 0.3 | | 0.3 | |
| DC\_28\_n41 | | 0.3 | | 0.3 | |
| DC\_28\_n50 | | 0.3 | | 0.4 | |
| DC\_28\_n51 | | 0.5 | | 0.5 | |
| DC\_28\_n66 | | 0.6 | | 0.3 | |
| DC\_28\_n77 | | 0.5 | | 0.8 | |
| DC\_28\_n78 | | 0.5 | | 0.8 | |
| DC\_30\_n2 | | 0.3 | | 0.5 | |
| DC\_30\_n5 | | 0.3 | | 0.3 | |
| DC\_30\_n66 | | 0.5 | | 0.8 | |
| DC\_30\_n77 | | 0.5 | | 0.8 | |
| DC\_38\_n1 | | 0.5 | | 0.5 | |
| DC\_38\_n3 | | 0.5 | | 0.5 | |
| DC\_38\_n8 | | 0.6 | | 0.3 | |
| DC\_38\_n28 | | 0.3 | | 0.3 | |
| DC\_38\_n78 | | - | | 0.5 | |
| DC\_38\_n79 | | 0.3 | | 0.8 | |
| DC\_39-n41 | | 0.5 | | 0.5 | |
| DC\_39\_n78 | | 0.3 | | 0.8 | |
| DC\_39\_n79 | | 0.3 | | 0.8 | |
| DC\_40\_n1 | | 0.5 | | 0.5 | |
| DC\_40\_n3 | | 0.5 | | 0.5 | |
| DC\_40\_n7 | | 0.6 | | 0.5 | |
| DC\_40\_n41 | | 0.5 | | 0.5 | |
| DC\_40\_n77 | | - | | 0.5 | |
| DC\_40\_n78 | | - | | 0.56 | |
| DC\_40\_n79 | | 0.3 | | 0.8 | |
| DC\_41\_n1 | | 0.5 | | 0.5 | |
| DC\_41\_n3 | | 0.33 / 0.84 | | 0.5 | |
| DC\_41\_n28 | | 0.3 | | 0.3 | |
| DC\_41\_n77 | | 0.3 | | 0.8 | |
| DC\_41\_n78 | | 0.3 | | 0.8 | |
| DC\_41\_n79 | | 0.3 | | 0.8 | |
| DC\_42\_n1 | | 0.8 | | 0.3 | |
| DC\_42\_n3 | | 0.8 | | 0.6 | |
| DC\_42\_n28 | | 0.5 | | 0.8 | |
| DC\_42\_n51 | | 0.6 | | 0.8 | |
| DC\_48\_n2 | | 0.8 | | 0.6 | |
| DC\_48\_n5 | | 0.3 | | 0.3 | |
| DC\_48\_n12 | | 0.3 | | 0.3 | |
| DC\_48\_n25 | | 0.8 | | 0.6 | |
| DC\_48\_n46 | | 0.8 | | - | |
| DC\_48\_n66 | | 0.8 | | 0.6 | |
| DC\_48\_n71  DC\_48-48\_n71  DC\_48-48-48\_n71 | | 0.3 | | 0.3 | |
| DC\_66\_n2  DC\_66-66\_n2  DC\_66-66-66\_n2 | | 0.5 | | 0.5 | |
| DC\_66\_n5,  DC\_66-66\_n5,  DC\_66-66-66\_n5 | | 0.3 | | 0.3 | |
| DC\_66\_n7  DC\_66-66\_n7 | | 0.5 | | 0.5 | |
| DC\_66\_n12 | | 0.8 | | 0.3 | |
| DC\_66\_n25 | | 0.5 | | 0.5 | |
| DC\_66\_n28 | | 0.3 | | 0.6 | |
| DC\_66\_n30  DC\_66-66\_n30 | | 0.5 | | 0.8 | |
| DC\_66\_n38  DC\_66-66\_n38 | | 0.5 | | 0.5 | |
| DC\_66\_n41 | | 0.5 | | 0.81 / 1.32 | |
| DC\_66\_n48  DC\_66-66\_n48 | | 0.6 | | 0.8 | |
| DC\_66\_n71  DC\_66-66\_n71 | | 0.3 | | 0.3 | |
| DC\_66\_n77  DC\_66-66\_n77  DC\_66-66-66\_n77 | | 0.6 | | 0.8 | |
| DC\_66\_n78  DC\_66-66\_n78 | | 0.6 | | 0.8 | |
| DC\_71\_n2 | | 0.3 | | 0.3 | |
| DC\_71\_n5 | | 0.5 | | 0.5 | |
| DC\_71\_n7 | | 0.6 | | 0.3 | |
| DC\_71\_n12 | | 1 | | 1 | |
| DC\_71\_n25 | | 0.3 | | 0.3 | |
| DC\_71\_n38 | | 0.6 | | 0.3 | |
| DC\_71\_n41 | | 0.6 | | 0.3 | |
| DC\_71\_n48 | | 0.3 | | 0.3 | |
| DC\_71\_n66 | | 0.3 | | 0.3 | |
| DC\_71\_n77 | | 0.5 | | 0.8 | |
| DC\_71\_n78 | | 0.5 | | 0.8 | |
| NOTE 1: The requirement is applied for UE transmitting on the frequency range of 2545-2690 MHz.  NOTE 2: The requirement is applied for UE transmitting on the frequency range of 2496-2545 MHz.  NOTE 3: Applicable for the frequency range of 2515 – 2690 MHz.  NOTE 4: Applicable for the frequency range of 2496 - 2515 MHz.  NOTE 5: Applicable for UE supporting inter-band EN-DC without simultaneous Rx/Tx.  NOTE 6: Only applicable for UE supporting inter-band carrier aggregation with uplink in one E-UTRA band and without simultaneous Rx/Tx.  NOTE 7: “-” denotes ΔTIB,c = 0.  NOTE 8: The component band order in the configuration should be listed by the order of E-UTRA band and NR band respectively. | | | | | |

## **<<Next Change>>**

##### 7.3B.2.3.4 Reference sensitivity exceptions due to cross band isolation for EN-DC in NR FR1

Sensitivity degradation is allowed for a band if it is impacted by UL of another band part of the same EN-DC configuration due to cross band isolation issues. Reference sensitivity exceptions for the victim band are specified only for the specific uplink and downlink test points specified in Table 7.3B.2.3.4-1 and Table 7.3B.2.3.4-1a.

In Tables 7.3B.2.3.4-1 and 7.3B.2.3.4-1a the following terminology is used to define the source of cross-band isolation interference:

- “ACLR1” indicates that the first adjacent channel of the aggressor UL band falls into the Rx channel of victim band.

- “ACLR2” indicates that the second adjacent channel of the aggressor UL band falls into the Rx channel of victim band.

- “>ACLR2” indicates that neither the first, nor the second adjacent channel of the aggressor UL band falls into the Rx channel of victim band.

Table 7.3B.2.3.4-1: Reference sensitivity exceptions (MSD) due to cross band isolation and uplink/downlink configurations for PC3 EN-DC in NR FR1

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **UL band** | **DL band** | **UL Fc** | **UL BW** | **SCS of UL band** | **UL RB Allocation** | **DL Fc** | **DL BW** | **MSD** | **Cross-band**  **Interference**  **source** |
| **(MHz)** | **(MHz)** | **(kHz)** | **LCRB** | **(MHz)** | **(MHz)** | **(dB)** |
| n1 | 3 | 1922.5 | 5 | 15 | 25 (RBSTART=0) | 1877.5 | 5 | 3 | >ACLR2 |
| n1 | 3 | 1945 | 50 | 15 | 128 (RBSTART=0) | 1877.5 | 5 | 19.7 | ACLR1 |
| n1 | 38 | 1955 | 50 | 15 | 128 (RBSTART=142) | 2572.5 | 5 | 2.9 | >ACLR2 |
| n1 | 40 | 1955 | 50 | 15 | 128 (RBSTART=142) | 2302.5 | 5 | 6.6 | >ACLR2 |
| n1 | 41 | 1955 | 50 | 15 | 128 (RBSTART=142) | 2498.5 | 5 | 6.1 | >ACLR2 |
| 1 | n3 | 1930 | 20 | 15 | 100 (RBSTART=0) | 1877.5 | 5 | [7.9] | >ACLR2 |
| 1 | n3 | 1922.5 | 5 | 15 | 25 (RBSTART=0) | 1877.5 | 5 | 3 | >ACLR2 |
| 1 | n40 | 1970 | 20 | 15 | 100 (RBSTART=0) | 2302.5 | 5 | 6.6 | >ACLR2 |
| 1 | n41 | 1970 | 20 | 15 | 100 (RBSTART=0) | 2501 | 10 | 6.1 | >ACLR2 |
| n3 | 11 | 1735 | 50 | 15 | 50 (RBSTART=0) | 1493.4 | 5 | 6.4 | >ACLR2 |
| n3 | 11 | 1712.5 | 5 | 15 | 25 (RBSTART=0) | 1493.4 | 5 | 6.4 | >ACLR2 |
| n3 | 41 | 1760 | 50 | 15 | 50 (RBSTART=220) | 2498.5 | 5 | 0.7 | >ACLR2 |
| 3 | n41 | 1775 | 20 | 15 | 50 (RBSTART=50) | 2501 | 10 | 0.7 | >ACLR2 |
| 3 | n51 | 1720 | 20 | 15 | 50 (RBSTART=0) | 1429.5 | 5 | 6.4 | >ACLR2 |
| 3 | n51 | 1712.5 | 5 | 15 | 25 (RBSTART =0) | 1429.5 | 5 | 6.4 | >ACLR2 |
| n5 | 28 | 834 | 20 | 15 | 20 (RBSTART=0) | 800.5 | 5 | 17.5 | ACLR2 |
| 5 | n28 | 829 | 10 | 15 | 25 (RBSTART=0) | 800.5 | 5 | 11.4 | >ACLR2 |
| 7 | n40 | 2510 | 20 | 15 | 75 (RBSTART=0) | 2397.5 | 5 | 3.7 | >ACLR2 |
| n12 | 71 | 706.5 | 15 | 15 | 20 (RBSTART=0) | 649.5 | 5 | 3.8 | >ACLR2 |
| 12 | n71 | 704 | 10 | 15 | 20 (RBSTART=0) | 649.5 | 5 | 3.8 | >ACLR2 |
| 18 | n287 | 822.5 | 15 | 15 | 25 (RBSTART=0) | 800.5 | 5 | 31.3 | ACLR1 |
| 18 | n28 | 822.5 | 15 | 15 | 25 (RBSTART=0) | 785.5 | 5 | 12.7 | ACLR2 |
| n25 | 2 | 1895 | 40 | 15 | 40 (RBSTART=176) | 1932.5 | 5 | 33 | ACLR1 |
| 30 | n66 | 2310 | 10 | 15 | 25 (RBSTARTt=0) | 2197.5 | 5 | 8.3 | >ACLR2 |
| n34 | 3 | 2017.5 | 15 | 15 | 75 (RBSTART=0) | 1877.5 | 5 | 3 | >ACLR2 |
| n38 | 1 | 2590 | 40 | 15 | 216 (RBSTART=0) | 2167.5 | 5 | 1.9 | >ACLR2 |
| n38 | 2 | 2590 | 40 | 15 | 216 (RBSTART=0) | 1987.5 | 5 | 0.6 | >ACLR2 |
| n38 | 4 | 2590 | 40 | 15 | 216 (RBSTART=0) | 2152.5 | 5 | 1.9 | >ACLR2 |
| n38 | 66 | 2590 | 40 | 15 | 216 (RBSTART=0) | 2197.5 | 5 | 1.9 | >ACLR2 |
| 38 | n1 | 2580 | 20 | 15 | 100 (RBSTART=0) | 2167.5 | 5 | 1.9 | >ACLR2 |
| 39 | n41 | 1910 | 20 | 15 | 100 (RBSTART=0) | 2501 | 10 | [3.3] | >ACLR2 |
| n40 | 1 | 2350 | 100 | 30 | 270 (RBSTART=0) | 2167.5 | 5 | [21.9] | ACLR2 |
| n40 | 7 | 2350 | 100 | 30 | 270 (RBSTART=3) | 2622.5 | 5 | 22.3 | >ACLR2 |
| 40 | n1 | 2310 | 20 | 15 | 100 (RBSTART=0) | 2167.5 | 5 | 8.3 | >ACLR2 |
| 40 | n7 | 2390 | 20 | 15 | 100 (RBSTART=0) | 2622.5 | 5 | [22.3] | >ACLR2 |
| n41 | 1 | 2546 | 100 | 30 | 270 (RBSTART=0) | 2167.5 | 5 | 9.1 | >ACLR2 |
| n41 | 2 | 2546 | 100 | 30 | 270 (RBSTART=0) | 1987.5 | 5 | 0.6 | >ACLR2 |
| n41 | 3 | 2546 | 100 | 30 | 270 (RBSTART=0) | 1877.5 | 5 | 0.6 | >ACLR2 |
| n41 | 4 | 2546 | 100 | 30 | 270 (RBSTART=0) | 2152.5 | 5 | 3.5 | >ACLR2 |
| n41 | 25 | 2546 | 100 | 30 | 270 (RBSTART=0) | 1992.5 | 5 | 0.6 | >ACLR2 |
| n41 | 39 | 2546 | 100 | 30 | 270 (RBstart=3) | 1917.5 | 5 | 1.6 | >ACLR2 |
| n41 | 40 | 2546 | 100 | 30 | 270 (RBstart=0) | 2397.5 | 5 | 31.4 | ACLR2 |
| n41 | 661 | 2546 | 100 | 30 | 270 (RBSTART=0) | 2197.5 | 5 | 3.5 | >ACLR2 |
| 41 | n1 | 2506 | 20 | 15 | 100 (RBSTART=0) | 2167.5 | 5 | 9.1 | >ACLR2 |
| 41 | n3 | 2506 | 20 | 15 | 100 (RBSTART=0) | 1877.5 | 5 | 0.6 | >ACLR2 |
| 41 | n77 | 2680 | 20 | 15 | 100 (RBSTART=0) | 3305 | 10 | 8.3 | >ACLR2 |
| 41 | n78 | 2680 | 20 | 15 | 100 (RBSTART=0) | 3305 | 10 | 8.3 | >ACLR2 |
| n46 | 48 | 5190 | 80 | 30 | 216 (RBSTART=0) | 3697.5 | 5 | 13.3 | >ACLR2 |
| 48 | n46 | 3690 | 20 | 15 | 100 (RBSTART=0) | 5160 | 20 | 7 | >ACLR2 |
| n50 | 3 | 1487 | 60 | 30 | 162 (RBSTART=0) | 1807.5 | 5 | 2.5 | >ACLR2 |
| n71 | 12 | 688 | 20 | 15 | 20 (RBSTART=86) | 731.5 | 5 | 8.2 | >ACLR2 |
| 71 | n12 | 688 | 20 | 15 | 20 (RBSTART=80) | 731.5 | 5 | 8.2 | ACLR2 |
| n77 | 71 | 3350 | 100 | 30 | 270 (RBSTART=0) | 2687.5 | 5 | 4.5 | >ACLR2 |
| n77 | 411 | 3350 | 100 | 30 | 270 (RBSTART=0) | 2687.5 | 5 | 4.5 | >ACLR2 |
| n78 | 71 | 3350 | 100 | 30 | 270 (RBSTART=0) | 2687.5 | 5 | 4.5 | >ACLR2 |
| n78 | 38 | 3350 | 100 | 30 | 270 (RBSTART=0) | 2617.5 | 5 | 3.3 | >ACLR2 |
| n78 | 401 | 3350 | 100 | 30 | 270 (RBSTART=0) | 2397.5 | 5 | 4.5 | >ACLR2 |
| n78 | 411 | 3350 | 100 | 30 | 270 (RBSTART=0) | 2687.5 | 5 | 4.5 | >ACLR2 |
| n78 | 46 | 3750 | 100 | 30 | 270 (RBSTART=3) | 5160 | 20 | 13.5 | >ACLR2 |
| n79 | 426 | 4550 | 100 | 30 | 270 (RBSTART=0) | 3597.5 | 5 | 2.6 | >ACLR2 |
| n84 | 3 | 1945 | 50 | 15 | 128 (RBSTART=0) | 1877.5 | 5 | 19.7 | ACLR1 |
| NOTE 1: Applicable only when harmonic mixing MSD for this combination is not applied.  NOTE 2: The B41 requirements are modified by -0.5dB when carrier frequency of the assigned E-UTRA channel bandwidth is within 2515 – 2690 MHz.  NOTE 3: Void.  NOTE 4: The NR DL band should be configured using the lowest SCS that is compatible with the specified DL CBW.  NOTE 5: Void.  NOTE 6: The requirements only apply for UEs supporting inter-band DC\_42\_n79 ENDC with simultaneous Rx/Tx capability. Simultaneous Rx/Tx capability does not apply for UEs supporting band 42 with a n77 implementation only. These restrictions are applicable to related higher order configurations.  NOTE 7: The MSD exceptions are applicable to the case that interference of UL band 3rd order IMD product falls into the affected DL channels. | | | | | | | | | |

## **<<End of Change>>**