**3GPP TSG-RAN WG4 Meeting #** **104-e R4-2214594**

**Electronic Meeting, Aug. 15 – Aug. 26, 2022**

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| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
|  |
|  | **38.133** | **CR** | **2476** | **rev** | **1** | **Current version:** | **17.6.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  | CR on unified TCI in R17 feMIMO |
|  |  |
| ***Source to WG:*** | vivo |
| ***Source to TSG:*** | R4 |
|  |  |
| ***Work item code:*** | NR\_feMIMO-Core |  | ***Date:*** | 2022-08-26 |
|  |  |  |  |  |
| ***Category:*** | **F** |  | ***Release:*** | Rel-17 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:**Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
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| ***Reason for change:*** | 1. For unified TCI, the TCI state assumption during DCI BWP switching is not clarified yet.2. Some square brackets for unified TCI should be removed. |
|  |  |
| ***Summary of change:*** | 1. Clarify the TCI state assumption during DCI BWP switching2. The square bracket for unified TCI are removed. |
|  |  |
| ***Consequences if not approved:*** | 1. For unified TCI, the TCI state assumption during DCI BWP switching is not clear.2. The square bracket are kept. |
|  |  |
| ***Clauses affected:*** | 8.6.2, 8.6.2A.1, 8.6.2B.1, 8.6.2B.2, 8.15.3, 8.15.4, 8.16.3, 8.16.4 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS38.533 |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** | R4-2212665 |

<Start of Change #1>

### 8.6.2 DCI and timer based BWP switch delay on a single CC

The requirements in this clause only apply to the case that the BWP switch is performed on a single CC with more than one BWP configurations configured.

For DCI-based BWP switch, after the UE receives BWP switching request at DL slot n on a serving cell, UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after a time duration of TBWPswitchDelay + Y which starts from the beginning of DL slot n. Where,

- Y=0, if the serving cell where UE receives DCI for BWP switch request is same as the serving cell on which BWP switch occurs.

- Y equals to the length of 1 slot, if the serving cell where UE receives DCI for BWP switch is different from the serving cell on which BWP switch occurs for any involved serving cell. In this scenario, TBWPswitchDelay + Y shall follow the smaller SCS of scheduling cell, scheduled cells before and scheduled cells after active BWP change.If both scheduling cell and scheduled cell are in FR2-2, Y shall follow the SCS of 120 KHz.

The UE is not required to transmit UL signals or receive DL signals until the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n except DCI triggering BWP switch on the cell where DCI-based BWP switch occurs. The UE is not required to follow the requirements defined in this clause when performing a DCI-based BWP switch between the BWPs in disjoint channel bandwidths or in partially overlapping channel bandwidths.

For timer-based BWP switch, the UE shall start BWP switch at DL slot n, where slot n is the first slot of a DL subframe (FR1) or DL half-subframe (FR2) immediately after a BWP-inactivity timer *bwp-InactivityTimer* [2] expires on a serving cell, and the UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after a time duration of TBWPswitchDelay which starts from the beginning of DL slot n.

The UE is not required to transmit UL signals or receive DL signals during time duration TBWPswitchDelay after *bwp-InactivityTimer* [2] expires on the cell where timer-based BWP switch occurs.

Depending on UE capability *bwp-SwitchingDelay* [2], UE shall finish BWP switch within the time duration TBWPswitchDelay defined in Table 8.6.2-1.

Table 8.6.2-1: BWP switch delay

|  |  |  |
| --- | --- | --- |
|  | NR Slot length  | BWP switch delay TBWPswitchDelay (slots) |
|  | (ms) | Type 1Note 1 | Type 2Note 1 |
| 0 | 1 | 1 | 3 |
| 1 | 0.5 | 2 | 5 |
| 2 | 0.25 | 3 | 9 |
| 3 | 0.125 | 6 | 18 |
| 5 | 0.03125 | 20 | 65 |
| 6 | 0.015625 | 39 | 129 |
| Note 1: Depends on UE capability.Note 2: If the BWP switch involves changing of SCS, the BWP switch delay is determined by the smaller SCS between the SCS before BWP switch and the SCS after BWP switch. |

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

Provided the UE does not have the required activated TCI-state(s) information to receive PDCCH/ PDSCH and to transmit PUSCH/PUCCH/SRS in the new BWP, the UE shall use old TCI-state(s) before the BWP switch until a new MAC CE updating the required activated TCI-state(s) information is received after the BWP switch.

If the BWP switch is triggered within or outside DRX active time, and one of the two BWPs in a BWP switching is a dormant BWP [TS 38.321, 7], UE shall be able to complete active BWP switching within the time duration of

- TdormantBWPswitchDelay =TBWPswitchDelay+ X, provided that the dormancy indication is received in any of the first 3 OFDM symbols of a slot in the serving cell where DCI for dormancy indication is receiveds, or

- TdormantBWPswitchDelay =TBWPswitchDelay + X + Z, provided that the dormancy indication is received after the first 3 OFDM symbols of a slot in the serving cell where DCI for dormancy indication is received, where

- TBWPswitchDelay is defined in Table 8.6.2-1 corresponding to the smallest value among the SCS of the serving cell where UE receives dormancy indication and the SCSs of the dormant BWP and the active BWP immediately before or after switching the BWP of the serving cell where BWP switching occurs;

- X equals to the length of 1 slot corresponding to the smallest value among the SCS of the serving cell where UE receives dormancy indication and the SCSs of the dormant BWP and the active BWP immediately before or after switching the BWP of the serving cell where BWP switching occurs. If both scheduling cell and scheduled cell are in FR2-2, X shall follow the SCS of 120 KHz.

- Z equals to the length of 1 slot corresponding to the SCS of the serving cell where UE receives dormancy indication.

For DCI-based BWP switch, if the new BWP is a dormant BWP, after the UE receives BWP switching request at DL slot n on a serving cell, UE shall be able to receive CSI-RS (for DL active BWP switch) on the new BWP on the serving cell on which BWP switch on the first DL slot occurs right after a time duration of TdormantBWPswitchDelay which starts from the beginning of DL slot n.

### 8.6.2A DCI based BWP switch delay on multiple CCs

The requirements in this clause only apply to the case when the same type of BWP switch (DCI based BWP switch) is performed on multiple CCs simultaneously or over partially overlapping time period.

#### 8.6.2A.1 Simultaneous DCI based BWP switch delay on multiple CCs

The delay requirements for simultaneous DCI based BWP switch on multiple CCs in this clause apply only if the timing difference among the first symbol of slot carrying DCI for all CCs is received within the MRTD for inter-band CA as defined in clause 7.6.4.

For DCI-based BWP switch on multiple CCs, after the UE receives BWP switching request, UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWPs on the serving cells on which BWP switch on the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelay which starts from the beginning of DL slot n, where slot n is slot which UE receives the earliest BWP switching request among CCs on which UE is performing simultaneous DCI-based BWP switching.

The UE is not required to transmit UL signals or receive DL signals until the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelay which starts from the beginning of DL slot n except DCI triggering BWP switch on the cell where DCI-based BWP switch occurs. The UE is not required to follow the requirements defined in this clause when performing a DCI-based BWP switch between the BWPs in disjoint channel bandwidths or in partially overlapping channel bandwidths on any serving cell.

UE shall finish BWP switch within the time duration TMultipleBWPswitchDelay + Y, which is defined as:

 TMultipleBWPswitchDelay = TBWPswitchDelay + D\*(N-1)

Where:

- TBWPswitchDelay is the BWP switching delay on single CC defined in Table 8.6.2-1 depending on UE capability *bwp-SwitchingDelay* [2]. TBWPswitchDelay shall be based on the smallest SCS among SCS of all involved CCs before and after BWP switch. If the BWP switch on multiple CCs results in the change of the SCS on any CC among involved CCs, TBWPswitchDelay should be based on the smallest SCS among all SCS values of all involved CCs.

- D is the incremental delay for each additional CC involved in simultaneous BWP switch and depends on UE capability *bwp-SwitchingMultiCCs-r16* [TS 38.306, 14] for switching between non-dormant BWPs, and *bwp-SwitchingMultiDormancyCCs-r16* for switching between non-dormant and dormant BWPs.

- For UE which is capable of per-FR gap, and no BWP switch involves SCS change, N is the number of CCs in same FR; For UE which is not capable of per-FR gap, or the BWP switches on any CC involves SCS changing, N is the number of CCs undergoing simultaneous BWP switch.

* Y=0, ­if the serving cell where UE receives DCI for BWP switch is same as the serving cell on which BWP switch occurs for each involved serving cell.

Y equals to the length of one slot at smaller SCS of scheduling cell, scheduled cells before and scheduled cells after active BWP change,

- ­if the serving cell where UE receives DCI for BWP switch is different from the serving cell on which BWP switch occurs for any involved serving cell. If both scheduling cell and scheduled cell are in FR2-2, Y shall follow the SCS of 120 KHz. .

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

Provided the UE does not have the required activated TCI-state(s) information to receive PDCCH/ PDSCH and to transmit PUSCH/PUCCH/SRS in the new BWP, the UE shall use old TCI-state(s) before the BWP switch until a new MAC CE updating the required activated TCI-state(s) information is received after the BWP switch.

If the BWP switch is triggered on multiple CCs simultaneously within or outside DRX active time, and one of the two BWPs on each CC in a BWP switching is a dormant BWP [TS 38.321, 7], UE shall be able to complete active BWP switching within the time duration of

- TDormantMultipleBWPswitchDelay = TMultipleBWPswitchDelay+X, provided that the dormancy indication is received in any of the first 3 OFDM symbols of a slot in the serving cell where DCI for dormancy indication is received, or

- TDormantMultipleBWPswitchDelay = TMultipleBWPswitchDelay +X+Z, provided that the dormancy indication is received after the first 3 OFDM symbols of a slot in the serving cell where DCI for dormancy indication is received, where

- TMultipleBWPswitchDelay is defined above corresponding to the smallest value among the SCS of the serving cell where UE receives dormancy indication and the SCSs of the dormant BWP and the active BWP immediately before or after switching the BWP of the serving cell where BWP switching occurs;

- X equals to the length of 1 slot corresponding to the smallest value among the SCS of the serving cell where UE receives dormancy indication and the SCSs of the dormant BWP and the active BWP immediately before or after switching the BWP of the serving cell where BWP switching occurs. If both scheduling cell and scheduled cell are in FR2-2, X shall follow the SCS of 120 KHz.

- Z equals to the length of 1 slot corresponding to the SCS of the serving cell where DCI for dormancy indication is received.

The number of CCs, N, on which the UE can simultaneously switch BWPs while still meeting the requirements, if any, related to allocations on downlink, uplink, or transmission of HARQ-ACK, depends on the UE reported capabilities related to BWP switching, the network configuration and the BWP switch method.

<Unchanged part omitted>

### 8.6.2B Timer based BWP switch delay on multiple CCs

The requirements in this clause only apply to the case when the same type of BWP switch (timer based BWP switch) is performed on multiple CCs simultaneously or over partially overlapping time period.

#### 8.6.2B.1 Simultaneous timer based BWP switch delay on multiple CCs

The delay requirements for simultaneous timer based BWP switch on multiple CCs in this clause apply only if the timing difference among the beginning of the slot where timer based BWP switching starts for all CCs is within the MRTD for inter-band CA as defined in clause 7.6.4.

For timer-based BWP switch on multiple CCs, UE shall start BWP switch at DL slot n, where slot n is the first slot of a DL subframe (in FR1) or DL half-subframe ((in FR2) immediately after the earliest BWP-inactivity timer *bwp-InactivityTimer* [2] expiration occurs on multiple serving cells, and the UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWPs on the serving cells on which BWP switch on the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelay which starts from the beginning of DL slot n, where TMultipleBWPswitchDelay is defined in 8.6.2A.1.

The UE is not required to transmit UL signals or receive DL signals during time duration TMultipleBWPswitchDelay after *bwp-InactivityTimer* [2] expires on the cell where timer-based BWP switch occurs.

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

Provided the UE does not have the required activated TCI-state(s) information to receive PDCCH/ PDSCH and to transmit PUSCH/PUCCH/SRS in the new BWP, the UE shall use old TCI-state(s) before the BWP switch until a new MAC CE updating the required activated TCI-state(s) information is received after the BWP switch.

#### 8.6.2B.2 Non-simultaneous timer based BWP switch delay on multiple CCs

In non-simultaneous case, the timer-based BWP switch on multiple CCs is triggered over partially overlapping time period.

The delay requirements for non-simultaneous timer based BWP switch on multiple CCs in this clause apply if the timing difference among the beginning of the slot where timer based BWP switching starts for all CCs is exceeds the MRTD for inter-band CA as defined in clause 7.6.4, and the BWP switch does not involve SCS change. The UE performs the non-simultaneous timer-based BWP switch on the CCs sequentially.

For non-simultaneous timer-based BWP switch, the UE shall be able to receive PDSCH (for DL active BWP switch) or transmit PUSCH (for UL active BWP switch) on the new BWP on the serving cell on which BWP switch on the first DL or UL slot occurs right after a time duration of TMultipleBWPswitchDelayTotal which starts from the beginning of DL slot n, where slot n is the first slot of a DL subframe (in FR1) or DL half-subframe (in FR2) immediately after the earliest BWP-inactivity timer *bwp-InactivityTimer* [2] expires.

 TMultipleBWPswitchDelayTotal = TDelay + TMultipleBWPswitchDelay

Where:

TDelay is the time required to complete the ongoing timer-based BWP switching on other CCs.

TMultipleBWPswitchDelay is the timer-based BWP switch delay on current single CC defined in clause 8.6.2 or simultaneously triggered on multiple CCs defined in clause 8.6.2B.1.

The UE is not required to transmit UL signals or receive DL signals during time duration TMultipleBWPswitchDelayTotal after *bwp-InactivityTimer* [2] expires on the cell where timer-based BWP switch occurs.

Provided the UE does not have the required TCI-state information to receive PDCCH and PDSCH in the new BWP, the UE shall use old TCI-states before the BWP switch until a new MAC CE updating the required TCI-state information for PDCCH and PDSCH is received after the BWP switch.

If UE has the information on the required TCI-state information to receive PDCCH and PDSCH in the new BWP,

- UE shall be able to receive PDCCH and PDSCH with old TCI-states before the delay as specified in Clause 8.10 in the new BWP.

- UE shall be able to receive PDCCH and PDSCH with new TCI-states after the delay as specified in Clause 8.10 in the new BWP.

Provided the UE does not have the required activated TCI-state(s) information to receive PDCCH/ PDSCH and to transmit PUSCH/PUCCH/SRS in the new BWP, the UE shall use old TCI-state(s) before the BWP switch until a new MAC CE updating the required activated TCI-state(s) information is received after the BWP switch.

<End of Change #1>

<Start of Change #2>

8.15.3 MAC-CE based downlink TCI state switch delay

The requirements in this clause shall apply for DL TCI state switch using separate DL TCI state or joint TCI state of unified TCI state switch framework.

In case that source RS in DL TCI state or joint TCI state is associated with a PCI different from that of the serving cell, the requirements in this clause shall apply if the cell with different PCI satisfies the known cell condition defined in 8.15.1. If the known cell condition is not met, longer delay may be expected.

[In case of joint TCI state switch, UE is not expected to receive on DL before UE completes the DL and UL TCI state switch.]

If the target TCI state is known, upon receiving PDSCH carrying MAC-CE activation command in slot n, UE shall be able to receive UE-dedicated PDCCH/PDSCH with target TCI state of the serving cell on which TCI state switch occurs at the first slot that is after slot n+ THARQ + $3N\_{slot}^{subframe,µ}$+ TOk\*(Tfirst-SSB + TSSB-proc) / *NR slot length*. The UE shall be able to receive UE-dedicated PDCCH/PDSCH with the old TCI state until slot n+ THARQ + $3N\_{slot}^{subframe,µ}$ .Where THARQ is the timing between DL data transmission and acknowledgement as specified in TS 38.213 [3];

- Tfirst-SSB is time to first SSB transmission after MAC CE command is decoded by the UE; The SSB shall be the QCL-TypeA or QCL-TypeC to target TCI state

- TSSB-proc = 2 ms;

- TOk = 1 if target TCI state is not in the active TCI state list for PDSCH/PDCCH, 0 otherwise.

If the target TCI state is unknown, upon receiving PDSCH carrying MAC-CE activation command in slot n, UE shall be able to receive UE-dedicated PDCCH/PDSCH with target TCI state of the serving cell on which TCI state switch occurs at the first slot that is after slot n+ THARQ +$3N\_{slot}^{subframe,µ}$ + TL1-RSRP +TOuk\*(Tfirst-SSB+ TSSB-proc) / *NR slot length*. The UE shall be able to receive UE-dedicated PDCCH/PDSCH with the old TCI state until slot n+ THARQ + $3N\_{slot}^{subframe,µ}$ .

Where

- T L1-RSRP = 0 in FR1 or when the TCI state switching not involving QCL-TypeD in FR2. Otherwise,

- T L1-RSRP is the time for Rx beam refinement in FR2, defined as

- TL1-RSPR\_Measurement\_Period\_SSB for SSB as specified in clause 9.5.4.1,

- with the assumption of M=1

- with TReport = 0

- TL1-RSRP\_Measurement\_Period\_CSI-RS for CSI-RS as specified in clause 9.5.4.2

- CSI-RS based L1-RSRP measurement only apply for TCI state switch when source RS is associated with serving cell

- configured with higher layer parameter *repetition* set to ON

- with the assumption of M=1 for periodic CSI-RS

- for aperiodic CSI-RS if number of resources in resource set at least equal to *MaxNumberRxBeam*

- with TReport = 0

- TOuk = 1 for CSI-RS based L1-RSRP measurement, and 0 for SSB based L1-RSRP measurement when TCI state switching involves QCL-TypeD

- TOuk = 1 when TCI state switching involves other QCL types only

- Tfirst-SSB is time to first SSB transmission after L1-RSRP measurement when TCI state switching involves QCL-TypeD;

- Tfirst-SSB is time to first SSB transmission after MAC CE command is decoded by the UE for other QCL types;

- The SSB shall be the QCL-TypeA or QCL-TypeC to target TCI state

### 8.15.4 DCI based downlink TCI state switch delay

When a UE is configured with the higher layer parameter with *DLorJointTCIState* or *UL-TCIState,* activated with TCI states for downlink transmission by MAC CE indication of more than one codepoints, and receives DCI format 1\_1/1\_2 with or without DL assignment providing indicated TCI-State or TCI state pair in the active TCI list for a CC or all CCs with a common indicated TCI-State in the same CC list configured by *simultaneousU-TCI-UpdateList1, simultaneousU-TCI-UpdateList2, simultaneousU-TCI-UpdateList3, simultaneousU-TCI-UpdateList4*., the UE transmits a PUCCH with HARQ-ACK information corresponding to the DCI carrying the TCI-State indication.

If the target TCI state is known, the downlink TCI switching to the indicated DL TCI state or joint TCI state in the DCI format shall be completed starting from the first slot that is at least *BeamAppTime-r17* symbols after the last symbol of the PUCCH carrying HARQ-ACK in response to the DCI triggering TCI state activation. The first slot and the *BeamAppTime-r17* symbols are both determined on the carrier with the smallest SCS among the carrier(s) applying the beam indication. The value of *BeamAppTime-r17* is defined in TS 38.331 [2]. The known condition for TCI state defined in clause 8.15.2 is applied.

<End of Change #2>

<Start of Change #3>

8.16.3 MAC-CE based uplink TCI state switch delay

The requirements in this clause shall apply for UL TCI state switch using separate UL TCI state or joint TCI state of unified TCI state switch framework.

In case that source RS in UL TCI state or joint TCI state is associated with a PCI different from that of the serving cell, the requirements in this clause shall apply if the cell with different PCI satisfies the known cell condition defined in 8.16.1. If the known cell condition is not met, longer delay may be expected.

In case of joint TCI state switch, UE is not expected to transmit on UL before UE completes the DL and UL TCI state switch.

For separate UL TCI state switch or joint TCI state switch for PUCCH or PUSCH, or semi-persistent/aperiodic/periodic SRS, when *beamCorrespondenceWithoutUL-BeamSweeping* is set to 1, upon receiving PDSCH carrying MAC-CE activation command in slot n on serving cell,

* + - If target TCI state is known,
* The UE shall be able to transmit uplink signal with the target TCI state in the slot n+THARQ + 3ms + NM*\** (Tfirst\_target-PL-RS + 4\*Ttarget\_PL-RS + 2ms).
	+ - If target TCI state is unknown,
* The UE shall be able to transmit uplink signal with the target TCI state in the slot n+THARQ + 3ms *+* TL1-RSRP+ Tfirst\_target-PL-RS + 4\*Ttarget\_PL-RS + 2ms.

Where,

- THARQ is the timing between DL data transmission and acknowledgement as specified in TS 38.213 [3].

- NM = 1, if the target PL-RS is not maintained by the UE, 0 otherwise.

- Tfirst\_target-PL-RS is time to first pathloss RS transmission after L1-RSRP measurement when target TCI state is unknown.

- Tfirst\_target-PL-RS is time to first pathloss RS transmission after MAC CE command is decoded by the UE for known TCI State.

- Ttarget\_PL-RS is the periodicity of the target pathloss reference signal which would be SSB or NZP CSI-RS when PL-RS is associated with serving cell

- Ttarget\_PL-RS is the periodicity of the target pathloss reference signal which would be SSB when PL-RS is associated with PCI different from serving cell

- T L1-RSRP is the time for Rx beam refinement in FR2, defined as

- TL1-RSPR\_Measurement\_Period\_SSB for SSB as specified in clause 9.5.4.1,

- with the assumption of M=1

- with TReport = 0

- TL1-RSRP\_Measurement\_Period\_CSI-RS for CSI-RS as specified in clause 9.5.4.2

- CSI-RS based L1-RSRP measurement only apply for TCI state switch when source RS is associated with serving cell

- configured with higher layer parameter *repetition* set to ON

- with the assumption of M=1 for periodic CSI-RS

- for aperiodic CSI-RS if number of resources in resource set at least equal to *MaxNumberRxBeam*

- with TReport = 0

Editor note: when PL-RS is SSB in FR2, the delay requirement is FFS.

### 8.16.4 DCI based uplink TCI state switch delay

When a UE is configured with the higher layer parameter with *DLorJointTCIState* or *UL-TCIState,* activated with TCI states for uplink transmission by MAC CE indication of more than one codepoints, and receives DCI format 1\_1/1\_2 with or without DL assignment providing indicated TCI-State or TCI state pair in the active TCI list for a CC or all CCs with a common indicated TCI-State in the same CC list configured by *simultaneousU-TCI-UpdateList1, simultaneousU-TCI-UpdateList2, simultaneousU-TCI-UpdateList3, simultaneousU-TCI-UpdateList4*, the UE transmits a PUCCH with HARQ-ACK information corresponding to the DCI carrying the TCI-State indication.

The requirements in this clause are applicable only if the DCI format indicating UL TCI state or joint TCI state switch is received by UE when

- target TCI state is known, and

- target TCI state is in active TCI state list, and

- PL-RS is maintained for UL or Joint TCI state switch

If the target TCI state is known, the uplink TCI switching to the indicated UL TCI state or joint TCI state in the DCI format shall be completed starting from the first slot that is at least *BeamAppTime-r17* symbols after the last symbol of the PUCCH carrying HARQ-ACK in response to the DCI triggering TCI state activation. The first slot and the *BeamAppTime-\_r17* symbols are both determined on the carrier with the smallest SCS among the carrier(s) applying the beam indication. The value of *BeamAppTime\_r17* is defined in TS 38.331 [2]. The known condition for TCI state defined in clause 8.16.2 is applied.

If a PL-RS is associated with or included in UL TCI state or joint TCI state, the UL TCI switching and PL-RS switching shall be completed at the same time.

<End of Change #3>