**3GPP TSG- Meeting #**

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| *CR-Form-v12.2* |
| **CHANGE REQUEST** |
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|  |  | **CR** | 2423 | **rev** |  | **Current version:** |  |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network |  | Core Network |  |

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|  |
| ***Title:***  | CR on RRM core requirements for measurement procedure requirements for HST FR2 |
|  |  |
| ***Source to WG:*** | CATT |
| ***Source to TSG:*** |  |
|  |  |
| ***Work item code:*** |  |  | ***Date:*** |  |
|  |  |  |  |  |
| ***Category:*** | F |  | ***Release:*** |  |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19)* |
|  |  |
| ***Reason for change:*** | According the WF R4-2206848, no enhanced requiremen should be applied to other than PC6 UEs even when HST FR2 flags are configured.  |
|  |  |
| ***Summary of change:*** | 1. For Tq reuirements, make clear that if other PC class when highSpeedMeasFlagFR2-r17 is configured, the legacy K=2.5 is also applied.
2. For 9.2.5.1 and 9.2.5.2, for power class 6 UE with *highSpeedMeasFlagFR2-r17* configured when SMTC<40ms, use new table, in other cases, use legacy table.In legacy table, there is no definition for Mpss/sss\_sync\_w/o\_gaps andMmeas\_period\_w/o\_gaps. Add the definition for power class 6, otherwise, the requirements are not clear.
3. For 9.2.6.2 and 9.2.6.3, apply the similar change in 9.2.5.1 and 9.2.5.2
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|  |  |
| ***Consequences if not approved:*** | The requirements for HT FR2 is not correct for PC6 and other power class UEs.  |
|  |  |
| ***Clauses affected:*** | 7.1.2.1 9.2.5.1 9.2.5.2 9.2.6.2 9.2.6.3 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **X** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** | **X** |  |  Test specifications | TS38.533  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

========================= First Change Request ===========================

#### 7.1.2.1 Gradual timing adjustment

Requirements in this section shall apply regardless of whether the reference cell is on a carrier frequency subject to CCA or not.

When the transmission timing error between the UE and the reference timing exceeds ±Te then the UE is required to adjust its timing to within ±Te. The reference timing shall be  before the downlink timing of the reference cell. All adjustments made to the UE uplink timing shall follow these rules:

1) The maximum amount of the magnitude of the timing change in one adjustment shall be Tq.

2) The minimum aggregate adjustment rate shall be Tp per second.

3) The maximum aggregate adjustment rate shall be Tq per 200 ms for SCS of UL signals smaller or equal to 120 kHz and 100 ms for SCS of upling signals larger or equal to 480 kHz.

 where the maximum autonomous time adjustment step Tq and the aggregate adjustment rate Tp are specified in Table 7.1.2.1-1.

Table 7.1.2.1-1: Tq Maximum Autonomous Time Adjustment Step and Tp Minimum Aggregate Adjustment rate

|  |  |  |  |
| --- | --- | --- | --- |
| Frequency Range | SCS of uplink signals (kHz) | Tq | Tp  |
| 1 | 15 | 5.5\*64\*Tc | 5.5\*64\*Tc |
|  | 30 | 5.5\*64\*Tc | 5.5\*64\*Tc |
|  | 60 | 5.5\*64\*Tc | 5.5\*64\*Tc |
| 2-1 | 60 | K\*64\*Tc | 2.5\*64\*Tc |
|  | 120 | K\*64\*Tc | 2.5\*64\*Tc |
| 2-2 | 120 | 2.5\*64\*Tc | 2.5\*64\*Tc |
|  | 480 | [0.8]\*64\*Tc | [0.8]\*64\*Tc |
|  | 960 | [0.8]\*64\*Tc | [0.8]\*64\*Tc |
| NOTE 1: Tc is the basic timing unit defined in TS 38.211 [6]NOTE 2: When *highSpeedMeasFlagFR2-r17* is configured for UE supporting power class 6, K = 4.5; otherwise, K = 2.5. |

========================= Second Change Request ===========================

#### 9.2.5.1 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_without\_index if the UE is not indicated to report SSB based RRM measurement result with the associated SSB index(*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE is indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2.

Tidentify\_intra\_without\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra) ms

Tidentify\_intra\_with\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra) ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.5.1-1, 9.2.5.1-2, 9.2.5.1-4 (deactivated SCell) or 9.2.5.1-5 (deactivated SCell) or 9.2.5.1-9 (deactivated SCell) or 9.2.5.1-11 or 9.2.5.1-12 (deactivated PSCell) or 9.2.5.1-13 (deactivated PSCell).

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TPSS/SSS\_sync\_intra is given in Table 9.2.5.1-11; otherwise, TPSS/SSS\_sync\_intra is given in Table 9.2.5.1-2.

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.5.1-3, 9.2.5.1-6 (deactivated SCell), 9.2.5.1-10(deactivated SCell) or 9.2.5.1-14 (deactivated PSCell).

 T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.5.2-1, table 9.2.5.2-2 table 9.2.5.2-3 (deactivated SCell), 9.2.5.2-4(deactivated SCell), 9.2.5.2-5 or 9.2.5.2-6(deactivated SCell), 9.2.5.2-8(deactivated PSCell) or 9.2.5.2-9(deactivated PSCell).

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TSSB\_measurement\_period\_intra is given in Table 9.2.5.2-7; otherwise, T SSB\_measurement\_period\_intra is given in Table 9.2.5.2-2.

 CSSFintra: it is a carrier specific scaling factor and is determined

 according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside measurement gaps, i.e. when intra-frequency SMTC is fully non overlapping or partially overlapping with measurement gaps or NCSG, or according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps, i.e. when intra-frequency SMTC is fully overlapping with measurement gaps, or according to CSSFwithin\_ncsg,i in clause 9.1.5.3 for measurement conducted within NCSG, i.e. when intra-frequency SMTC is fully overlapping with NCSG.

 For a UE that supports Pre-MG, an SMTC occasion is only considered to be overlapped by Pre-MG if the Pre-MG is activated.

 if the high layer in TS 38.331 [2] signalling of *smtc2* is configured, the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc2*; Otherwise the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc1*.

 Mpss/sss\_sync\_w/o\_gaps : For a UE supporting FR2 power class 1 or 5, Mpss/sss\_sync\_w/o\_gaps =40. For a UE supporting power class 2, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2 power class 3, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2 power class 4, Mpss/sss\_sync\_w/o\_gaps =24, For a UE supporting FR2 power class 6, Mpss/sss\_sync\_w/o\_gaps =24

 Mmeas\_period\_w/o\_gaps : For a UE supporting power class 1 or 5, Mmeas\_period\_w/o\_gaps =40. For a UE supporting FR2 power class 2, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 3, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 4, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 6, Mmeas\_period\_w/o\_gaps =24.

When UE supports *concurrentMeasGap-r17* and is configured with concurrent measurement gaps,

 Kp is the scaling factor for an SSB frequency layer to be measured without measurement gaps. Kp = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

- For a window W of duration max(SMTC period, MGRP\_max), where MGRP max is the maximum MGRP across all configured per-UE measurement gap and/or per-FR measurement gap within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions within the window, including those overlapped with measurement gap occasions within the window, and

- Navailable is the number of SMTC occasions that are not overlapped with any non-dropped MG occasion within the window W, after accounting for measurement gap collisions by applying the measurement gap collision rule in section 9.1.2B.3.

 Kp = 1 when Navailable = 0.

- Otherwise, when UE is not configured with or UE does not support concurrent measurement gaps:

 When intra-frequency SMTC is fully non overlapping with measurement gaps or intra-frequency SMTC is fully overlapping with MGs, Kp=1

 When intra-frequency SMTC is partially overlapping with measurement gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP. When intra-frequency SMTC is partially overlapping with the ML of NCSG, Kp = 1/(1- (SMTC period /VIRP)), where SMTC period < VIRP. For calculation of Kp, if the high layer signalling (TS 38.331 [2]) of *smtc2* is configured, for cells indicated in the *pci-List* parameter in *smtc2*, the SMTC periodicity corresponds to the value of higher layer parameter *smtc2*; for the other cells, the SMTC periodicity corresponds to the value of higher layer parameter *smtc1.*

 If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index

 For FR2,

 Klayer1\_measurement=1,

- if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap are not fully overlapped by intra-frequency SMTC occasions, or

- if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap and fully-overlapped by intra-frequency SMTC occasions are not overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols, given that *SSB-ToMeasure* and *SS-RSSI-Measurement* are configured, where SSB symbols are indicated by the union set of SSB-ToMeasure from all the configured measurement objects on the same serving carrier which can be merged.and RSSI symbols are indicated by *SS-RSSI-Measurement*;

 Klayer1\_measurement=1.5, otherwise.

 If the above-mentioned reference signal configured for L1-RSRP measurement is aperiodic CSI-RS resource, longer cell identification delay would be expected.

 If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5 and Table 9.2.5.1-6 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.5.1-1, Table 9.2.5.1-2, Table 9.2.5.1-3, Table 9.2.5.1-4, Table 9.2.5.1-5, Table 9.2.5.1-6, Table 9.2.5.1-12, Table 9.2.5.1-13 and Table 9.2.5.1-14 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.5.1-1: Time period for PSS/SSS detection, (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max( 600ms, ceil( 5 x Kp) x SMTC period )Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max( 600ms, ceil(M2 Note 2x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(5 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1.NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. |

Table 9.2.5.1-2: Time period for PSS/SSS detection, (Frequency range FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.1-3: Time period for time index detection (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil( 3 x Kp )x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil (M2 Note 2 x 3 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(3 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-4: Time period for PSS/SSS detection, deactivated SCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.1-5: Time period for PSS/SSS detection, deactivated SCell (FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.1-6: Time period for time index detection, deactivated SCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.1-7: Void

Table 9.2.5.1-8: Void

Table 9.2.5.1-9: Time period for PSS/SSS detection, deactivated SCell (FR1), when *highSpeedMeasCA-Scell-r17* is configured

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms |  Ceil(5 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-10: Time period for time index detection, deactivated SCell (FR1)，when *highSpeedMeasCA-Scell-r17* is configured

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms |  Ceil(3 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp)x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

Table 9.2.5.1-11: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (Frequency range FR2) when SMTC period <= 40ms

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(1.5x Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle) x CSSFintra |
| DRX cycle>320ms | ceil(Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

Table 9.2.5.1-12: Time period for PSS/SSS detection, deactivated PSCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(5 x Kp) x [measCyclePSCell] x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max([measCyclePSCell], 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max([measCyclePSCell], DRX cycle) x CSSFintra |

Table 9.2.5.1-13: Time period for PSS/SSS detection, deactivated PSCell (FR2)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x [measCyclePSCell] x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max([measCyclePSCell], 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max([measCyclePSCell], DRX cycle) x CSSFintra |

Table 9.2.5.1-14: Time period for time index detection, deactivated PSCell (FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | Ceil(3 x Kp) x [measCyclePSCell] x CSSFintra |
| DRX cycle≤ 320ms | Ceil(3 x Kp) x max([measCyclePSCell], 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(3 x Kp) x max([measCyclePSCell], DRX cycle) x CSSFintra |

#### 9.2.5.2 Measurement period

The measurement period for intra-frequency measurements without gaps is as shown in table 9.2.5.2-1, 9.2.5.2-2, 9.2.5.2-3 (deactivated SCell), 9.2.5.2-4 (deactivated SCell), 9.2.5.2-8 (deactivated SCG applicable for PSCell) or 9.2.5.2-9 (deactivated SCG applicable for PSCell). When *highSpeedMeasFlag-r16* is configured, T SSB\_measurement\_period\_intra is specified in Table 9.2.5.2-5.

If the higher layer signaling in TS38.331 [2] signalling of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for TSSB\_measurement\_period\_intra

For a UE that supports Pre-MG, an SMTC occasion is only considered to be overlapped by Pre-MG if the Pre-MG is activated.

If MCG DRX is in use, measurement period requirements for intra-frequency measurement in MCG specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3 and Table 9.2.5.2-4 shall depend on the MCG DRX cycle. If SCG DRX is in use, measurement period requirements for intra-frequency measurement in SCG specified in Table 9.2.5.2-1, Table 9.2.5.2-2, Table 9.2.5.2-3, Table 9.2.5.2-4, Table 9.2.5.2-8 and Table 9.2.5.2-9, shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

For FR2, a longer measurement period is allowed, if aperiodic CSI-RS resource is measured for L1-RSRP measurement on any FR2 serving frequency in the same band, and the CSI-RS resource is outside measurement gap and overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols. If *SSB-ToMeasure* or *SS-RSSI-Measurement* is configured, the SSB symbols are indicated by the union set of *SSB-ToMeasure* from all the configured measurement objects on the same band which can be merged and the RSSI symbols are indicated by *SS-RSSI-Measurement*.

Table 9.2.5.2-1: Measurement period for intra-frequency measurements without gaps (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | ceil( 5 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.2-2: Measurement period for intra-frequency measurements without gaps (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5x Mmeas\_period\_w/o\_gaps x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra  |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identified |

Table 9.2.5.2-3: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCel |

Table 9.2.5.2-4: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to deactivated SCG SCell. |

Table 9.2.5.2-5: T SSB\_measurement\_period\_intra When *highSpeedMeasFlag-r16* and/or highSpeedMeasCA-Scell-r17 is configured (Frequency range FR1

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX Note 2 | max(200ms, ceil( 5 x Kp) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 160ms | max(200ms, ceil(5 x M2 Note 2 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | ceil(4 x M2 Note 2 x Kp) x DRX cycle x CSSFintra |
| DRX cycle>320ms | ceil( Y Note 3 x Kp ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC period > 40 ms, otherwise M2=1NOTE 3: Y=3 when SMTC period <= 40ms, Y=5 when SMTC period > 40msNOTE 4: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 5: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to measurements of secondary component carrier with active SCell. |

Table 9.2.5.2-6: Measurement period for intra-frequency measurements without gaps (deactivated SCell) (FR1), when highSpeedMeasCA-Scell-r17 is configured

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | ceil( 5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 160ms | ceil(5 x Kp) x max(measCycleSCell, M2 Note 1 x DRX cycle) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | ceil(4 x Kp) x max(measCycleSCell, M2 Note 1 x DRX cycle) |
| DRX cycle>320ms | ceil( Y Note 2 x Kp ) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 2: Y=3 when SMTC <= 40ms, Y=5 when SMTC > 40ms |

Table 9.2.5.2-7: Measurement period for intra-frequency measurements without gaps when *highSpeedMeasFlagFR2-r17* is configured (FR2) when SMTC period <= 40ms

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(M1Note 2 x Kp x Klayer1\_measurement) x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80ms | max(400ms, ceil(M1Note 2 x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle)) x CSSFintra |
| 80ms< DRX cycle≤ 320ms | ceil(1.5x Mmeas\_period\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement) x max(SMTC period,DRX cycle) x CSSFintra  |
| DRX cycle>320ms | ceil(Mmeas\_period\_w/o\_gaps Note 3 xKp x Klayer1\_measurement ) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

Table 9.2.5.2-8 Measurement period for intra-frequency measurements without gaps (deactivated SCG applicable for PSCell) (FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(5 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(5 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(5 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.2-9: Measurement period for intra-frequency measurements without gaps (deactivated SCG applicable for PSCell) (FR2)**

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320ms | Ceil(Mmeas\_period\_w/o\_gaps x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

========================= Third Change Request ===========================

#### 9.2.6.1 Void

#### 9.2.6.2 Intra-frequency cell identification

When a measurement gap is provided or an activated Pre-MG is provided without any pre-MG status changed during the measurement period, the UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE has been indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2.

 Tidentify\_intra\_without\_index = TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra ms

 Tidentify\_intra\_with\_index = TPSS/SSS\_sync\_ntra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in table 9.2.6.2-1, 9.2.6.2-2 or 9.2.6.2-9.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TPSS/SSS\_sync\_intra is given in Table 9.2.6.2-9; otherwise, TPSS/SSS\_sync\_intra is given in Table 9.2.6.2-2. TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.6.2-3.

 T SSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.6.3-1 or 9.2.6.3-2.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC <= 40ms, TSSB\_measurement\_period\_intra is given in Table 9.2.6.3-4; otherwise, T SSB\_measurement\_period\_intra is given in Table 9.2.6.3-2.

 CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps.

 Kgap is the scaling factor for a SSB frequency layer to be measured within an associated measurement gap pattern. Kgap = 1 when the UE is not configured with concurrent measurement gaps or not supporting [concurrent measurement gaps]. Otherwise, Kgap = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

 For a window W of duration max(SMTC period, MGRP\_max), where MGRP max is the maximum MGRP across all configured per-UE measurement gap and per-FR measurement gap within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

-- Ntotal is the total number of SMTC occasions that are covered by instances of the associated measurement gap within the window W, including those overlapped with other measurement gap occasions within the window, and

 Navailable is the number of SMTC occasions that are covered by instances of the non-dropped associated measurement gap within the window W after accounting for measurement gap collisions by applying the measurement gap collision rule in section 9.1.2B.3.

 When concurrent measurement gaps are configured, requirements in this clause do not apply if Navailable =0.

 Mpss/sss\_sync\_with\_gaps : For a UE supporting FR2 power class 1 or 5, Mpss/sss\_sync with\_gaps=40. For a UE supporting FR2 power class 2, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2 power class 3, Mpss/sss\_sync with\_gaps =24. For a UE supporting power class 4, Mpss/sss\_sync with\_gaps =24. For a UE supporting power class 6, Mpss/sss\_sync with\_gaps =24

 Mmeas\_period\_ with\_gaps: For a UE supporting power class 1 or 5, Mmeas\_period\_ with\_gaps =40. For a UE supporting power class 2, Mmeas\_period\_ with\_gaps =24. For a UE supporting power class 3, Mmeas\_period\_ with\_gaps =24. For a UE supporting power class 4, Mmeas\_period with\_gaps =24. For a UE supporting power class 6, Mmeas\_period with\_gaps =24.

If the higher layer signaling in TS 38.331 [2] of *smtc2* is present and smtc1 is fully overlapping with measurement gaps and smtc2 is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in Table 9.2.6.2-1, Table 9.2.6.2-2, and Table 9.2.6.2-3 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

Table 9.2.6.2-1: Time period for PSS/SSS detection (FR1)

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, 5 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(M2Note 1x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil( 5 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 3: For a UE supporting concurrent measurement gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 4: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. |

**Table 9.2.6.2-2: Time period for PSS/SSS detection (FR2)**

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, Mpss/sss\_sync\_with\_gaps x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(600ms, ceil(1.5x Mpss/sss\_sync\_with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil( Mpss/sss\_sync\_with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer. |

Table 9.2.6.2-3: Time period for time index detection (Frequency range FR1)

|  |  |
| --- | --- |
| DRX cycle | TSSB\_time\_index\_intra |
| No DRX | max(120ms, ceil(3 x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(120ms, ceil(M2Note 1x 3 x Kgap) x max(MGRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320ms | Ceil(3 x Kgap )x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 3: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 4: When highSpeedMeasCA-Scell-r17 is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. |

Table 9.2.6.2-7: Void

Table 9.2.6.2-8: Void

Table 9.2.6.2-9: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (FR2) when SMTC period <=40ms

|  |  |
| --- | --- |
| DRX cycle | TPSS/SSS\_sync\_intra |
| No DRX | max(600ms, M1Note 2 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 80ms | max(600ms, ceil(M1Note2 x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| 80ms< DRX cycle≤ 320ms | max(600ms, ceil(Mpss/sss\_sync\_with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil( Mpss/sss\_sync\_with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

#### 9.2.6.3 Intrafrequency Measurement Period

The requirements in this clause apply when a measurement gap is provided or when an activated Pre-MG is provided without any pre-MG status changed during the measurement period.

The measurement period for FR1 intrafrequency measurements with gaps is as shown in table 9.2.6.3-1.

The measurement period for FR2 intrafrequency measurements with gaps is as shown in table 9.2.6.3-2.

When *highSpeedMeasFlag-r16* is configured, T SSB\_measurement\_period\_intra is specified in Table 9.2.6.3-3.

If MCG DRX is in use, measurement period requirements for intra-frequency measurement in MCG specified in Table 9.2.6.3-1 and Table 9.2.6.3-2, shall depend on the MCG DRX cycle. If SCG DRX is in use, measurement period requirements for intra-frequency measurement in SCG specified in Table 9.2.6.3-1and Table 9.2.6.3-2, shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

For either an FR1 or FR2 serving cell, longer measurement period would be expected during the period Tidentify\_CGI when the UE is requested to decode an NR CGI.

Table 9.2.6.3-1: Measurement period for intra-frequency measurements with gaps(FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil(5 x Kgap )x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(200ms, ceil(1.5x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle))x CSSFintra |
| DRX cycle>320ms | Ceil(5 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer. |

Table 9.2.6.3-2: Measurement period for intra-frequency measurements with gaps(FR2)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(Mmeas\_period with\_gaps x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320ms | max(400ms, ceil(1.5 x Mmeas\_period with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| DRX cycle>320ms | Ceil( Mmeas\_period with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer. |

Table 9.2.6.3-3: Measurement period When *highSpeedMeasFlag-r16* is configured (Frequency Range FR1)

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(200ms, ceil( 5 x Kgap ) x max(MGRP, SMTC period)) Note 1 x CSSFintra |
| DRX cycle≤ 160ms | max(200ms, ceil(M2Note 2 x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| 160ms < DRX cycle≤ 320ms | max(200ms, ceil(M2Note 2 x 4 x Kgap) x max(MGRP, DRX cycle)) x CSSFintra |
| DRX cycle>320ms | Ceil(Y Note 3 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 3: Y=3 when SMTC <= 40ms, Y=5 when SMTC > 40msNOTE 4: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 5: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 6: When highSpeedMeasCA-Scell-r17 is configured, the requirements also apply to UE on measurements of secondary component carrier with active SCell. |

Table 9.2.6.3-4: Measurement period for intra-frequency measurements with gaps when *highSpeedMeasFlagFR2-r17* is configured (FR2) when SMTC period<=40ms

|  |  |
| --- | --- |
| DRX cycle | T SSB\_measurement\_period\_intra  |
| No DRX | max(400ms, ceil(M1Note 2 x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 80ms | max(400ms, ceil(M1Note 2 x Kgap) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| 80ms< DRX cycle≤ 320ms | max(400ms, ceil(Mmeas\_period with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle)) Note 1 x CSSFintra |
| DRX cycle>320ms | Ceil( Mmeas\_period with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent gaps, if multiple concurrent gaps are configured, the MGRP is the periodicity of the MG pattern associated to the intra-frequency layer.NOTE 2: For UE supporting power class 6, M1= 6 if [*highSpeedMeasFlagFR2-r17* = set1] or M1= 18 if [*highSpeedMeasFlagFR2-r17* = set2] |