**3GPP TSG RAN2#118-e *R2-220xxxx***

**E-meeting, 09 – 20 May 2022**

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| *CR-Form-v12.2* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.331** | **CR** |  | **rev** | **-** | **Current version:** | **17.0.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:*** | Draft CR for TDRA configuration modifications | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Huawei, HiSilicon | | | | | | | | | |
| ***Source to TSG:*** | RAN2 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_cov\_enh-Core, NR\_ext\_to\_71GHz-Core | | | | |  | ***Date:*** | | | 2022-05 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-17 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18) Rel-19 (Release 19)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | |  | | | | | | | | |
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| ***Consequences if not approved:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | | TS/TR … CR ... | | |
| ***affected:*** | |  | **X** | Test specifications | | | | TS/TR ... CR ... | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | | TS/TR ... CR ... | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

### 6.3.2 Radio resource control information elements

<UNCHANGED PARTS OMITTED>

#### – *PDSCH-Config*

The *PDSCH-Config* IE is used to configure the UE specific PDSCH parameters. If this IE is used for MBS CFR, the following fields shall be absent: minimumSchedulingOffsetK0, antennaPortsFieldPresenceDCI-1-2, aperiodicZP-CSI-RS-ResourceSetsToAddModListDCI-1-2, aperiodicZP-CSI-RS-ResourceSetsToReleaseListDCI-1-2, dmrs-DownlinkForPDSCH-MappingTypeA-DCI-1-2, dmrs-DownlinkForPDSCH-MappingTypeB-DCI-1-2, dmrs-SequenceInitializationDCI-1-2, harq-ProcessNumberSizeDCI-1-2, mcs-TableDCI-1-2, numberOfBitsForRV-DCI-1-2, pdsch-AggregationFactor, pdsch-TimeDomainAllocationListDCI-1-2, prb-BundlingTypeDCI-1-2, priorityIndicatorDCI-1-2, rateMatchPatternGroup1DCI-1-2, rateMatchPatternGroup2DCI-1-2, resourceAllocationType1GranularityDCI-1-2, vrb-ToPRB-InterleaverDCI-1-2, referenceOfSLIVDCI-1-2, resourceAllocationDCI-1-2, dataScramblingIdentityPDSCH2-r16, repetitionSchemeConfig.

*PDSCH-Config* information element

-- ASN1START

-- TAG-PDSCH-CONFIG-START

PDSCH-Config ::= SEQUENCE {

dataScramblingIdentityPDSCH INTEGER (0..1023) OPTIONAL, -- Need S

dmrs-DownlinkForPDSCH-MappingTypeA SetupRelease { DMRS-DownlinkConfig } OPTIONAL, -- Need M

dmrs-DownlinkForPDSCH-MappingTypeB SetupRelease { DMRS-DownlinkConfig } OPTIONAL, -- Need M

tci-StatesToAddModList SEQUENCE (SIZE(1..maxNrofTCI-States)) OF TCI-State OPTIONAL, -- Need N

tci-StatesToReleaseList SEQUENCE (SIZE(1..maxNrofTCI-States)) OF TCI-StateId OPTIONAL, -- Need N

vrb-ToPRB-Interleaver ENUMERATED {n2, n4} OPTIONAL, -- Need S

resourceAllocation ENUMERATED { resourceAllocationType0, resourceAllocationType1, dynamicSwitch},

pdsch-TimeDomainAllocationList SetupRelease { PDSCH-TimeDomainResourceAllocationList } OPTIONAL, -- Need M

pdsch-AggregationFactor ENUMERATED { n2, n4, n8 } OPTIONAL, -- Need S

rateMatchPatternToAddModList SEQUENCE (SIZE (1..maxNrofRateMatchPatterns)) OF RateMatchPattern OPTIONAL, -- Need N

rateMatchPatternToReleaseList SEQUENCE (SIZE (1..maxNrofRateMatchPatterns)) OF RateMatchPatternId OPTIONAL, -- Need N

rateMatchPatternGroup1 RateMatchPatternGroup OPTIONAL, -- Need R

rateMatchPatternGroup2 RateMatchPatternGroup OPTIONAL, -- Need R

rbg-Size ENUMERATED {config1, config2},

mcs-Table ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

maxNrofCodeWordsScheduledByDCI ENUMERATED {n1, n2} OPTIONAL, -- Need R

prb-BundlingType CHOICE {

staticBundling SEQUENCE {

bundleSize ENUMERATED { n4, wideband } OPTIONAL -- Need S

},

dynamicBundling SEQUENCE {

bundleSizeSet1 ENUMERATED { n4, wideband, n2-wideband, n4-wideband } OPTIONAL, -- Need S

bundleSizeSet2 ENUMERATED { n4, wideband } OPTIONAL -- Need S

}

},

zp-CSI-RS-ResourceToAddModList SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-Resources)) OF ZP-CSI-RS-Resource

OPTIONAL, -- Need N

zp-CSI-RS-ResourceToReleaseList SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-Resources)) OF ZP-CSI-RS-ResourceId

OPTIONAL, -- Need N

aperiodic-ZP-CSI-RS-ResourceSetsToAddModList SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-ResourceSets)) OF ZP-CSI-RS-ResourceSet

OPTIONAL, -- Need N

aperiodic-ZP-CSI-RS-ResourceSetsToReleaseList SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-ResourceSets)) OF ZP-CSI-RS-ResourceSetId

OPTIONAL, -- Need N

sp-ZP-CSI-RS-ResourceSetsToAddModList SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-ResourceSets)) OF ZP-CSI-RS-ResourceSet

OPTIONAL, -- Need N

sp-ZP-CSI-RS-ResourceSetsToReleaseList SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-ResourceSets)) OF ZP-CSI-RS-ResourceSetId

OPTIONAL, -- Need N

p-ZP-CSI-RS-ResourceSet SetupRelease { ZP-CSI-RS-ResourceSet }

OPTIONAL, -- Need M

...,

[[

maxMIMO-Layers-r16 SetupRelease { MaxMIMO-LayersDL-r16 } OPTIONAL, -- Need M

minimumSchedulingOffsetK0-r16 SetupRelease { MinSchedulingOffsetK0-Values-r16 } OPTIONAL, -- Need M

-- Start of the parameters for DCI format 1\_2 introduced in V16.1.0

antennaPortsFieldPresenceDCI-1-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

aperiodicZP-CSI-RS-ResourceSetsToAddModListDCI-1-2-r16 SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-ResourceSets)) OF ZP-CSI-RS-ResourceSet

OPTIONAL, -- Need N

aperiodicZP-CSI-RS-ResourceSetsToReleaseListDCI-1-2-r16 SEQUENCE (SIZE (1..maxNrofZP-CSI-RS-ResourceSets)) OF ZP-CSI-RS-ResourceSetId

OPTIONAL, -- Need N

dmrs-DownlinkForPDSCH-MappingTypeA-DCI-1-2-r16 SetupRelease { DMRS-DownlinkConfig } OPTIONAL, -- Need M

dmrs-DownlinkForPDSCH-MappingTypeB-DCI-1-2-r16 SetupRelease { DMRS-DownlinkConfig } OPTIONAL, -- Need M

dmrs-SequenceInitializationDCI-1-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

harq-ProcessNumberSizeDCI-1-2-r16 INTEGER (0..4) OPTIONAL, -- Need R

mcs-TableDCI-1-2-r16 ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

numberOfBitsForRV-DCI-1-2-r16 INTEGER (0..2) OPTIONAL, -- Need R

pdsch-TimeDomainAllocationListDCI-1-2-r16 SetupRelease { PDSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

prb-BundlingTypeDCI-1-2-r16 CHOICE {

staticBundling-r16 SEQUENCE {

bundleSize-r16 ENUMERATED { n4, wideband } OPTIONAL -- Need S

},

dynamicBundling-r16 SEQUENCE {

bundleSizeSet1-r16 ENUMERATED { n4, wideband, n2-wideband, n4-wideband } OPTIONAL, -- Need S

bundleSizeSet2-r16 ENUMERATED { n4, wideband } OPTIONAL -- Need S

}

} OPTIONAL, -- Need R

priorityIndicatorDCI-1-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

rateMatchPatternGroup1DCI-1-2-r16 RateMatchPatternGroup OPTIONAL, -- Need R

rateMatchPatternGroup2DCI-1-2-r16 RateMatchPatternGroup OPTIONAL, -- Need R

resourceAllocationType1GranularityDCI-1-2-r16 ENUMERATED {n2,n4,n8,n16} OPTIONAL, -- Need S

vrb-ToPRB-InterleaverDCI-1-2-r16 ENUMERATED {n2, n4} OPTIONAL, -- Need S

referenceOfSLIVDCI-1-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

resourceAllocationDCI-1-2-r16 ENUMERATED { resourceAllocationType0, resourceAllocationType1, dynamicSwitch}

OPTIONAL, -- Need M

-- End of the parameters for DCI format 1\_2 introduced in V16.1.0

priorityIndicatorDCI-1-1-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

dataScramblingIdentityPDSCH2-r16 INTEGER (0..1023) OPTIONAL, -- Need R

pdsch-TimeDomainAllocationList-r16 SetupRelease { PDSCH-TimeDomainResourceAllocationList-r16 } OPTIONAL, -- Need M

repetitionSchemeConfig-r16 SetupRelease { RepetitionSchemeConfig-r16} OPTIONAL -- Need M

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[[

repetitionSchemeConfig-v1630 SetupRelease { RepetitionSchemeConfig-v1630} OPTIONAL -- Need M

]],

[[

pdsch-HARQ-ACK-OneShotFeedbackDCI-1-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

pdsch-HARQ-ACK-EnhType3DCI-1-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

pdsch-HARQ-ACK-EnhType3DCI-Field-1-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

pdsch-HARQ-ACK-RetxDCI-1-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

pucch-sSCellDynDCI-1-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

dl-OrJoint-TCIState CHOICE {

lists SEQUENCE {

dl-orJoint-TCIState-ToAddModList-r17 SEQUENCE (SIZE (1..max-DLorJointTCI-r17)) OF DLorJoint-TCIState-r17

OPTIONAL, -- Need N

dl-orJoint-TCIState-ToReleaseList-r17 SEQUENCE (SIZE (1..max-DLorJointTCI-r17)) OF TCI-StateId

OPTIONAL -- Need N

},

refUnifiedTCIStateList-r17 RefUnifiedTCIStateList-r17

} OPTIONAL, -- Need R

beamAppTime-r17 ENUMERATED {n1, n2, n4, n7, n14, n28, n42, n56, n70, n84, n98, n112, n224, n336, spare2,

spare1} OPTIONAL, -- Need R

sfnSchemePdsch-r17 ENUMERATED {sfnSchemeA,sfnSchemeB} OPTIONAL, -- Need R

pdsch-TimeDomainAllocationListForMultiPDSCH-r17 SetupRelease { MultiPDSCH-TimeDomainResourceAllocationList-r17 } OPTIONAL, -- Need M

pdsch-TimeDomainAllocationList-r17 SetupRelease { MultiPDSCH-TimeDomainResourceAllocationList-r17 } OPTIONAL, -- Need M

pdsch-TimeDomainAllocationListDCI-1-2-r17 SetupRelease { MultiPDSCH-TimeDomainResourceAllocationList-r17 } OPTIONAL, -- Need M

dmrs-FD-OCC-DisabledForRank1-PDSCH-r17 ENUMERATED {true} OPTIONAL, -- Need R

minimumSchedulingOffsetK0-r17 SetupRelease { MinSchedulingOffsetK0-Values-r17 } OPTIONAL, -- Need M

harq-ProcessNumberSizeDCI-1-2-v1700 INTEGER (0..5) OPTIONAL, -- Need R

harq-ProcessNumberSizeDCI-1-1-r17 INTEGER (5) OPTIONAL, -- Need R

mcs-Table-r17 ENUMERATED {qam1024} OPTIONAL, -- Need R

mcs-TableDCI-1-2-r17 ENUMERATED {qam1024} OPTIONAL, -- Need R

xOverheadMulticast-r17 ENUMERATED {xOh6, xOh12, xOh18} OPTIONAL, -- Need S

priorityIndicatorDCI-4-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need S

sizeDCI-4-2-r17 INTEGER (20..maxDCI-4-2-Size-r17) OPTIONAL -- Need R

]]

}

RateMatchPatternGroup ::= SEQUENCE (SIZE (1..maxNrofRateMatchPatternsPerGroup)) OF CHOICE {

cellLevel RateMatchPatternId,

bwpLevel RateMatchPatternId

}

MinSchedulingOffsetK0-Values-r16 ::= SEQUENCE (SIZE (1..maxNrOfMinSchedulingOffsetValues-r16)) OF INTEGER (0..maxK0-SchedulingOffset-r16)

MinSchedulingOffsetK0-Values-r17 ::= SEQUENCE (SIZE (1..maxNrOfMinSchedulingOffsetValues-r16)) OF INTEGER (0..maxK0-SchedulingOffset-r17)

MaxMIMO-LayersDL-r16 ::= INTEGER (1..8)

RefUnifiedTCIStateList-r17 ::= SEQUENCE {

servingcell-r17 ServCellIndex,

bwp-r17 BWP-Id

}

-- TAG-PDSCH-CONFIG-STOP

-- ASN1STOP

|  |
| --- |
| *PDSCH-Config* field descriptions |
| ***antennaPortsFieldPresenceDCI-1-2***  Configure the presence of "Antenna ports" field in DCI format 1\_2. When the field is configured, then the "Antenna ports" field is present in DCI format 1\_2. Otherwise, the field size is set to 0 for DCI format 1\_2 (See TS 38.212 [17], clause 7.3.1.1.3). If neither *dmrs-DownlinkForPDSCH-MappingTypeA-DCI-1-2* nor *dmrs-DownlinkForPDSCH-MappingTypeB-DCI-1-2* is configured, this field is absent. |
| ***aperiodic-ZP-CSI-RS-ResourceSetsToAddModList, aperiodic-ZP-CSI-RS-ResourceSetsToAddModListDCI-1-2***  AddMod/Release lists for configuring aperiodically triggered zero-power CSI-RS resource sets. Each set contains a *ZP-CSI-RS-ResourceSetId* and the IDs of one or more *ZP-CSI-RS-Resources* (the actual resources are defined in the *zp-CSI-RS-ResourceToAddModList*). The network configures the UE with at most 3 aperiodic *ZP-CSI-RS-ResourceSets* and it uses only the *ZP-CSI-RS-ResourceSetId* 1 to 3. The network triggers a set by indicating its *ZP-CSI-RS-ResourceSetId* in the DCI payload. The DCI codepoint '01' triggers the resource set with *ZP-CSI-RS-ResourceSetId* 1, the DCI codepoint '10' triggers the resource set with *ZP-CSI-RS-ResourceSetId 2*, and the DCI codepoint '11' triggers the resource set with *ZP-CSI-RS-ResourceSetId* 3 (see TS 38.214 [19], clause 5.1.4.2). The field *aperiodic-ZP-CSI-RS-ResourceSetsToAddModList* applies to DCI format 1\_1 and the field *aperiodic-ZP-CSI-RS-ResourceSetsToAddModListDCI-1-2* applies to DCI format 1\_2 (see TS 38.214 [19], clause 5.1.4.2 and TS 38.212 [17] clause 7.3.1). |
| ***beamAppTime***  Indicates the first slot to apply the unified TCI indicated by DCI as specified in TS 38.214 Clause 5.1.5. The value n1 means 1 symbol, n2 two symbols and so on. The first slot is at least Y symbols indicated by beamAppTime parameter after the last symbol of the acknowledgment of the joint or separate DL/UL beam indication. The same value shall be configured for all serving cells in any one of the *simultaneousU-TCI-UpdateListN* configured in IE *CellGroupConfig* based on the smallest SCS of the active BWP. |
| ***dataScramblingIdentityPDSCH, dataScramblingIdentityPDSCH2***  Identifier(s) used to initialize data scrambling (c\_init) for PDSCH as specified in TS 38.211 [16], clause 7.3.1.1. The *dataScramblingIdentityPDSCH2* is configured if *coresetPoolIndex* is configured with 1 for at least one CORESET in the same BWP. |
| ***dl-OrJoint-TCIState-ToAddModList***  A list of Transmission Configuration Indicator (TCI) states indicating a transmission configuration which includes QCL-relationships between the DL RSs in one RS set and the PDSCH DMRS ports (see TS 38.214 [19], clause 5.1.5). |
| ***dmrs-DownlinkForPDSCH-MappingTypeA, dmrs-DownlinkForPDSCH-MappingTypeA-DCI-1-2***  DMRS configuration for PDSCH transmissions using PDSCH mapping type A (chosen dynamically via *PDSCH-TimeDomainResourceAllocation*). Only the fields *dmrs-Type*, *dmrs-AdditionalPosition* and *maxLength* may be set differently for mapping type A and B. The field *dmrs-DownlinkForPDSCH-MappingTypeA* applies to DCI format 1\_1 and the field *dmrs-DownlinkForPDSCH-MappingTypeA-DCI-1-2* applies to DCI format 1\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***dmrs-DownlinkForPDSCH-MappingTypeB, dmrs-DownlinkForPDSCH-MappingTypeB-DCI-1-2***  DMRS configuration for PDSCH transmissions using PDSCH mapping type B (chosen dynamically via *PDSCH-TimeDomainResourceAllocation*). Only the fields *dmrs-Type*, *dmrs-AdditionalPosition* and *maxLength* may be set differently for mapping type A and B. The field *dmrs-DownlinkForPDSCH-MappingTypeB* applies to DCI format 1\_1 and the field *dmrs-DownlinkForPDSCH-MappingTypeB-DCI-1-2* applies to DCI format 1\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***dmrs-FD-OCC-DisabledForRank1-PDSCH***  If configured, the UE may assume that the set of remaining orthogonal antenna ports, which are within the same code division multiplexing (CDM) group and have different frequency domain orthogonal cover codes (FD-OCC), are not associated with the PDSCH of another UE (see TS 38.214 [19], clause 5.1.6.2). It is applicable for PDSCH SCS of 480 and 960 kHz when rank 1 PDSCH with type-1 or type-2 DMRS is scheduled. |
| ***dmrs-SequenceInitializationDCI-1\_2***  Configure whether the field "DMRS Sequence Initialization" is present or not in DCI format 1\_2 If the field is absent, then the UE applies the value of 0 bit for the field "DMRS Sequence Initialization" in DCI format 1\_2. If the field is present, then the UE applies the value of 1 bit as in DCI format 1\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***harq-ProcessNumberSizeDCI-1-2***  Configure the number of bits for the field "HARQ process number" in DCI format 1\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***maxMIMO-Layers***  Indicates the maximum number of MIMO layers to be used for PDSCH in this DL BWP. If not configured, the UE uses the *maxMIMO-Layers* configuration in IE *PDSCH-ServingCellConfig* of the serving cell to which this BWP belongs, when the UE operates in this BWP. The value of *maxMIMO-Layers* for a DL BWP shall be smaller than or equal to the value of *maxMIMO-Layers* configured in IE *PDSCH-ServingCellConfig* of the serving cell to which this BWP belongs.  For MBS multicast, indicates the maximum number of MIMO layers to be used for group-common PDSCH of MBS multicast in this CFR. If not configured for CFR, the UE applies value 1. The value of *maxMIMO-Layers* for a CFR shall be smaller than or equal to the value of *maxMIMO-Layers* configured in *PDSCH-ServingCellConfig* IE of the serving cell to which this CFR belongs. |
| ***maxNrofCodeWordsScheduledByDCI***  Maximum number of code words that a single DCI may schedule. This changes the number of MCS/RV/NDI bits in the DCI message from 1 to 2. |
| ***mcs-Table***  Indicates which MCS table the UE shall use for PDSCH for DCI formats 1\_0 and 1\_1 (see TS 38.214 [19], clause 5.1.3.1). If all fields are absent the UE applies the value 64QAM. If the field *mcs-Table-r17* is present for DCI format 1\_1, the network does not configure the field *mcs-Table* (without suffix). For a RedCap UE, the 256QAM MCS table for PDSCH is only supported if the UE indicates support of 256QAM for PDSCH. |
| ***mcs-TableDCI-1-2***  Indicates which MCS table the UE shall use for PDSCH for DCI format 1\_2 (see TS 38.214 [19], clause 5.1.3.1). If all fields are absent the UE applies the value 64QAM. If the field *mcs-TableDCI-1-2-r17* is present, the network does not configure the field *mcs-TableDCI-1-2-r16*. For a RedCap UE, the 256QAM MCS table for PDSCH is only supported if the UE indicates support of 256QAM for PDSCH. |
| ***minimumSchedulingOffsetK0***  List of minimum K0 values. Minimum K0 parameter denotes minimum applicable value(s) for the TDRA table for PDSCH and for A-CSI RS triggering Offset(s) (see TS 38.214 [19], clause 5.3.1). |
| ***numberOfBitsForRV-DCI-1-2***  Configures the number of bits for "Redundancy version" in the DCI format 1\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.214 [19], clause 5.1.2.1). |
| ***pdsch-AggregationFactor***  Number of repetitions for data (see TS 38.214 [19], clause 5.1.2.1). When the field is absent in *PDSCH-Config* which is not used for MBS CFR, the UE applies the value 1. |
| ***pdsch-HARQ-ACK-EnhType3DCI-1-2***  When configured, enhanced Type 3 HARQ-ACK codebook triggering by DCI format 1\_2 is enabled. |
| ***pdsch-HARQ-ACK-EnhType3DCI-Field-1-2***  Enables the enhanced Type 3 codebook through a new DCI field to indicate the enhanced Type 3 HARQ-ACK codebook in DCI format 1\_2 if the more than one enhanced Type 3 HARQ-ACK codebook is configured for the primary PUCCH cell group. |
| ***pdsch-HARQ-ACK-OneShotFeedbackDCI-1-2***  When configured, DCI format 1\_2 can request the UE to report A/N for all HARQ processes and all component carriers configured in the PUCCH group (see TS 38.212 [17], clause 7.3.1). |
| ***pdsch-HARQ-ACK-RetxDCI-1-2***  When configured, DCI format 1\_2 can request the UE to perform a HARQ-ACK re-transmission on a PUCCH resource (see TS 38.213 [13], clause 9.1.5). |
| ***pdsch-TimeDomainAllocationList, pdsch-TimeDomainAllocationListDCI-1-2, pdsch-TimeDomainAllocationListForMultiPDSCH***  List of time-domain configurations for timing of DL assignment to DL data.  The field *pdsch-TimeDomainAllocationList* (with or without suffix) applies to DCI format 1\_0 and DCI format 1\_1 (see table 5.1.2.1.1-1 in TS 38.214 [19]), and if the field *pdsch-TimeDomainAllocationListDCI-1-2* is not configured, to DCI format 1\_2. If the field *pdsch-TimeDomainAllocationListDCI-1-2* is configured, it applies to DCI format 1\_2 (see table 5.1.2.1.1-1A in TS 38.214 [19]). The field *pdsch-TimeDomainAllocationListForMultiPDSCH* applies to DCI format 1\_1.  The network does not configure the *pdsch-TimeDomainAllocationList-r16* simultaneously with the *pdsch-TimeDomainAllocationList* (without suffix) in the same *PDSCH-Config*. |
| ***prb-BundlingType,*** ***prb-BundlingTypeDCI-1-2***  Indicates the PRB bundle type and bundle size(s) (see TS 38.214 [19], clause 5.1.2.3). If *dynamic* is chosen, the actual *bundleSizeSet1 or bundleSizeSet2* to use is indicated via DCI. Constraints on *bundleSize(Set)* setting depending on *vrb-ToPRB-Interleaver* and *rbg-Size* settings are described in TS 38.214 [19], clause 5.1.2.3. If a *bundleSize(Set)* value is absent, the UE applies the value *n2*. The field *prb-BundlingType* applies to DCI format 1\_1, and the field *prb-BundlingTypeDCI-1-2* applies to DCI format 1\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.214 [19], clause 5.1.2.3). |
| ***priorityIndicatorDCI-1-1, priorityIndicatorDCI-1-2, priorityIndicatorDCI-4-2***  Configure the presence of "priority indicator" in DCI format 1\_1/1\_2/4\_2. When the field is absent in the IE, then 0 bit for "priority indicator" in DCI format 1\_1/1\_2/4\_2. The field *priorityIndicatorDCI-1-1* applies to DCI format 1\_1, the field *priorityIndicatorDCI-1-2* applies to DCI format 1\_2 and the field *priorityIndicatorDCI-4-2* applies to DCI format 4\_2, respectively (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13] clause 9). |
| ***pucch-sSCellDynDCI-1-2***  When configured, PUCCH cell switching based on dynamic indication in DCI format 1\_2 is enabled (see TS 38.213 [13], clause 9.A). |
| ***p-ZP-CSI-RS-ResourceSet***  A set of periodically occurring ZP-CSI-RS-Resources (the actual resources are defined in the zp-CSI-RS-ResourceToAddModList). The network uses the ZP-CSI-RS-ResourceSetId=0 for this set.  If *p-ZP-CSI-RS-ResourceSet* is configured in both *PDSCH-Config* for MBS CFR and *PDSCH-Config* for the assoicated BWP, it is subject to UE capability whether the *p-ZP-CSI-RS-ResourceSet* configured in *PDSCH-Config* for MBS CFR can be different from the *p-ZP-CSI-RS-ResourceSet* configured in *PDSCH-Config* for the assoicated BWP. |
| ***rateMatchPatternGroup1, rateMatchPatternGroup1DCI-1-2***  The IDs of a first group of *RateMatchPatterns* defined in *PDSCH-Config*->*rateMatchPatternToAddModList* (BWP level) or in *ServingCellConfig* ->*rateMatchPatternToAddModLis*t (cell level). These patterns can be activated dynamically by DCI (see TS 38.214 [19], clause 5.1.4.1). The field *rateMatchPatternGroup1* applies to DCI format 1\_1, and the field *rateMatchPatternGroup1DCI-1-2* applies to DCI format 1\_2 (see TS 38.214 [19], clause 5.1.4.1). |
| ***rateMatchPatternGroup2, rateMatchPatternGroup2DCI-1-2***  The IDs of a second group of *RateMatchPatterns* defined in *PDSCH-Config*->*rateMatchPatternToAddModList* (BWP level) or in *ServingCellConfig* ->*rateMatchPatternToAddModLis*t (cell level). These patterns can be activated dynamically by DCI (see TS 38.214 [19], clause 5.1.4.1). The field *rateMatchPatternGroup2* applies to DCI format 1\_1, and the field *rateMatchPatternGroup2DCI-1-2* applies to DCI format 1\_2 (see TS 38.214 [19], clause 5.1.4.1). |
| ***rateMatchPatternToAddModList***  Resources patterns which the UE should rate match PDSCH around. The UE rate matches around the union of all resources indicated in the rate match patterns (see TS 38.214 [19], clause 5.1.4.1). |
| ***rbg-Size***  Selection between config 1 and config 2 for RBG size for PDSCH. The UE ignores this field if *resourceAllocation* is set to *resourceAllocationType1* (see TS 38.214 [19], clause 5.1.2.2.1). |
| ***referenceOfSLIVDCI-1-2***  Enable using the starting symbol of the PDCCH monitoring occasion in which the DL assignment is detected as the reference of the SLIV for DCI format 1\_2. When the RRC parameter enables the utilization of the new reference, the new reference is applied for TDRA entries with K0=0. For other entries (if any) in the same TDRA table, the reference is slot boundary as in Rel-15. PDSCH mapping type A is not supported with the new reference. The new reference of SLIV is not configured for a serving cell configured to be scheduled by cross-carrier scheduling on a scheduling cell with different numerology (see TS 38.212 [17] clause 7.3.1 and TS 38.214 [19] clause 5.1.2.1). |
| ***refUnifiedTCIStateList***  Provides the serving cell and BWP where the configuration for *DLorJoint-TCIState-ToAddModList-r17* in this IE or *ul-TCIState-ToAddModList* in IE *BWP-UplinkDedicated* is for this serving cell and BWP. When this field is present, DLorJoint-TCIState-ToAddModList and DLorJoint-TCIState-ToReleaseList are not present. |
| ***repetitionSchemeConfig***  Configure the UE with repetition schemes. The network does not configure *repetitionSchemeConfig-r16* and *repetitionSchemeConfig-v1630* simultaneously to *setup* in the same *PDSCH-Config*. |
| ***resourceAllocation, resourceAllocationDCI-1-2***  Configuration of resource allocation type 0 and resource allocation type 1 for non-fallback DCI (see TS 38.214 [19], clause 5.1.2.2). The field *resourceAllocation* applies to DCI format 1\_1, and the field *resourceAllocationDCI-1-2* applies to DCI format 1\_2 (see TS 38.214 [19], clause 5.1.2.2). |
| ***resourceAllocationType1GranularityDCI-1-2***  Configure the scheduling granularity applicable for both the starting point and length indication for resource allocation type 1 in DCI format 1\_2. If this field is absent, the granularity is 1 PRB (see TS 38.214 [19], clause 5.1.2.2.2). |
| ***sfnSchemePdsch***  This parameter is used to configure SFN scheme for PDSCH: sfnSchemeA or sfnSchemeB as specified in TS 38.xxx. The field can be configured only for BWP-DownlinkDedicated. |
| ***sizeDCI-4-2***  Indicates the Size of DCI format 4-2 (see TS 38.213 [13], clause 10.1). |
| ***sp-ZP-CSI-RS-ResourceSetsToAddModList***  AddMod/Release lists for configuring semi-persistent zero-power CSI-RS resource sets. Each set contains a *ZP-CSI-RS-ResourceSetId* and the IDs of one or more *ZP-CSI-RS-Resources* (the actual resources are defined in the *zp-CSI-RS-ResourceToAddModList*) (see TS 38.214 [19], clause 5.1.4.2). |
| ***tci-StatesToAddModList***  A list of Transmission Configuration Indicator (TCI) states indicating a transmission configuration which includes QCL-relationships between the DL RSs in one RS set and the PDSCH DMRS ports (see TS 38.214 [19], clause 5.1.5). |
| ***vrb-ToPRB-Interleaver, vrb-ToPRB-InterleaverDCI-1-2***  Interleaving unit configurable between 2 and 4 PRBs (see TS 38.211 [16], clause 7.3.1.6). When the field is absent, the UE performs non-interleaved VRB-to-PRB mapping. |
| ***xOverheadMulticast***  Accounts for an overhead from CSI-RS, CORESET etc. If the field is absent, the UE applies value xOh0 (see TS 38.214 [19]). |
| ***zp-CSI-RS-ResourceToAddModList***  A list of Zero-Power (ZP) CSI-RS resources used for PDSCH rate-matching. Each resource in this list may be referred to from only one type of resource set, i.e., aperiodic, semi-persistent or periodic (see TS 38.214 [19]). |

<UNCHANGED PARTS OMITTED>

#### – *PDSCH-TimeDomainResourceAllocationList*

The IE *PDSCH-TimeDomainResourceAllocation* is used to configure a time domain relation between PDCCH and PDSCH. The *PDSCH-TimeDomainResourceAllocationList* contains one or more of such *PDSCH-TimeDomainResourceAllocations*. The network indicates in the DL assignment which of the configured time domain allocations the UE shall apply for that DL assignment. The UE determines the bit width of the DCI field based on the number of entries in the *PDSCH-TimeDomainResourceAllocationList*. Value 0 in the DCI field refers to the first element in this list, value 1 in the DCI field refers to the second element in this list, and so on.

*PDSCH-TimeDomainResourceAllocationList* information element

-- ASN1START

-- TAG-PDSCH-TIMEDOMAINRESOURCEALLOCATIONLIST-START

PDSCH-TimeDomainResourceAllocationList ::= SEQUENCE (SIZE(1..maxNrofDL-Allocations)) OF PDSCH-TimeDomainResourceAllocation

PDSCH-TimeDomainResourceAllocation ::= SEQUENCE {

k0 INTEGER(0..32) OPTIONAL, -- Need S

mappingType ENUMERATED {typeA, typeB},

startSymbolAndLength INTEGER (0..127)

}

PDSCH-TimeDomainResourceAllocationList-r16 ::= SEQUENCE (SIZE(1..maxNrofDL-Allocations)) OF PDSCH-TimeDomainResourceAllocation-r16

PDSCH-TimeDomainResourceAllocation-r16 ::= SEQUENCE {

k0-r16 INTEGER(0..32) OPTIONAL, -- Need S

mappingType-r16 ENUMERATED {typeA, typeB},

startSymbolAndLength-r16 INTEGER (0..127),

repetitionNumber-r16 ENUMERATED {n2, n3, n4, n5, n6, n7, n8, n16} OPTIONAL, -- Cond Formats1-0and1-1

...

}

MultiPDSCH-TDRA-List-r17 ::= SEQUENCE (SIZE(1.. maxNrofDL-Allocations)) OF MultiPDSCH-TDRA-r17

MultiPDSCH-TDRA-r17 ::= SEQUENCE {

pdsch-TDRA-List-r17 SEQUENCE (SIZE(1..maxNrofMultiplePDSCHs-r17)) OF PDSCH-TimeDomainResourceAllocation-r17,

...

}

PDSCH-TimeDomainResourceAllocation-r17 ::= SEQUENCE {

k0-r17 INTEGER (0..128) OPTIONAL, -- Need S

mappingType-r17 ENUMERATED {typeA, typeB},

startSymbolAndLength-r17 INTEGER (0..127),

repetitionNumber-r17 ENUMERATED {n2, n3, n4, n5, n6, n7, n8, n16} OPTIONAL, -- Cond Formats1-0and1-1

...

}

-- TAG-PDSCH-TIMEDOMAINRESOURCEALLOCATIONLIST-STOP

-- ASN1STOP

|  |
| --- |
| *PDSCH-TimeDomainResourceAllocation* field descriptions |
| ***k0***  Slot offset between DCI and its scheduled PDSCH (see TS 38.214 [19], clause 5.1.2.1). For k0-r17, only values {0..32} are applicable for PDSCH SCS of 120 kHz. When the field is absent the UE applies the value 0. |
| ***mappingType***  PDSCH mapping type (see TS 38.214 [19], clause 5.3). |
| ***repetitionNumber***  Indicates the number of PDSCH transmission occasions for slot-based repetition scheme in IE *RepetitionSchemeConfig.* The parameter is used as specified in 38.214 [19]. This field is not present when *PDSCH-TimeDomainResourceAllocation-r16* is included in SIB20. |
| ***startSymbolAndLength***  An index giving valid combinations of start symbol and length (jointly encoded) as start and length indicator (SLIV). The network configures the field so that the allocation does not cross the slot boundary (see TS 38.214 [19], clause 5.1.2.1). |

|  |
| --- |
| *MultiPDSCH-TDRA* field descriptions |
| ***pdsch-TDRA-List***  One or multiple PDSCHs which can be in consecutive or non-consecutive slots (see TS 38.214 [19], clause 5.1.2.1). |

|  |  |
| --- | --- |
| Conditional Presence | Explanation |
| *Formats1-0and1-1* | In *pdsch-TimeDomainResourceAllocationList-r16*, this field is optionally present, Need R.  In *pdsch-TimeDomainResourceAllocationList-r17*, this field is optionally present, Need R.  In pdsch-TimeDomainAllocationListDCI-1-2and *pdsch-TimeDomainAllocationListForMultiPDSCH*, this field is absent. |

<UNCHANGED PARTS OMITTED>

#### – *PUSCH-Config*

The IE *PUSCH-Config* is used to configure the UE specific PUSCH parameters applicable to a particular BWP.

*PUSCH-Config* information element

-- ASN1START

-- TAG-PUSCH-CONFIG-START

PUSCH-Config ::= SEQUENCE {

dataScramblingIdentityPUSCH INTEGER (0..1023) OPTIONAL, -- Need S

txConfig ENUMERATED {codebook, nonCodebook} OPTIONAL, -- Need S

dmrs-UplinkForPUSCH-MappingTypeA SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

dmrs-UplinkForPUSCH-MappingTypeB SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

pusch-PowerControl PUSCH-PowerControl OPTIONAL, -- Need M

frequencyHopping ENUMERATED {intraSlot, interSlot} OPTIONAL, -- Need S

frequencyHoppingOffsetLists SEQUENCE (SIZE (1..4)) OF INTEGER (1.. maxNrofPhysicalResourceBlocks-1)

OPTIONAL, -- Need M

resourceAllocation ENUMERATED { resourceAllocationType0, resourceAllocationType1, dynamicSwitch},

pusch-TimeDomainAllocationList SetupRelease { PUSCH-TimeDomainResourceAllocationList } OPTIONAL, -- Need M

pusch-AggregationFactor ENUMERATED { n2, n4, n8 } OPTIONAL, -- Need S

mcs-Table ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

mcs-TableTransformPrecoder ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

transformPrecoder ENUMERATED {enabled, disabled} OPTIONAL, -- Need S

codebookSubset ENUMERATED {fullyAndPartialAndNonCoherent, partialAndNonCoherent,nonCoherent}

OPTIONAL, -- Cond codebookBased

maxRank INTEGER (1..4) OPTIONAL, -- Cond codebookBased

rbg-Size ENUMERATED { config2} OPTIONAL, -- Need S

uci-OnPUSCH SetupRelease { UCI-OnPUSCH} OPTIONAL, -- Need M

tp-pi2BPSK ENUMERATED {enabled} OPTIONAL, -- Need S

...,

[[

minimumSchedulingOffsetK2-r16 SetupRelease { MinSchedulingOffsetK2-Values-r16 } OPTIONAL, -- Need M

ul-AccessConfigListDCI-0-1-r16 SetupRelease { UL-AccessConfigListDCI-0-1-r16 } OPTIONAL, -- Need M

-- Start of the parameters for DCI format 0\_2 introduced in V16.1.0

harq-ProcessNumberSizeDCI-0-2-r16 INTEGER (0..4) OPTIONAL, -- Need R

dmrs-SequenceInitializationDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

numberOfBitsForRV-DCI-0-2-r16 INTEGER (0..2) OPTIONAL, -- Need R

antennaPortsFieldPresenceDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2-r16 SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2-r16 SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

frequencyHoppingDCI-0-2-r16 CHOICE {

pusch-RepTypeA ENUMERATED {intraSlot, interSlot},

pusch-RepTypeB ENUMERATED {interRepetition, interSlot}

} OPTIONAL, -- Need S

frequencyHoppingOffsetListsDCI-0-2-r16 SetupRelease { FrequencyHoppingOffsetListsDCI-0-2-r16} OPTIONAL, -- Need M

codebookSubsetDCI-0-2-r16 ENUMERATED {fullyAndPartialAndNonCoherent, partialAndNonCoherent,nonCoherent}

OPTIONAL, -- Cond codebookBased

invalidSymbolPatternIndicatorDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

maxRankDCI-0-2-r16 INTEGER (1..4) OPTIONAL, -- Cond codebookBased

mcs-TableDCI-0-2-r16 ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

mcs-TableTransformPrecoderDCI-0-2-r16 ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

priorityIndicatorDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

pusch-RepTypeIndicatorDCI-0-2-r16 ENUMERATED { pusch-RepTypeA, pusch-RepTypeB} OPTIONAL, -- Need R

resourceAllocationDCI-0-2-r16 ENUMERATED { resourceAllocationType0, resourceAllocationType1, dynamicSwitch}

OPTIONAL, -- Need M

resourceAllocationType1GranularityDCI-0-2-r16 ENUMERATED { n2,n4,n8,n16 } OPTIONAL, -- Need S

uci-OnPUSCH-ListDCI-0-2-r16 SetupRelease { UCI-OnPUSCH-ListDCI-0-2-r16} OPTIONAL, -- Need M

pusch-TimeDomainAllocationListDCI-0-2-r16 SetupRelease { PUSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

-- End of the parameters for DCI format 0\_2 introduced in V16.1.0

-- Start of the parameters for DCI format 0\_1 introduced in V16.1.0

pusch-TimeDomainAllocationListDCI-0-1-r16 SetupRelease { PUSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

invalidSymbolPatternIndicatorDCI-0-1-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

priorityIndicatorDCI-0-1-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

pusch-RepTypeIndicatorDCI-0-1-r16 ENUMERATED { pusch-RepTypeA, pusch-RepTypeB} OPTIONAL, -- Need R

frequencyHoppingDCI-0-1-r16 ENUMERATED {interRepetition, interSlot} OPTIONAL, -- Cond RepTypeB

uci-OnPUSCH-ListDCI-0-1-r16 SetupRelease { UCI-OnPUSCH-ListDCI-0-1-r16 } OPTIONAL, -- Need M

-- End of the parameters for DCI format 0\_1 introduced in V16.1.0

invalidSymbolPattern-r16 InvalidSymbolPattern-r16 OPTIONAL, -- Need S

pusch-PowerControl-v1610 SetupRelease {PUSCH-PowerControl-v1610} OPTIONAL, -- Need M

ul-FullPowerTransmission-r16 ENUMERATED {fullpower, fullpowerMode1, fullpowerMode2} OPTIONAL, -- Need R

pusch-TimeDomainAllocationListForMultiPUSCH-r16 SetupRelease { PUSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

numberOfInvalidSymbolsForDL-UL-Switching-r16 INTEGER (1..4) OPTIONAL -- Cond RepTypeB2

]],

[[

ul-AccessConfigListDCI-0-2-r17 SetupRelease { UL-AccessConfigListDCI-0-2-r17 } OPTIONAL, -- Need M

betaOffsetsCrossPri0-r17 SetupRelease { BetaOffsetsCrossPriSel-r17 } OPTIONAL, -- Need M

betaOffsetsCrossPri1-r17 SetupRelease { BetaOffsetsCrossPriSel-r17 } OPTIONAL, -- Need M

betaOffsetsCrossPri0DCI-0-2-r17 SetupRelease { BetaOffsetsCrossPriSelDCI-0-2-r17 } OPTIONAL, -- Need M

betaOffsetsCrossPri1DCI-0-2-r17 SetupRelease { BetaOffsetsCrossPriSelDCI-0-2-r17 } OPTIONAL, -- Need M

mappingPattern-r17 ENUMERATED {cyclicMapping, sequentialMapping} OPTIONAL, -- Need R

secondTPCFieldDCI-0-1-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

secondTPCFieldDCI-0-2-r17 ENUMERATED {enabled} OPTIONAL, -- Need R

sequenceOffsetForRV-r17 INTEGER (0..3) OPTIONAL, -- Need R

ul-AccessConfigListDCI-0-1-r17 SetupRelease { UL-AccessConfigListDCI-0-1-r17 } OPTIONAL, -- Need M

minimumSchedulingOffsetK2-r17 SetupRelease { MinSchedulingOffsetK2-Values-r17 } OPTIONAL, -- Need M

availableSlotCounting-r17 ENUMERATED { enabled } OPTIONAL, -- Need S

dmrs-BundlingPUSCH-Config-r17 SetupRelease { DMRS-BundlingPUSCH-Config-r17 } OPTIONAL, -- Need M

harq-ProcessNumberSizeDCI-0-2-v1700 INTEGER (5) OPTIONAL, -- Need R

harq-ProcessNumberSizeDCI-0-1-r17 INTEGER (5) OPTIONAL -- Need R

]]

}

UCI-OnPUSCH ::= SEQUENCE {

betaOffsets CHOICE {

dynamic SEQUENCE (SIZE (4)) OF BetaOffsets,

semiStatic BetaOffsets

} OPTIONAL, -- Need M

scaling ENUMERATED { f0p5, f0p65, f0p8, f1 }

}

MinSchedulingOffsetK2-Values-r16 ::= SEQUENCE (SIZE (1..maxNrOfMinSchedulingOffsetValues-r16)) OF INTEGER (0..maxK2-SchedulingOffset-r16)

MinSchedulingOffsetK2-Values-r17 ::= SEQUENCE (SIZE (1..maxNrOfMinSchedulingOffsetValues-r16)) OF INTEGER (0..maxK2-SchedulingOffset-r17)

UCI-OnPUSCH-DCI-0-2-r16 ::= SEQUENCE {

betaOffsetsDCI-0-2-r16 CHOICE {

dynamicDCI-0-2-r16 CHOICE {

oneBit-r16 SEQUENCE (SIZE (2)) OF BetaOffsets,

twoBits-r16 SEQUENCE (SIZE (4)) OF BetaOffsets

},

semiStaticDCI-0-2-r16 BetaOffsets

} OPTIONAL, -- Need M

scalingDCI-0-2-r16 ENUMERATED { f0p5, f0p65, f0p8, f1 }

}

FrequencyHoppingOffsetListsDCI-0-2-r16 ::= SEQUENCE (SIZE (1..4)) OF INTEGER (1.. maxNrofPhysicalResourceBlocks-1)

UCI-OnPUSCH-ListDCI-0-2-r16 ::= SEQUENCE (SIZE (1..2)) OF UCI-OnPUSCH-DCI-0-2-r16

UCI-OnPUSCH-ListDCI-0-1-r16 ::= SEQUENCE (SIZE (1..2)) OF UCI-OnPUSCH

UL-AccessConfigListDCI-0-1-r16 ::= SEQUENCE (SIZE (1..64)) OF INTEGER (0..63)

UL-AccessConfigListDCI-0-1-r17 ::= SEQUENCE (SIZE (1..3)) OF INTEGER (0..2)

UL-AccessConfigListDCI-0-2-r17 ::= SEQUENCE (SIZE (1..64)) OF INTEGER (0..63)

BetaOffsetsCrossPriSel-r17 ::= CHOICE {

dynamic-r17 SEQUENCE (SIZE (4)) OF BetaOffsetsCrossPri-r17,

semiStatic-r17 BetaOffsetsCrossPri-r17

}

BetaOffsetsCrossPriSelDCI-0-2-r17 ::= CHOICE {

dynamicDCI-0-2-r17 CHOICE {

oneBit-r17 SEQUENCE (SIZE (2)) OF BetaOffsetsCrossPri-r17,

twoBits-r17 SEQUENCE (SIZE (4)) OF BetaOffsetsCrossPri-r17

},

semiStaticDCI-0-2-r17 BetaOffsetsCrossPri-r17

}

-- TAG-PUSCH-CONFIG-STOP

-- ASN1STOP

|  |
| --- |
| *PUSCH-Config* field descriptions |
| ***antennaPortsFieldPresenceDCI-0-2***  Configure the presence of "Antenna ports" field in DCI format 0\_2. When the field is configured, then the "Antenna ports" field is present in DCI format 0\_2. Otherwise, the field size is set to 0 for DCI format 0\_2 (See TS 38.212 [17], clause 7.3.1.1.3). If neither *dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2* nor *dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2* is configured, this field is absent. |
| ***availableSlotCounting***  Indicate whether PUSCH repetitions counted on the basis of available slots is enabled. If the field is absent, PUSCH repetitions counted on the basis of available slots is disabled. |
| ***betaOffsetsCrossPri0, betaOffsetsCrossPri1,*** ***betaOffsetsCrossPri0DCI-0-2, betaOffsetsCrossPri1DCI-0-2***  Selection between and configuration of dynamic and semi-static beta-offset for multiplexing HARQ-ACK on dynamically scheduled PUSCH with different priorities, see TS 38.213 [13], clause 9.3.  The field *betaOffsetsCrossPrio0* indicates multiplexing low priority (LP) HARQ-ACK on dynamically scheduled high priority (HP) PUSCH.  The field *betaOffsetsCrossPrio1* indicates multiplexing HP HARQ-ACK on dynamically scheduled LP PUSCH.  The field *betaOffsetsCrossPrio0DCI-0-2* indicates multiplexing LP HARQ-ACK on dynamically scheduled HP PUSCH by DCI format 0\_2.  The field *betaOffsetsCrossPrio1DCI-0-2* indicates multiplexing HP HARQ-ACK on dynamically scheduled LP PUSCH by DCI format 0\_2. |
| ***codebookSubset, codebookSubsetDCI-0-2***  Subset of PMIs addressed by TPMI, where PMIs are those supported by UEs with maximum coherence capabilities (see TS 38.214 [19], clause 6.1.1.1). The field *codebookSubset* applies to DCI format 0\_1 and the field *codebookSubsetDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.1.1). |
| ***dataScramblingIdentityPUSCH***  Identifier used to initialise data scrambling (c\_init) for PUSCH. If the field is absent, the UE applies the physical cell ID. (see TS 38.211 [16], clause 6.3.1.1). |
| ***dmrs-BundlingPUSCH-Config***  Configure the parameters for DMRS bundling for PUSCH (see TS 38.214 [19], clause 6.1.7). |
| ***dmrs-SequenceInitializationDCI-0-2***  Configure whether the field "DMRS Sequence Initialization" is present or not in DCI format 0\_2. If the field is absent, then 0 bit for the field "DMRS Sequence Initialization" in DCI format 0\_2. If the field is present, then the number of bits is determined in the same way as DCI format 0\_1 (see TS 38.212 [17], clause 7.3.1). |
| ***dmrs-UplinkForPUSCH-MappingTypeA, dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2***  DMRS configuration for PUSCH transmissions using PUSCH mapping type A (chosen dynamically via *PUSCH-TimeDomainResourceAllocation*). Only the fields *dmrs-Type*, *dmrs-AdditionalPosition* and *maxLength* may be set differently for mapping type A and B. The field *dmrs-UplinkForPUSCH-MappingTypeA* applies to DCI format 0\_1 and the field *dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2* applies to DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***dmrs-UplinkForPUSCH-MappingTypeB, dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2***  DMRS configuration for PUSCH transmissions using PUSCH mapping type B (chosen dynamically via *PUSCH-TimeDomainResourceAllocation*). Only the fields *dmrs-Type*, *dmrs-AdditionalPosition* and *maxLength* may be set differently for mapping type A and B. The field *dmrs-UplinkForPUSCH-MappingTypeB* applies to DCI format 0\_1 and the field *dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2* applies to DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***frequencyHopping***  The value *intraSlot* enables 'Intra-slot frequency hopping' and the value *interSlot* enables 'Inter-slot frequency hopping'. If the field is absent, frequency hopping is not configured for 'pusch-RepTypeA' (see TS 38.214 [19], clause 6.3). The field *frequencyHopping* applies to DCI format 0\_0 and 0\_1 for 'pusch-RepTypeA'. |
| ***frequencyHoppingDCI-0-1***  Indicates the frequency hopping scheme for DCI format 0\_1 when *pusch-RepTypeIndicatorDCI-0-1* is set to 'pusch-RepTypeB', The value *interRepetition* enables 'Inter-repetition frequency hopping', and the value *interSlot* enables 'Inter-slot frequency hopping'. If the field is absent, frequency hopping is not configured for DCI format 0\_1 (see TS 38.214 [19], clause 6.1). |
| ***frequencyHoppingDCI-0-2***  Indicate the frequency hopping scheme for DCI format 0\_2. The value *intraSlot* enables 'intra-slot frequency hopping', and the value *interRepetition* enables 'Inter-repetition frequency hopping', and the value *interSlot* enables 'Inter-slot frequency hopping'. When *pusch-RepTypeIndicatorDCI-0-2* is set to '*pusch-RepTypeA*', the frequency hopping scheme can be chosen between 'intra-slot frequency hopping and 'inter-slot frequency hopping' if enabled. When *pusch-RepTypeIndicatorDCI-0-2* is set to '*pusch-RepTypeB'*, the frequency hopping scheme can be chosen between 'inter-repetition frequency hopping' and 'inter-slot frequency hopping' if enabled. If the field is absent, frequency hopping is not configured for DCI format 0\_2 for 'pusch-RepTypeB' (see TS 38.214 [19], clause 6.3). |
| ***frequencyHoppingOffsetLists, frequencyHoppingOffsetListsDCI-0-2***  Set of frequency hopping offsets used when frequency hopping is enabled for granted transmission (not msg3) and type 2 configured grant activation (see TS 38.214 [19], clause 6.3). The field *frequencyHoppingOffsetLists* applies to DCI format 0\_0 and DCI format 0\_1 and the field *frequencyHoppingOffsetListsDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.3). |
| ***harq-ProcessNumberSizeDCI-0-2***  Configure the number of bits for the field "HARQ process number" in DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***invalidSymbolPattern***  Indicates one pattern for invalid symbols for PUSCH transmission repetition type B applicable to both DCI format 0\_1 and 0\_2. If *InvalidSymbolPattern* is not configured, semi-static flexible symbols are used for PUSCH. Segmentation occurs only around semi-static DL symbols (see TS 38.214 [19] clause 6.1). |
| ***invalidSymbolPatternIndicatorDCI-0-1, invalidSymbolPatternIndicatorDCI-0-2***  Indicates the presence of an additional bit in the DCI format 0\_1/0\_2. If *invalidSymbolPattern* is absent, then both *invalidSymbolPatternIndicatorDCI-0-1* and *invalidSymbolPatternIndicatorDCI-0-2* are absent. The field *invalidSymbolPatternIndicatorDCI-0-1* applies to the DCI format 0\_1 and the field *invalidSymbolPatternIndicatorDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19] clause 6.1). If the field is absent, the UE behaviour is specified in TS 38.214 [19], clause 6.1.2.1. |
| ***mappingPattern***  Indicates whether the UE should follow Cyclical mapping pattern or Sequential mapping pattern for when two SRS resource sets are configured for PUSCH transmission and the PUSCH transmission occasions are associated with both SRS resource sets. |
| ***maxRank, maxRankDCI-0-2***  Subset of PMIs addressed by TRIs from 1 to ULmaxRank (see TS 38.214 [19], clause 6.1.1.1). The field *maxRank* applies to DCI format 0\_1 and the field *maxRankDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.1.1). |
| ***mcs-Table, mcs-TableFormat0-2***  Indicates which MCS table the UE shall use for PUSCH without transform precoder (see TS 38.214 [19], clause 6.1.4.1). If the field is absent the UE applies the value 64QAM. The field *mcs-Table* applies to DCI format 0\_0 and DCI format 0\_1 and the field *mcs-TableDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.4.1). |
| ***mcs-TableTransformPrecoder, mcs-TableTransformPrecoderDCI-0-2***  Indicates which MCS table the UE shall use for PUSCH with transform precoding (see TS 38.214 [19], clause 6.1.4.1) If the field is absent the UE applies the value 64QAM. The field *mcs-TableTransformPrecoder* applies to DCI format 0\_0 and DCI format 0\_1 and the field *mcs-TableTransformPrecoderDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.4.1). |
| ***minimumSchedulingOffsetK2***  List of minimum K2 values. Minimum K2 parameter denotes minimum applicable value(s) for the *Time domain resource assignment* table for PUSCH (see TS 38.214 [19], clause 6.1.2.1). |
| ***numberOfBitsRV-DCI-0-2***  Configures the number of bits for "Redundancy version" in the DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.214 [19], clause 6.1.2.1). |
| ***numberOfInvalidSymbolsForDL-UL-Switching***  Indicates the number of symbols after the last semi-static DL symbol that are invalid symbols for PUSCH repetition Type B. If it is absent, no symbol is explicitly defined for DL-to-UL switching (see TS 38.214 [19], clause 6.1). |
| ***priorityIndicatorDCI-0-1, priorityIndicatorDCI-0-2***  Configures the presence of "priority indicator" in DCI format 0\_1/0\_2. When the field is absent in the IE, then the UE shall apply 0 bit for "Priority indicator" in DCI format 0\_1/0\_2. The field *priorityIndicatorDCI-0-1* applies to DCI format 0\_1 and the field *priorityIndicatorDCI-0-2* applies to DCI format 0\_2 (see TS 38.212 [17] clause 7.3.1 and TS 38.213 [13] clause 9). |
| ***pusch-AggregationFactor***  Number of repetitions for data (see TS 38.214 [19], clause 6.1.2.1). If the field is absent the UE applies the value 1. |
| ***pusch-RepTypeIndicatorDCI-0-1, pusch-RepTypeIndicatorDCI-0-2***  Indicates whether UE follows the behavior for "PUSCH repetition type A" or the behavior for "PUSCH repetition type B" for the PUSCH scheduled by DCI format 0\_1/0\_2 and for Type 2 CG associated with the activating DCI format 0\_1/0\_2.The value *pusch-RepTypeA* enables the 'PUSCH repetition type A' and the value *pusch-RepTypeB* enables the 'PUSCH repetition type B'. The field *pusch-RepTypeIndicatorDCI-0-1* applies to DCI format 0\_1 and the field *pusch-RepTypeIndicatorDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.2.1). |
| ***pusch-TimeDomainAllocationList***  List of time domain allocations for timing of UL assignment to UL data (see TS 38.214 [19], table 6.1.2.1.1-1). The field *pusch-TimeDomainAllocationList* applies to DCI formats 0\_0 or DCI format 0\_1 when the field *pusch-TimeDomainAllocationListDCI-0-1* is not configured (see TS 38.214 [19], table 6.1.2.1.1-1 and table 6.1.2.1.1-1A). The network does not configure the *pusch-TimeDomainAllocationList* (without suffix) simultaneously with the *pusch-TimeDomainAllocationListDCI-0-2-r16* or *pusch-TimeDomainAllocationListDCI-0-1-r16* or *pusch-TimeDomainAllocationListForMultiPUSCH-r16*. |
| ***pusch-TimeDomainAllocationListDCI-0-1***  Configuration of the time domain resource allocation (TDRA) table for DCI format 0\_1 (see TS 38.214 [19], clause 6.1, table 6.1.2.1.1-1A). |
| ***pusch-TimeDomainAllocationListDCI-0-2***  Configuration of the time domain resource allocation (TDRA) table for DCI format 0\_2 (see TS 38.214 [19], clause 6.1.2, table 6.1.2.1.1-1B). |
| ***pusch-TimeDomainAllocationListForMultiPUSCH***  Configuration of the time domain resource allocation (TDRA) table for multiple PUSCH (see TS 38.214 [19], clause 6.1.2). The network configures at most 16 rows in this TDRA table in *PUSCH-TimeDomainResourceAllocationList-r16* configured by this field. This field is not configured simultaneously with *pusch-AggregationFactor*. |
| ***rbg-Size***  Selection between configuration 1 and configuration 2 for RBG size for PUSCH. The UE does not apply this field if *resourceAllocation* is set to *resourceAllocationType1*. Otherwise, the UE applies the value *config1* when the field is absent (see TS 38.214 [19], clause 6.1.2.2.1). |
| ***resourceAllocation, resourceAllocationDCI-0-2***  Configuration of resource allocation type 0 and resource allocation type 1 for non-fallback DCI (see TS 38.214 [19], clause 6.1.2). The field *resourceAllocation* applies to DCI format 0\_1 and the field *resourceAllocationDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.2). |
| ***resourceAllocationType1GranularityDCI-0-2***  Configures the scheduling granularity applicable for both the starting point and length indication for resource allocation type 1 in DCI format 0\_2. If this field is absent, the granularity is 1 PRB (see TS 38.214 [19], clause 6.1.2.2.2). |
| ***secondTPCFieldDCI-0-1, secondTPCFieldDCI-0-2***  A second TPC field can be configured via RRC for DCI-0-1 and DCI-0-2. Each TPC field is for each closed-loop index value respectively (i.e., 1st /2nd TPC fields correspond to "closedLoopIndex" value = 0 and 1, |
| ***sequenceOffsetForRV***  Configures the RV offset for the starting RV for the first repetition (first actual repetition in PUSCH repetition Type B) towards the second 'SRS resource set' for PUSCH. |
| ***tp-pi2BPSK***  Enables pi/2-BPSK modulation with transform precoding if the field is present and disables it otherwise. |
| ***transformPrecoder***  The UE specific selection of transformer precoder for PUSCH (see TS 38.214 [19], clause 6.1.3). When the field is absent the UE applies the value of the field *msg3-transformPrecoder*. |
| ***txConfig***  Whether UE uses codebook based or non-codebook based transmission (see TS 38.214 [19], clause 6.1.1). If the field is absent, the UE transmits PUSCH on one antenna port, see TS 38.214 [19], clause 6.1.1. |
| ***uci-OnPUSCH-ListDCI-0-1, uci-OnPUSCH-ListDCI-0-2***  Configuration for up to 2 HARQ-ACK codebooks specific to DCI format 0\_1/0\_2. The field uci-OnPUSCH-ListDCI-0-1 applies to DCI format 0\_1 and the field uci-OnPUSCH-ListDCI-0-2 applies to DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13] clause 9.3). |
| ***ul-AccessConfigListDCI-0-1, ul-AccessConfigListDCI-0-2***  List of the combinations of cyclic prefix extension, channel access priority class (CAPC), and UL channel access type (see TS 38.212 [17], Table 7.3.1.1.2-35) applicable for DCI format 0\_1 and DCI format 0\_2, respectively.The field *ul-AccessConfigListDCI-0-1-r17* only contains a list of UL channel access types and is only applicable for FR2-2 (see TS 38.212 [17], Table 7.3.1.1.2-35A). |
| ***ul-FullPowerTransmission***  Configures the UE with UL full power transmission mode as specified in TS 38.213. |

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| *UCI-OnPUSCH* field descriptions |
| ***betaOffsets***  Selection between and configuration of dynamic and semi-static beta-offset for DCI formats other than DCI format 0\_2. If the field is not configured, the UE applies the value 'semiStatic' (see TS 38.213 [13], clause 9.3). |
| ***scaling***  Indicates a scaling factor to limit the number of resource elements assigned to UCI on PUSCH for DCI formats other than DCI format 0\_2. Value *f0p5* corresponds to 0.5, value *f0p65* corresponds to 0.65, and so on. The value configured herein is applicable for PUSCH with configured grant (see TS 38.212 [17], clause 6.3). |

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| *UCI-OnPUSCH-DCI-0-2* field descriptions |
| ***betaOffsetsDCI-0-2***  Configuration of beta-offset for DCI format 0\_2. If semiStaticDCI-0-2 is chosen, the UE shall apply the value of 0 bit for the field of beta offset indicator in DCI format 0\_2. If dynamicDCI-0-2 is chosen, the UE shall apply the value of 1 bit or 2 bits for the field of beta offset indicator in DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13] clause 9.3). |
| ***dynamicDCI-0-2***  Indicates the UE applies the value 'dynamic' for DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13], clause 9.3). |
| ***semiStaticDCI-0-2***  Indicates the UE applies the value 'semiStatic' for DCI format 0\_2. (see TS 38.212 [17], clause 7.3.1 and see TS 38.213 [13], clause 9.3). |
| ***scalingDCI-0-2***  Indicates a scaling factor to limit the number of resource elements assigned to UCI on PUSCH for DCI format 0\_2. Value f0p5 corresponds to 0.5, value *f0p65* corresponds to 0.65, and so on (see TS 38.212 [17], clause 6.3). |

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| Conditional Presence | Explanation |
| *codebookBased* | The field is mandatory present if *txConfig* is set to codebook and absent otherwise. |
| *RepTypeB* | The field is optionally present, Need S, if *pusch-RepTypeIndicatorDCI-0-1* is set to pusch-RepTypeB. It is absent otherwise. |
| *RepTypeB2* | The field is optionally present, Need S, if *pusch-RepTypeIndicatorDCI-0-1* or *pusch-RepTypeIndicatorDCI-0-2* is set to pusch-RepTypeB. It is absent otherwise. |

<UNCHANGED PARTS OMITTED>

#### – *PUSCH-TimeDomainResourceAllocationList*

The IE *PUSCH-TimeDomainResourceAllocation* is used to configure a time domain relation between PDCCH and PUSCH. *PUSCH-TimeDomainResourceAllocationList* contains one or more of such *PUSCH-TimeDomainResourceAllocations*. The network indicates in the UL grant which of the configured time domain allocations the UE shall apply for that UL grant. The UE determines the bit width of the DCI field based on the number of entries in the *PUSCH-TimeDomainResourceAllocationList*. Value 0 in the DCI field refers to the first element in this list, value 1 in the DCI field refers to the second element in this list, and so on.

*PUSCH-TimeDomainResourceAllocation* information element

-- ASN1START

-- TAG-PUSCH-TIMEDOMAINRESOURCEALLOCATIONLIST-START

PUSCH-TimeDomainResourceAllocationList ::= SEQUENCE (SIZE(1..maxNrofUL-Allocations)) OF PUSCH-TimeDomainResourceAllocation

PUSCH-TimeDomainResourceAllocation ::= SEQUENCE {

k2 INTEGER(0..32) OPTIONAL, -- Need S

mappingType ENUMERATED {typeA, typeB},

startSymbolAndLength INTEGER (0..127)

}

PUSCH-TimeDomainResourceAllocationList-r16 ::= SEQUENCE (SIZE(1..maxNrofUL-Allocations-r16)) OF PUSCH-TimeDomainResourceAllocation-r16

PUSCH-TimeDomainResourceAllocation-r16 ::= SEQUENCE {

k2-r16 INTEGER(0..32) OPTIONAL, -- Need S

puschAllocationList-r16 SEQUENCE (SIZE(1..maxNrofMultiplePUSCHs-r16)) OF PUSCH-Allocation-r16,

...

}

PUSCH-Allocation-r16 ::= SEQUENCE {

mappingType-r16 ENUMERATED {typeA, typeB} OPTIONAL, -- Cond NotFormat01-02-Or-TypeA

startSymbolAndLength-r16 INTEGER (0..127) OPTIONAL, -- Cond NotFormat01-02-Or-TypeA

startSymbol-r16 INTEGER (0..13) OPTIONAL, -- Cond RepTypeB

length-r16 INTEGER (1..14) OPTIONAL, -- Cond RepTypeB

numberOfRepetitions-r16 ENUMERATED {n1, n2, n3, n4, n7, n8, n12, n16} OPTIONAL, -- Cond Format01-02

...,

[[

numberOfRepetitionsExt-r17 ENUMERATED {n1, n2, n3, n4, n7, n8, n12, n16, n20, n24, n28, n32, spare4, spare3, spare2,

spare1} OPTIONAL, -- Cond Format01-02-For-TypeA

numberOfSlots-TBoMS-r17 ENUMERATED {n1, n2, n4, n8, spare4, spare3, spare2, spare1} OPTIONAL, -- Need M

extendedK2-r17 INTEGER (0..128) OPTIONAL -- Cond MultiPUSCH

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}

-- TAG-PUSCH-TIMEDOMAINRESOURCEALLOCATIONLIST-STOP

-- ASN1STOP

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| *PUSCH-TimeDomainResourceAllocationList* field descriptions |
| ***k2***  Corresponds to L1 parameter 'K2' (see TS 38.214 [19], clause 6.1.2.1). When the field is absent the UE applies the value 1 when PUSCH SCS is 15/30 kHz; the value 2 when PUSCH SCS is 60 kHz, and the value 3 when PUSCH SCS is 120 kHz. |
| ***extendedK2***  Corresponds to L1 parameter 'K2' (see TS 38.214 [19], clause 6.1.2.1) configurable per PUSCH allocation. Only values {0..32} are applicable for PUSCH SCS of 120 kHz.  When the field is absent for the first PUSCH if multiple PUSCH are configured per PDCCH, or when the field is absent and only one PUSCH is configured per PDCCH, the UE applies the value 1 when PUSCH SCS is 15/30 kHz; the value 2 when PUSCH SCS is 60 kHz, the value 3 when PUSCH SCS is 120 kHz, the value 11 when PUSCH SCS is 480 kHz, and the value 21 when PUSCH SCS is 960 kHz. |
| ***length***  Indicates the length allocated for PUSCH for DCI format 0\_1/0\_2 (see TS 38.214 [19], clause 6.1.2.1). |
| ***mappingType***  Mapping type (see TS 38.214 [19], clause 6.1.2.1). |
| ***numberOfRepetitions***  Number of repetitions for DCI format 0\_1/0\_2 (see TS 38.214 [19], clause 6.1.2.1). When *numberOfSlotsTBoMS-r17* is set to 2, 4 or 8 (i.e. TB processing over multi-slot (TBoMS) PUSCH is enabled), it indicates the number of repetitions of a single TBoMS. |
| ***numberOfRepetitionsExt***  Number of repetitions for DCI format 0\_1/0\_2 if *pusch-RepTypeIndicatorDCI-0-1*/*pusch-RepTypeIndicatorDCI-0-2* is set to pusch-RepTypeA (see TS 38.214 [19], clause 6.1.2.1). If this field is present, the field *numberOfRepeitions-r16* is ignored for PUSCH repetition Type A. |
| ***numberOfSlots-TBoMS***  Number of slots allocated for TB processing over multi-slot PUSCH for DCI format 0\_1/0\_2 (see TS 38.214 [19], clause 6.1.2.1). |
| ***puschAllocationList***  The field *puschAllocationList-r16* indicates one or multiple PUSCH continuous in time domain which share a common k2 (see TS 38.214 [19], clause 6.1.2.1). The field *pusch-AllocationList-r17* configures one or multiple PUSCH that may be in consecutive or non-consecutive slots (see TS 38.214 [19], clause 6.1.2.1). The *puschAllocationList-r16* only has one element in *pusch-TimeDomainAllocationListDCI-0-1-r16* and in *pusch-TimeDomainAllocationListDCI-0-2-r16*. The *puschAllocationList-r17* only has one element in *pusch-TimeDomainAllocationListDCI-0-1-r17* and in *pusch-TimeDomainAllocationListDCI-0-2-r17*. |
| ***startSymbol***  Indicates the index of start symbol for PUSCH for DCI format 0\_1/0\_2 (see TS 38.214 [19], clause 6.1.2.1). |
| ***startSymbolAndLength***  An index giving valid combinations of start symbol and length (jointly encoded) as start and length indicator (SLIV). The network configures the field so that the allocation does not cross the slot boundary. (see TS 38.214 [19], clause 6.1.2.1). |

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| Conditional Presence | Explanation |
| *Format01-02* | In *pusch-TimeDomainAllocationListForMultiPUSCH-r16* and *pusch-TimeDomainAllocationListForMultiPUSCH-r17*, the field is absent.  In *pusch-TimeDomainAllocationListDCI-0-1* and in *pusch-TimeDomainAllocationListDCI-0-2*, the field is mandatory present. |
| *Format01-02-For-TypeA* | In *pusch-TimeDomainAllocationListForMultiPUSCH-r16*, the field is absent.  In *pusch-TimeDomainAllocationListDCI-0-1*, the field is optionally present if *pusch-RepTypeIndicatorDCI-0-1* is set to pusch-RepTypeA, Need R. It is absent otherwise, Need R.  In *pusch-TimeDomainAllocationListDCI-0-2*, the field is optionally present if *pusch-RepTypeIndicatorDCI-0-2* is set to pusch-RepTypeA, Need R. It is absent otherwise, Need R. |
| *NotFormat01-02-Or-TypeA* | In *pusch-TimeDomainAllocationListForMultiPUSCH-r16 and pusch-TimeDomainAllocationListForMultiPUSCH-r17*, the field is mandatory present.  In *pusch-TimeDomainAllocationListDCI-0-1*, the field is optionally present if *pusch-RepTypeIndicatorDCI-0-1* is set to pusch-RepTypeA, Need R. It is absent otherwise, Need R.  In *pusch-TimeDomainAllocationListDCI-0-2*, the field is optionally present if *pusch-RepTypeIndicatorDCI-0-2* is set to pusch-RepTypeA, Need R. It is absent otherwise, Need R. |
| *RepTypeB* | In *pusch-TimeDomainAllocationListForMultiPUSCH-r16 and pusch-TimeDomainAllocationListForMultiPUSCH-r17*, the field is absent.  In *pusch-TimeDomainAllocationListDCI-0-1*, the field is optionally present if *pusch-RepTypeIndicatorDCI-0-1* is set to pusch-RepTypeB, Need R. It is absent otherwise, Need R.  In *pusch-TimeDomainAllocationListDCI-0-2*, the field is optionally present if *pusch-RepTypeIndicatorDCI-0-2* is set to pusch-RepTypeB, Need R. It is absent otherwise, Need R. |
| *MultiPUSCH* | In case size of *puschAllocationList* is higher than 1, the field extendedK2(n) corresponding to k2 of the n-th PUSCH, n>1, is mandatory present. Otherwise, it is optionally present, Need S. |