**3GPP TSG-RAN WG2 Meeting #113-eR2-210xxxx**

**eMeeting, 25th Jan – 5th Feb, 2021**

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| *CR-Form-v12.0* |
| **CHANGE REQUEST** |
|  |
|  | **38.306** | **CR** | **NNN** | **rev** | **-** | **Current version:** | **16.3.0** |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:***  | Capability for dormant BWP switching of multiple SCells |
|  |  |
| ***Source to WG:*** | MediaTek Inc. |
| ***Source to TSG:*** | R2 |
|  |  |
| ***Work item code:*** | LTE\_NR\_DC\_CA\_enh-Core |  | ***Date:*** | 2021/01/25 |
|  |  |  |  |  |
| ***Category:*** | F |  | ***Release:*** | Rel-16 |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)Rel-12 (Release 12)Rel-13 (Release 13)Rel-14 (Release 14)Rel-15 (Release 15)Rel-16 (Release 16)* |
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| ***Reason for change:*** | According to R4 feature table 6-3 in feature list R4-2103479, it is agreed to have new capability for dormant BWP switching of multiple SCells. The CR proposes to add the corresponding ASN.1 and field description. |
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| ***Summary of change:*** | In 4.2.7, add field description for *bwp-SwitchingMultiDormancyCCs-r16*.**Impact analysis:**Impacted 5G architecture options: Standalone, EN-DC, NGEN-DC, NE-DC, NR-DCImpacted functionality: Dormant BWP switching of multiple SCellsInter-operability:If the network is implemented according to this CR while the UE is not or vice versa, there may be additional unclear BWP switching delay if network trigger dormant BWP switching on multiple SCells simultaneously. |
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| ***Consequences if not approved:*** | There may be additional unclear BWP switching delay if network trigger dormant BWP switching on multiple SCells simultaneously. |
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| ***Clauses affected:*** | 4.2.7 |
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|  | **Y** | **N** |  |  |
| ***Other specs*** | **X** |  |  Other core specifications  | TS 38.331 CR xxxx  |
| ***affected:*** |  | **x** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **x** |  O&M Specifications | TS/TR ... CR ...  |
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| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

1st change

4.2.7.10 *Phy-Parameters*

| **Definitions for parameters** | **Per** | **M** | **FDD-TDD****DIFF** | **FR1-FR2****DIFF** |
| --- | --- | --- | --- | --- |
| <Skip> |  |  |  |  |
| ***bwp-SwitchingDelay***Defines whether the UE supports DCI and timer based active BWP switching delay type1 or type2 specified in clause 8.6.2 of TS 38.133 [5]. It is mandatory to report type 1 or type 2. This capability is not applicable to IAB-MT. | UE | Yes | No | No |
| ***bwp-SwitchingMultiCCs-r16***Indicates whether the UE supports incremental delay for DCI and timer based active BWP switching on multiple CCs simultaneously as specified in TS 38.133 [5]. The capability signalling comprises of the following:- *type1-r16* indicates the delay value for type 1 BWP switching delay for type1 and has values of {100us, 200us}- *type2-r16* indicates the delay value for type 2 BWP switching delay and has values of {200us, 400us, 800us, 1000us}UE indicates support of this feature indicates support of *bwp-SwitchingDelay*, *bwp-SameNumerology* and *bwp-DiffNumerology*. | UE | No | No | No |
| ***bwp-SwitchingMultiDormancyCCs-r16***Indicates whether the UE supports incremental delay for BWP switch processing on additional SCells in DCI based simultaneous dormant BWP switching on multiple SCells as specified in TS 38.133 [5]. The capability signalling comprises of the following:- *type1-r16* indicates the delay value for type 1 BWP switching delay for type1 and has values of {100us, 200us}- *type2-r16* indicates the delay value for type 2 BWP switching delay and has values of {200us, 400us, 800us, 1000us}UE indicates support of this feature indicates support of *scellDormancyWithinActiveTime-r16* or *scellDormancyOutsideActiveTime-r16.* | UE | No | No | No |