
Agenda item:

Source: Philips

Title: TFCI in the case of invalid transport blocks

Document for: Decision

Introduction

This issue has been discussed previously in RAN WG1 [1,2]

The current document considers the contents of the TFCI field in the case that Layer 1 receives an invalid set of transport blocks, which is not currently mentioned in the Layer 1 specifications, and is described in [3], but only for the downlink.

The response from RAN WG3 to the LS [4] indicates that the current text should remain in [3], but it also seems appropriate that this is at least mentioned in the Layer 1 specifications. The proposed CR's go slightly further and add some further clarification from a Layer 1 perspective.

As an example, it might be the case that no transport blocks have been provided to Layer 1, but the corresponding Transport Format Combination has not been defined as one of the valid combinations for that radio link. This is likely to occur during the initial phases of DPCH synchronization, where DPCCH may be present but nothing is sent on DPDCH. In such a case the following solutions are proposed:-

?? In the uplink the TFCI codeword should be generated by setting the input to the TFCI coder to all "1"s.

?? In the downlink DTX should be applied to the TFCI field (as in [3])

The changes in CR 25.211-083 define the required behaviour for an invalid set of transport blocks and add a reference to [3]

The changes in CR 25.212-095 define how the TFCI codeword should be generated in this special case

These changes also cover the contents of the TFCI field in the DCH power control preamble.

The related question of what should be transmitted on the DPDCH in the case of invalid transport blocks is not considered here, but the case of the uplink could be the subject of further discussion.

References

[1] R1-00-R1-00-1046 Uplink transmission with invalid set of transport blocks, Source Philips

[2] R1-00-1146 LS on TFCI in the case of an invalid set of transport blocks and during DPCH synchronisation

[3] TS25.427 Ver3.3.0

[4] R1-00-1178 Response to Liaison Statement on "TFCI in the case of invalid set of transport blocks and during DPCH synchronisation", Source RAN3

CHANGE REQUEST		Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.	
25.211	CR	083	Current Version: 3.4.0 draft
GSM (AA.BB) or 3G (AA.BBB) specification number ?		? CR number as allocated by MCC support team	
For submission to: RAN #9 <small>list expected approval meeting #here ?</small>	for approval <input checked="" type="checkbox"/> for information <input type="checkbox"/>	strategic <input type="checkbox"/> non-strategic <input type="checkbox"/>	(for SMG use only)

Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: <ftp://ftp.3gpp.org/Information/CR-Form-v2.doc>

Proposed change affects: (U)SIM ME UTRAN / Radio Core Network
(at least one should be marked with an X)

Source: Philips **Date:** 2000-09-15

Subject: TFCI in the case of invalid transport blocks

Work item: _____

Category:	F Correction <input checked="" type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input type="checkbox"/> D Editorial modification <input type="checkbox"/>	Release:	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
------------------	--	-----------------	--

(only one category shall be marked with an X)

Reason for change: The contents of the TFCI field is not specified in the case of an invalid set of transport blocks

Clauses affected: 2, 5.2.1, 5.3.2

Other specs affected:	Other 3G core specifications <input checked="" type="checkbox"/> ? Other GSM core specifications <input type="checkbox"/> ? MS test specifications <input type="checkbox"/> ? BSS test specifications <input type="checkbox"/> ? O&M specifications <input type="checkbox"/> ?	List of CRs: CR 25.212-95 List of CRs: List of CRs: List of CRs: List of CRs:
------------------------------	--	---

Other comments: _____



<----- double-click here for help and instructions on how to create a CR.

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

?? References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.

?? For a specific reference, subsequent revisions do not apply.

?? For a non-specific reference, the latest version applies.

- [1] 3G TS 25.201: "Physical layer - general description".
- [2] 3G TS 25.211: "Physical channels and mapping of transport channels onto physical channels (FDD)".
- [3] 3G TS 25.212: "Multiplexing and channel coding (FDD)".
- [4] 3G TS 25.213: "Spreading and modulation (FDD)".
- [5] 3G TS 25.214: "Physical layer procedures (FDD)".
- [6] 3G TS 25.221: "Transport channels and physical channels (TDD)".
- [7] 3G TS 25.222: "Multiplexing and channel coding (TDD)".
- [8] 3G TS 25.223: "Spreading and modulation (TDD)".
- [9] 3G TS 25.224: "Physical layer procedures (TDD)".
- [10] 3G TS 25.215: "Physical layer - Measurements (FDD)".
- [11] 3G TS 25.301: "Radio Interface Protocol Architecture".
- [12] 3G TS 25.302: "Services Provided by the Physical Layer".
- [13] 3G TS 25.401: "UTRAN Overall Description".
- [14] 3G TS 25.133: "Requirements for Support of Radio Resource Management (FDD)".
- [15] 3G TS 25.427: "UTRAN Overall Description :UTRA Iub/Iur Interface User Plane Protocol for DCH data streams ".

5.2.1 Dedicated uplink physical channels

There are two types of uplink dedicated physical channels, the uplink Dedicated Physical Data Channel (uplink DPDCH) and the uplink Dedicated Physical Control Channel (uplink DPCCH).

The DPDCH and the DPCCH are I/Q code multiplexed within each radio frame (see [4]).

The uplink DPDCH is used to carry the DCH transport channel. There may be zero, one, or several uplink DPDCHs on each radio link.

The uplink DPCCH is used to carry control information generated at Layer 1. The Layer 1 control information consists of known pilot bits to support channel estimation for coherent detection, transmit power-control (TPC) commands, feedback information (FBI), and an optional transport-format combination indicator (TFCI). The transport-format combination indicator informs the receiver about the instantaneous transport format combination of the transport channels mapped to the simultaneously transmitted uplink DPDCH radio frame. There is one and only one uplink DPCCH on each radio link.

Figure 1 shows the frame structure of the uplink dedicated physical channels. Each radio frame of length 10 ms is split into 15 slots, each of length $T_{slot} = 2560$ chips, corresponding to one power-control period.

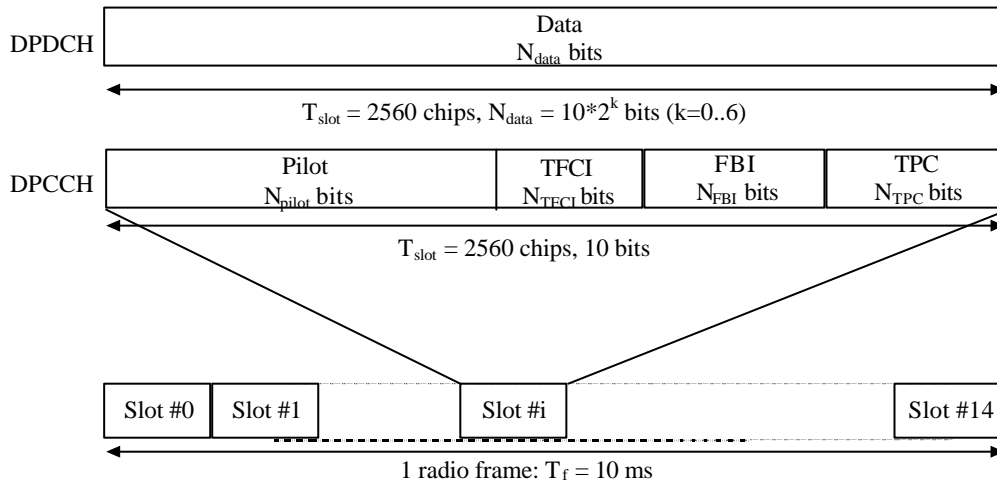


Figure 1: Frame structure for uplink DPDCH/DPCCH

The parameter k in figure 1 determines the number of bits per uplink DPDCH slot. It is related to the spreading factor SF of the DPDCH as $SF = 256/2^k$. The DPDCH spreading factor may range from 256 down to 4. The spreading factor of the uplink DPCCH is always equal to 256, i.e. there are 10 bits per uplink DPCCH slot.

The exact number of bits of the uplink DPDCH and the different uplink DPCCH fields (N_{pilot} , N_{TFCI} , N_{FBI} , and N_{TPC}) is given by table 1 and table 2. What slot format to use is configured by higher layers and can also be reconfigured by higher layers.

The channel bit and symbol rates given in table 1 and table 2 are the rates immediately before spreading. The pilot patterns are given in table 3 and table 4, the TPC bit pattern is given in table 5.

The FBI bits are used to support techniques requiring feedback from the UE to the UTRAN Access Point, including closed loop mode transmit diversity and site selection diversity transmission (SSDT). The structure of the FBI field is shown in figure 2 and described below.

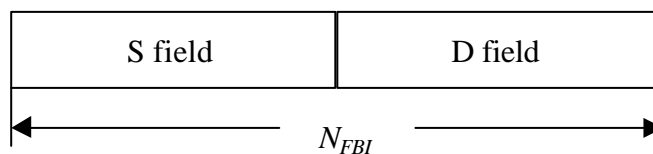


Figure 2: Details of FBI field

The S field is used for SSST signalling, while the D field is used for closed loop mode transmit diversity signalling. The S field consists of 0, 1 or 2 bits. The D field consists of 0 or 1 bit. The total FBI field size N_{FBI} is given by table 2. If total FBI field is not filled with S field or D field, FBI field shall be filled with "1". When N_{FBI} is 2bits, S field is 0bit and D field is 1bit, left side field shall be filled with "1" and right side field shall be D field. Simultaneous use of SSST power control and closed loop mode transmit diversity requires that the S field consists of 1 bit. The use of the FBI fields is described in detail in [5].

Table 1: DPDCH fields

Slot Format #i	Channel Bit Rate (kbps)	Channel Symbol Rate (ksps)	SF	Bits/Frame	Bits/Slot	N_{data}
0	15	15	256	150	10	10
1	30	30	128	300	20	20
2	60	60	64	600	40	40
3	120	120	32	1200	80	80
4	240	240	16	2400	160	160
5	480	480	8	4800	320	320
6	960	960	4	9600	640	640

There are two types of uplink dedicated physical channels; those that include TFCI (e.g. for several simultaneous services) and those that do not include TFCI (e.g. for fixed-rate services). These types are reflected by the duplicated rows of table 2. It is the UTRAN that determines if a TFCI should be transmitted and it is mandatory for all UEs to support the use of TFCI in the uplink. The mapping of TFCI bits onto slots is described in [3].

In compressed mode, DPCCH slot formats with TFCI fields are changed. There are two possible compressed slot formats for each normal slot format. They are labelled A and B and the selection between them is dependent on the number of slots that are transmitted in each frame in compressed mode.

Table 2: DPCCH fields

Slot Form at #i	Channel Bit Rate (kbps)	Channel Symbol Rate (ksps)	SF	Bits/Frame	Bits/Slot	N_{pilot}	N_{TPC}	N_{TFCI}	N_{FBI}	Transmitted slots per radio frame
0	15	15	256	150	10	6	2	2	0	15
0A	15	15	256	150	10	5	2	3	0	10-14
0B	15	15	256	150	10	4	2	4	0	8-9
1	15	15	256	150	10	8	2	0	0	8-15
2	15	15	256	150	10	5	2	2	1	15
2A	15	15	256	150	10	4	2	3	1	10-14
2B	15	15	256	150	10	3	2	4	1	8-9
3	15	15	256	150	10	7	2	0	1	8-15
4	15	15	256	150	10	6	2	0	2	8-15
5	15	15	256	150	10	5	1	2	2	15
5A	15	15	256	150	10	4	1	3	2	10-14
5B	15	15	256	150	10	3	1	4	2	8-9

The pilot bit patterns are described in table 3 and table 4. The shadowed column part of pilot bit pattern is defined as FSW and FSWs can be used to confirm frame synchronization. (The value of the pilot bit pattern other than FSWs shall be "1".)

Table 3: Pilot bit patterns for uplink DPCCH with $N_{\text{pilot}} = 3, 4, 5$ and 6

Bit #	$N_{\text{pilot}} = 3$			$N_{\text{pilot}} = 4$				$N_{\text{pilot}} = 5$					$N_{\text{pilot}} = 6$					
	0	1	2	0	1	2	3	0	1	2	3	4	0	1	2	3	4	5
Slot #0	1	1	1	1	1	1	1	1	1	1	1	0	1	1	1	1	1	0
1	0	0	1	1	0	0	1	0	0	1	1	0	1	0	0	1	1	0
2	0	1	1	1	0	1	1	0	1	1	0	1	1	0	1	1	0	1
3	0	0	1	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0
4	1	0	1	1	1	0	1	1	0	1	0	1	1	1	0	1	0	1
5	1	1	1	1	1	1	1	1	1	1	1	0	1	1	1	1	1	0
6	1	1	1	1	1	1	1	1	1	1	0	0	1	1	1	1	0	0
7	1	0	1	1	1	0	1	1	0	1	0	0	1	1	0	1	0	0
8	0	1	1	1	0	1	1	0	1	1	1	0	1	0	1	1	1	0
9	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
10	0	1	1	1	0	1	1	0	1	1	0	1	1	0	1	1	0	1
11	1	0	1	1	1	0	1	1	0	1	1	1	1	1	0	1	1	1
12	1	0	1	1	1	0	1	1	0	1	0	0	1	1	0	1	0	0
13	0	0	1	1	0	0	1	0	0	1	1	1	1	0	0	1	1	1
14	0	0	1	1	0	0	1	0	0	1	1	1	1	0	0	1	1	1

Table 4: Pilot bit patterns for uplink DPCCH with $N_{\text{pilot}} = 7$ and 8

Bit #	$N_{\text{pilot}} = 7$							$N_{\text{pilot}} = 8$							
	0	1	2	3	4	5	6	0	1	2	3	4	5	6	7
Slot #0	1	1	1	1	1	0	1	1	1	1	1	1	1	1	0
1	1	0	0	1	1	0	1	1	0	1	0	1	1	1	0
2	1	0	1	1	0	1	1	1	0	1	1	1	0	1	1
3	1	0	0	1	0	0	1	1	0	1	0	1	0	1	0
4	1	1	0	1	0	1	1	1	1	1	0	1	0	1	1
5	1	1	1	1	1	0	1	1	1	1	1	1	1	1	0
6	1	1	1	1	0	0	1	1	1	1	1	1	0	1	0
7	1	1	0	1	0	0	1	1	1	1	0	1	0	1	0
8	1	0	1	1	1	0	1	1	0	1	1	1	1	1	0
9	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
10	1	0	1	1	0	1	1	1	0	1	1	1	0	1	1
11	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1
12	1	1	0	1	0	0	1	1	1	1	0	1	0	1	0
13	1	0	0	1	1	1	1	1	0	1	0	1	1	1	1
14	1	0	0	1	1	1	1	1	0	1	0	1	1	1	1

The relationship between the TPC bit pattern and transmitter power control command is presented in table 5.

Table 5: TPC Bit Pattern

TPC Bit Pattern		Transmitter power control command
$N_{\text{TPC}} = 1$	$N_{\text{TPC}} = 2$	
1	11	1
0	00	0

Multi-code operation is possible for the uplink dedicated physical channels. When multi-code transmission is used, several parallel DPDCH are transmitted using different channelization codes, see [4]. However, there is only one DPCCH per radio link.

A power control preamble may be used for initialisation of a DCH. Both the UL and DL DPCCHs shall be transmitted during the power control preamble. The length of the power control preamble is a UE-specific higher layer parameter, N_{pcp} (see [5], section 5.1.2.4), signalled by the network. The UL DPCCH shall take the same slot format in the power control preamble as afterwards, as given in table 2. When, $N_{\text{pcp}} > 0$ the pilot patterns from slot # $(15 - N_{\text{pcp}})$ to slot #14 of table 3 and table 4 shall be used. The timing of the power control preamble is described in [5], subclause 4.3.2.2. ~~The TFCI field is filled with "1" bits.~~

If either no transport blocks, or an invalid set of transport blocks, have been provided to Layer 1, the TFCI code word shall be determined as follows: If a Transport Format Combination with no transport blocks is valid for that radio link,

then the corresponding TFCI code word shall be used, otherwise the TFCI code word shall be generated by setting the input to the TFCI channel coder to all "1" bits(see [3]).

5.3.2 Dedicated downlink physical channels

There is only one type of downlink dedicated physical channel, the Downlink Dedicated Physical Channel (downlink DPCH).

Within one downlink DPCH, dedicated data generated at Layer 2 and above, i.e. the dedicated transport channel (DCH), is transmitted in time-multiplex with control information generated at Layer 1 (known pilot bits, TPC commands, and an optional TFCI). The downlink DPCH can thus be seen as a time multiplex of a downlink DPDCH and a downlink DPCCH, compare subclause 5.2.1.

Figure 9 shows the frame structure of the downlink DPCH. Each frame of length 10 ms is split into 15 slots, each of length $T_{\text{slot}} = 2560$ chips, corresponding to one power-control period.

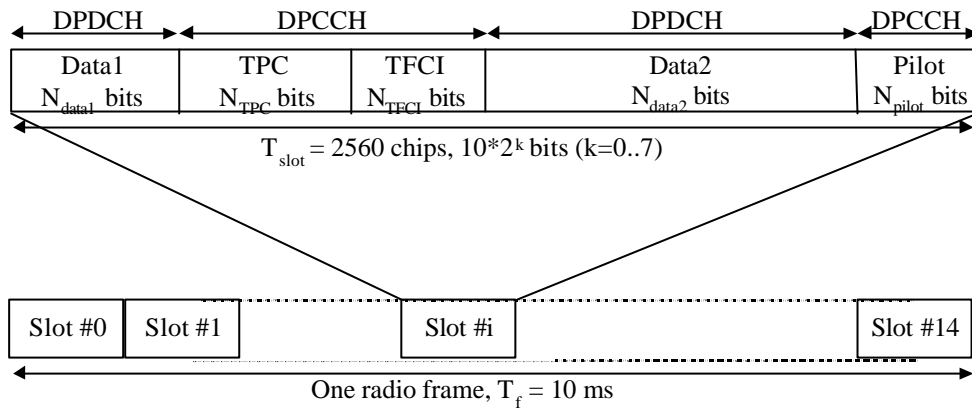


Figure 9: Frame structure for downlink DPCH

The parameter k in figure 9 determines the total number of bits per downlink DPCH slot. It is related to the spreading factor SF of the physical channel as $SF = 512/2^k$. The spreading factor may thus range from 512 down to 4.

The exact number of bits of the different downlink DPCH fields (N_{pilot} , N_{TPC} , N_{TFCI} , N_{data1} and N_{data2}) is given in table 11. What slot format to use is configured by higher layers and can also be reconfigured by higher layers.

There are basically two types of downlink Dedicated Physical Channels; those that include TFCI (e.g. for several simultaneous services) and those that do not include TFCI (e.g. for fixed-rate services). These types are reflected by the duplicated rows of table 11. It is the UTRAN that determines if a TFCI should be transmitted and it is mandatory for all UEs to support the use of TFCI in the downlink. The mapping of TFCI bits onto slots is described in [3].

In compressed mode, a different slot format is used compared to normal mode. There are two possible compressed slot formats that are labelled A and B. Format B is used for compressed mode by spreading factor reduction and format A is used for all other transmission time reduction methods. The channel bit and symbol rates given in table 11 are the rates immediately before spreading.

Table 11: DPDCH and DPCCH fields

Slot Format #i	Channel Bit Rate (kbps)	Channel Symbol Rate (ksps)	SF	Bits/Slot	DPDCH Bits/Slot		DPCCH Bits/Slot			Transmitted slots per radio frame N _{Tr}
					N _{Data1}	N _{Data2}	N _{TPC}	N _{TFCI}	N _{Pilot}	
0	15	7.5	512	10	0	4	2	0	4	15
0A	15	7.5	512	10	0	4	2	0	4	8-14
0B	30	15	256	20	0	8	4	0	8	8-14
1	15	7.5	512	10	0	2	2	2	4	15
1B	30	15	256	20	0	4	4	4	8	8-14
2	30	15	256	20	2	14	2	0	2	15
2A	30	15	256	20	2	14	2	0	2	8-14
2B	60	30	128	40	4	28	4	0	4	8-14
3	30	15	256	20	2	12	2	2	2	15
3A	30	15	256	20	2	10	2	4	2	8-14
3B	60	30	128	40	4	24	4	4	4	8-14
4	30	15	256	20	2	12	2	0	4	15
4A	30	15	256	20	2	12	2	0	4	8-14
4B	60	30	128	40	4	24	4	0	8	8-14
5	30	15	256	20	2	10	2	2	4	15
5A	30	15	256	20	2	8	2	4	4	8-14
5B	60	30	128	40	4	20	4	4	8	8-14
6	30	15	256	20	2	8	2	0	8	15
6A	30	15	256	20	2	8	2	0	8	8-14
6B	60	30	128	40	4	16	4	0	16	8-14
7	30	15	256	20	2	6	2	2	8	15
7A	30	15	256	20	2	4	2	4	8	8-14
7B	60	30	128	40	4	12	4	4	16	8-14
8	60	30	128	40	6	28	2	0	4	15
8A	60	30	128	40	6	28	2	0	4	8-14
8B	120	60	64	80	12	56	4	0	8	8-14
9	60	30	128	40	6	26	2	2	4	15
9A	60	30	128	40	6	24	2	4	4	8-14
9B	120	60	64	80	12	52	4	4	8	8-14
10	60	30	128	40	6	24	2	0	8	15
10A	60	30	128	40	6	24	2	0	8	8-14
10B	120	60	64	80	12	48	4	0	16	8-14
11	60	30	128	40	6	22	2	2	8	15
11A	60	30	128	40	6	20	2	4	8	8-14
11B	120	60	64	80	12	44	4	4	16	8-14
12	120	60	64	80	12	48	4	8*	8	15
12A	120	60	64	80	12	40	4	16*	8	8-14
12B	240	120	32	160	24	96	8	16*	16	8-14
13	240	120	32	160	28	112	4	8*	8	15
13A	240	120	32	160	28	104	4	16*	8	8-14
13B	480	240	16	320	56	224	8	16*	16	8-14
14	480	240	16	320	56	232	8	8*	16	15
14A	480	240	16	320	56	224	8	16*	16	8-14
14B	960	480	8	640	112	464	16	16*	32	8-14
15	960	480	8	640	120	488	8	8*	16	15
15A	960	480	8	640	120	480	8	16*	16	8-14
15B	1920	960	4	1280	240	976	16	16*	32	8-14
16	1920	960	4	1280	248	1000	8	8*	16	15
16A	1920	960	4	1280	248	992	8	16*	16	8-14

* If TFCI bits are not used, then DTX shall be used in TFCI field.

NOTE1: Compressed mode is only supported through spreading factor reduction for SF=512 with TFCI.

NOTE2: Compressed mode by spreading factor reduction is not supported for SF=4.

NOTE3: As specified in [15], if TFCI bits are being used and if either no transport blocks, or an invalid set of transport blocks, have been provided to Layer 1, the TFCI field shall be filled as follows: If a Transport Format Combination with no transport blocks is valid, then the corresponding TFCI code word shall be used, otherwise the DTX shall be used in the TFCI field

The pilot bit patterns are described in table 12. The shadowed column part of pilot bit pattern is defined as FSW and FSWs can be used to confirm frame synchronization. (The value of the pilot bit pattern other than FSWs shall be "11".) In table 12, the transmission order is from left to right.

In downlink compressed mode through spreading factor reduction, the number of bits in the TPC and Pilot fields are doubled. Symbol repetition is used to fill up the fields. Denote the bits in one of these fields in normal mode by $x_1, x_2, x_3, \dots, x_X$. In compressed mode the following bit sequence is sent in corresponding field: $x_1, x_2, x_1, x_2, x_3, x_4, x_3, x_4, \dots, x_X$.

Table 12: Pilot bit patterns for downlink DPCCH with $N_{\text{pilot}} = 2, 4, 8$ and 16

Symbol #	$N_{\text{pilot}} = 2$	$N_{\text{pilot}} = 4$ (*1)		$N_{\text{pilot}} = 8$ (*2)				$N_{\text{pilot}} = 16$ (*3)							
	0	0	1	0	1	2	3	0	1	2	3	4	5	6	7
Slot #0	11	11	11	11	11	11	10	11	11	11	10	11	11	11	10
1	00	11	00	11	00	11	10	11	00	11	10	11	11	11	00
2	01	11	01	11	01	11	01	11	01	11	01	11	10	11	00
3	00	11	00	11	00	11	00	11	00	11	00	11	01	11	10
4	10	11	10	11	10	11	01	11	10	11	01	11	11	11	11
5	11	11	11	11	11	11	10	11	11	11	10	11	01	11	01
6	11	11	11	11	11	11	00	11	11	11	00	11	10	11	11
7	10	11	10	11	10	11	00	11	10	11	00	11	10	11	00
8	01	11	01	11	01	11	10	11	01	11	10	11	00	11	11
9	11	11	11	11	11	11	11	11	11	11	11	11	00	11	11
10	01	11	01	11	01	11	01	11	01	11	01	11	11	11	10
11	10	11	10	11	10	11	11	11	10	11	11	11	00	11	10
12	10	11	10	11	10	11	00	11	10	11	00	11	01	11	01
13	00	11	00	11	00	11	11	11	00	11	11	11	00	11	00
14	00	11	00	11	00	11	11	11	00	11	11	11	10	11	01

NOTE *1: This pattern is used except slot formats 2B and 3B.

NOTE *2: This pattern is used except slot formats 0B, 1B, 4B, 5B, 8B, and 9B.

NOTE *3: This pattern is used except slot formats 6B, 7B, 10B, 11B, 12B, and 13B.

NOTE: For slot format nB where $n = 0, \dots, 15$, the pilot bit pattern corresponding to $N_{\text{pilot}}/2$ is to be used and symbol repetition shall be applied.

The relationship between the TPC symbol and the transmitter power control command is presented in table 13.

Table 13: TPC Bit Pattern

TPC Bit Pattern			Transmitter power control command
$N_{\text{TPC}} = 2$	$N_{\text{TPC}} = 4$	$N_{\text{TPC}} = 8$	
11	1111	11111111	1
00	0000	00000000	0

Multicode transmission may be employed in the downlink, i.e. the CCTrCH (see [3]) is mapped onto several parallel downlink DPCHs using the same spreading factor. In this case, the Layer 1 control information is transmitted only on the first downlink DPCH. DTX bits are transmitted during the corresponding time period for the additional downlink DPCHs, see figure 10.

In case there are several CCTrCHs mapped to different DPCHs transmitted to the same UE different spreading factors can be used on DPCHs to which different CCTrCHs are mapped. Also in this case, Layer 1 control information is only transmitted on the first DPCH while DTX bits are transmitted during the corresponding time period for the additional DPCHs.

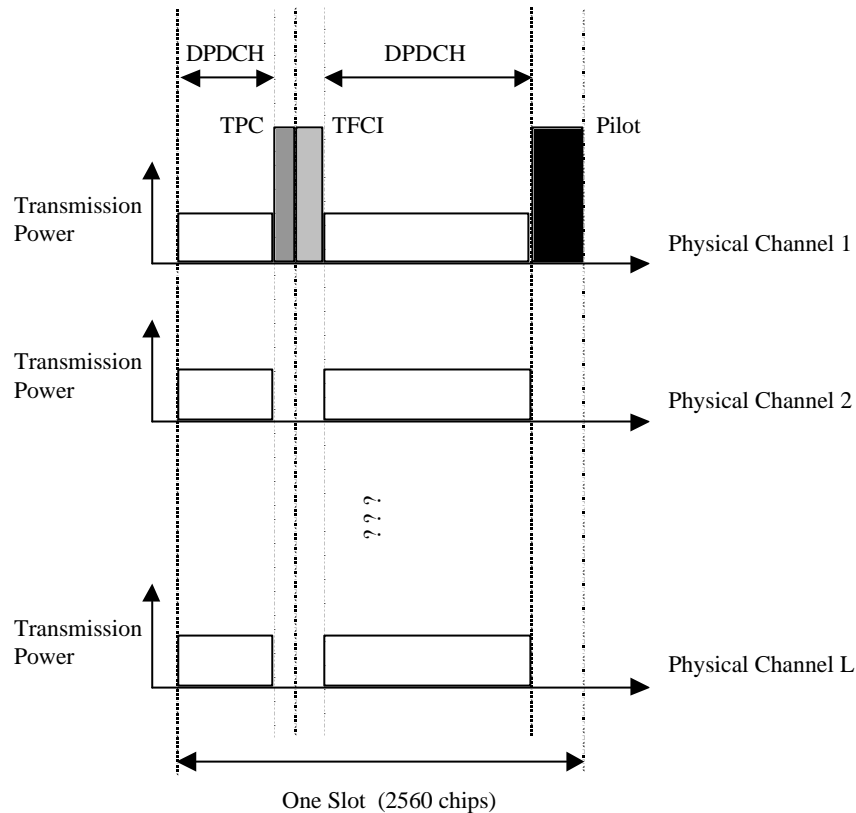


Figure 10: Downlink slot format in case of multi-code transmission

A power control preamble may be used for initialisation of a DCH. The DL DPCH shall take the same slot format in the power control preamble as afterwards, as given in Table 11, with the restriction that DTX shall be used in the DL DPDCH fields in the power control preamble. The length of the power control preamble is a UE-specific higher-layer parameter, N_{pcp} (see [5], section 5.1.2.4), signalled by the network. When $N_{pcp} > 0$, the pilot patterns from slot #(15 – N_{pcp}) to slot #14 of table 12 shall be used. *The TFCI field is filled with "1" bits.*

5.3.2.1 STTD for DPCH

The pilot bit pattern for the DPCH channel transmitted on antenna 2 is given in table 14.

- For $N_{pilot} = 8, 16$ the shadowed part indicates pilot bits that are obtained by STTD encoding the corresponding (shadowed) bits in Table 12. The non-shadowed pilot bit pattern is orthogonal to the corresponding (non-shadowed) pilot bit pattern in table 12.
- For $N_{pilot} = 4$, the diversity antenna pilot bit pattern is obtained by STTD encoding both the shadowed and non-shadowed pilot bits in table 12.
- For $N_{pilot} = 2$, the diversity antenna pilot pattern is obtained by STTD encoding the two pilot bits in table 12 with the last two bits (data or DTX) of the second data field (data2) of the slot. Thus for $N_{pilot} = 2$ case, the last two bits of the second data field (data 2) after STTD encoding, follow the diversity antenna pilot bits in Table 14.

STTD encoding for the DPDCH, TPC, and TFCI fields is done as described in subclause 5.3.1.1.1. For the SF=512 DPCH, the first two bits in each slot, i.e. TPC bits, are not STTD encoded and the same bits are transmitted with equal power from the two antennas. The remaining four bits are STTD encoded.

For compressed mode through spreading factor reduction and for $N_{pilot} > 4$, symbol repetition shall be applied to the pilot bit patterns of table 14, in the same manner as described in 5.3.2. For slot formats 2B and 3B, i.e. compressed mode through spreading factor reduction and $N_{pilot} = 4$, the pilot bits transmitted on antenna 2 are STTD encoded, and thus the pilot bit pattern is as shown in the most right set of table 14.

CHANGE REQUEST

Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.

25.212 CR 095

Current Version: **3.4.0 draft**

GSM (AA.BB) or 3G (AA.BBB) specification number ?

? CR number as allocated by MCC support team

For submission to: **RAN #9**
list expected approval meeting # here
?

for approval
for information

strategic
non-strategic (for SMG use only)

Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: <ftp://ftp.3gpp.org/Information/CR-Form-v2.doc>

Proposed change affects: (U)SIM ME UTRAN / Radio Core Network
(at least one should be marked with an X)

Source: Philips **Date:** 2000-09-15

Subject: TFCI in the case of invalid transport blocks

Work item:

Category: F Correction **Release:** Phase 2
(only one category shall be marked with an X) A Corresponds to a correction in an earlier release Release 96
B Addition of feature Release 97
C Functional modification of feature Release 98
D Editorial modification Release 99
Release 00

Reason for change: The current description does not cover this case.

Clauses affected: 4.3.3, 4.3.4

Other specs affected: Other 3G core specifications ? List of CRs: CR 25.111-083
Other GSM core specifications ? List of CRs:
MS test specifications ? List of CRs:
BSS test specifications ? List of CRs:
O&M specifications ? List of CRs:

Other comments:



help.doc

<----- double-click here for help and instructions on how to create a CR.

4.3.3 Coding of Transport-Format-Combination Indicator (TFCI)

The TFCI is encoded using a (32, 10) sub-code of the second order Reed-Muller code. The coding procedure is as shown in figure 9.

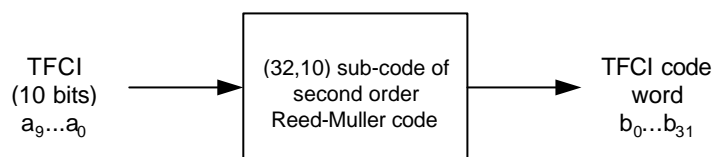


Figure 9: Channel coding of TFCI information bits

If an invalid set of transport blocks, (i.e. which does not correspond to one of the transport format combinations defined for that radio link) is provided to Layer 1 by higher layers, the TFCI code word shall be generated by setting all the input bits to the TFCI channel coder to "1".

If the TFCI consist of less than 10 bits, it is padded with zeros to 10 bits, by setting the most significant bits to zero. The length of the TFCI code word is 32 bits.

The code words of the (32,10) sub-code of second order Reed-Muller code are linear combination of 10 basis sequences. The basis sequences are as in the following table 8.

Table 8: Basis sequences for (32,10) TFCI code

i	$M_{i,0}$	$M_{i,1}$	$M_{i,2}$	$M_{i,3}$	$M_{i,4}$	$M_{i,5}$	$M_{i,6}$	$M_{i,7}$	$M_{i,8}$	$M_{i,9}$
0	1	0	0	0	0	1	0	0	0	0
1	0	1	0	0	0	1	1	0	0	0
2	1	1	0	0	0	1	0	0	0	1
3	0	0	1	0	0	1	1	0	1	1
4	1	0	1	0	0	1	0	0	0	1
5	0	1	1	0	0	1	0	0	1	0
6	1	1	1	0	0	1	0	1	0	0
7	0	0	0	1	0	1	0	1	1	0
8	1	0	0	1	0	1	1	1	1	0
9	0	1	0	1	0	1	1	0	1	1
10	1	1	0	1	0	1	0	0	1	1
11	0	0	1	1	0	1	0	1	1	0
12	1	0	1	1	0	1	0	1	0	1
13	0	1	1	1	0	1	1	0	0	1
14	1	1	1	1	0	1	1	1	1	1
15	1	0	0	0	1	1	1	1	0	0
16	0	1	0	0	1	1	1	1	0	1
17	1	1	0	0	1	1	1	0	1	0
18	0	0	1	0	1	1	0	1	1	1
19	1	0	1	0	1	1	0	1	0	1
20	0	1	1	0	1	1	0	0	1	1
21	1	1	1	0	1	1	0	1	1	1
22	0	0	0	1	1	1	0	1	0	0
23	1	0	0	1	1	1	1	1	0	1
24	0	1	0	1	1	1	1	0	1	0
25	1	1	0	1	1	1	1	0	0	1
26	0	0	1	1	1	1	0	0	1	0
27	1	0	1	1	1	1	1	1	0	0
28	0	1	1	1	1	1	1	1	1	0
29	1	1	1	1	1	1	1	1	1	1
30	0	0	0	0	0	1	0	0	0	0
31	0	0	0	0	0	1	1	1	0	0

The TFCI information bits $a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7, a_8, a_9$ (where a_0 is LSB and a_9 is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the CCTrCH in the associated DPCH radio frame.

The output code word bits b_i are given by:

$$b_i = \left(\sum_{n=0}^9 a_n M_{i,n} \right) \bmod 2$$

where $i = 0, \dots, 31$.

The output bits are denoted by $b_k, k = 0, 1, 2, \dots, 31$.

In downlink, when the SF < 128 the encoded TFCI code words are repeated yielding 8 encoded TFCI bits per slot in normal mode and 16 encoded TFCI bits per slot in compressed mode. Mapping of repeated bits to slots is explained in subclause 4.3.5.

4.3.4 Operation of Transport-Format-Combination Indicator (TFCI) in Split Mode

If one of the DCH is associated with a DSCH, the TFCI code word may be split in such a way that the code word relevant for TFCI activity indication is not transmitted from every cell. The use of such a functionality shall be indicated by higher layer signalling.

The TFCI is encoded using a (16, 5) bi-orthogonal (or first order Reed-Muller) code. The coding procedure is as shown in figure 10.

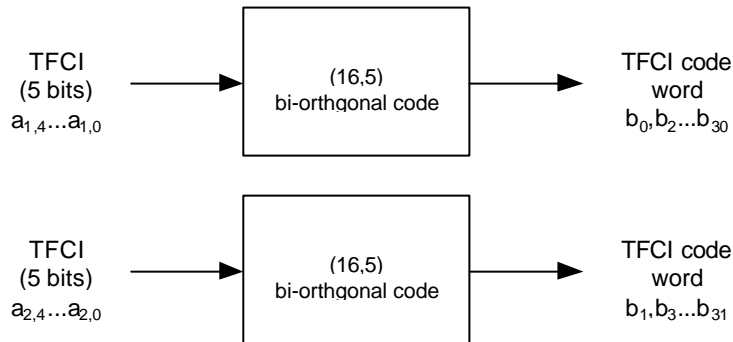


Figure 10: Channel coding of split mode TFCI information bits

If an invalid set of transport blocks (i.e. which does not correspond to one of the transport format combinations defined for that radio link) is provided to Layer 1, the TFCI code words shall be generated by setting all the input bits to both TFCI channel coders to "1".

The code words of the (16,5) bi-orthogonal code are linear combinations of 5 basis sequences as defined in table 9.

Table 9: Basis sequences for (16,5) TFCI code

i	M _{i,0}	M _{i,1}	M _{i,2}	M _{i,3}	M _{i,4}
0	1	0	0	0	1
1	0	1	0	0	1
2	1	1	0	0	1
3	0	0	1	0	1
4	1	0	1	0	1
5	0	1	1	0	1
6	1	1	1	0	1
7	0	0	0	1	1
8	1	0	0	1	1
9	0	1	0	1	1
10	1	1	0	1	1
11	0	0	1	1	1
12	1	0	1	1	1
13	0	1	1	1	1
14	1	1	1	1	1
15	0	0	0	0	1

The first set of TFCI information bits ($a_{1,0}, a_{1,1}, a_{1,2}, a_{1,3}, a_{1,4}$ where $a_{1,0}$ is LSB and $a_{1,4}$ is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the DCH CTrCH in the associated DPCH radio frame.

The second set of TFCI information bits ($a_{2,0}, a_{2,1}, a_{2,2}, a_{2,3}, a_{2,4}$ where $a_{2,0}$ is LSB and $a_{2,4}$ is MSB) shall correspond to the TFC index (expressed in unsigned binary form) defined by the RRC layer to reference the TFC of the associated DSCH CTrCH in the corresponding PDSCH radio frame.

The output code word bits b_k are given by:

$$b_{2i} = \sum_{n=0}^4 (a_{1,n} \cdot M_{i,n}) \bmod 2; \quad b_{2i+1} = \sum_{n=0}^4 (a_{2,n} \cdot M_{i,n}) \bmod 2$$

where $i = 0, \dots, 15$.

The output bits are denoted by $b_k, k = 0, 1, 2, \dots, 31$.