

## CHANGE REQUEST

Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.

**25.214 CR 129**

Current Version: **3.4.0**

GSM (AA.BB) or 3G (AA.BBB) specification number ?

? CR number as allocated by MCC support team

For submission to: **RAN#10**  
*list expected approval meeting # here ?*

for approval   
for information

strategic  (for SMG use only)  
non-strategic

Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: <http://ftp.3gpp.org/Information/CR-F-Formv2.doc>

**Proposed change affects:**  
*(at least one should be marked with an X)*

(U)SIM  ME  UTRAN / Radio  Core Network

**Source:** **Siemens** **Date:** **29 Sep 2000**

**Subject:** **Formula typography and reference corrections**

**Work item:**

**Category:**  
*(only one category shall be marked with an X)*

F Correction   
A Corresponds to a correction in an earlier release   
B Addition of feature   
C Functional modification of feature   
D Editorial modification

**Release:** Phase 2   
Release 96   
Release 97   
Release 98   
Release 99   
Release 00

**Reason for change:** **The changed expression is not in the correct 3GPP formulaic format. (it pretends to be in C code). Three document references have been changed into the proper reference table format.**

**Clauses affected:** **5.1.3.2, 5.2.1.3, 5.2.1.4.4, 7.1**

<b>Other specs affected:</b>	Other 3G core specifications <input type="checkbox"/>	? List of CRs:	
	Other GSM core specifications <input type="checkbox"/>	? List of CRs:	
	MS test specifications <input type="checkbox"/>	? List of CRs:	
	BSS test specifications <input type="checkbox"/>	? List of CRs:	
	O&M specifications <input type="checkbox"/>	? List of CRs:	

**Other comments:**

<----- double-click here for help and instructions on how to create a CR.

### 5.1.3.2 Power control in the message part

The uplink transmit power control procedure simultaneously controls the power of a PCPCH control part and its corresponding PCPCH data part. The relative transmit power offset between the PCPCH control part and the PCPCH data part is determined by the network and is computed according to sub-clause 5.1.2.5 using the gain factors signalled to the UE using higher-layer signalling, with the difference that:

- $\alpha_c$  is the gain factor for the PCPCH control part (similar to DPCCCH);
- $\alpha_d$  is the gain factor for the PCPCH data part (similar to DPDCH).

The gain factors are applied as shown in sub clause 4.2.3.2 of ~~[3]25.213~~.

The operation of the inner power control loop adjusts the power of the PCPCH control part and PCPCH data part by the same amount, provided there are no changes in gain factors.

Any change in the uplink PCPCH control part transmit power shall take place immediately before the start of the pilot field on the control part of the message part. The change in PCPCH control part power with respect to its value in the previous slot is derived by the UE and is denoted by  $\Delta_{PCPCH-CP}$  (in dB).

During the operation of the uplink power control procedure the UE transmit power shall not exceed a maximum allowed value which is the lower out of the maximum output power of the terminal power class and a value which may be set by higher layer signalling.

Uplink power control shall be performed while the UE transmit power is below the maximum allowed output power.

The provisions for power control at the maximum allowed value and below the required minimum output power (as defined in [7]) are described in sub-clause 5.1.2.6.

The uplink inner-loop power control adjusts the UE transmit power in order to keep the received uplink signal-to-interference ratio (SIR) at a given SIR target,  $SIR_{target}$ , which is set by the higher layer outer loop.

The network should estimate the signal-to-interference ratio  $SIR_{est}$  of the received PCPCH. The network should then generate TPC commands and transmit the commands once per slot according to the following rule: if  $SIR_{est} > SIR_{target}$  then the TPC command to transmit is "0", while if  $SIR_{est} < SIR_{target}$  then the TPC command to transmit is "1".

The UE derives a TPC command,  $TPC_{cmd}$ , for each slot. Two algorithms shall be supported by the UE for deriving a  $TPC_{cmd}$ . Which of these two algorithms is used is determined by a higher-layer parameter, "PowerControlAlgorithm", and is under the control of the UTRAN. If "PowerControlAlgorithm" indicates "algorithm1", then the layer 1 parameter PCA shall take the value 1 and if "PowerControlAlgorithm" indicates "algorithm2" then PCA shall take the value 2.

If PCA has the value 1, Algorithm 1, described in subclause 5.1.2.2.2, shall be used for processing TPC commands.

If PCA has the value 2, Algorithm 2, described in subclause 5.1.2.2.3, shall be used for processing TPC commands.

The step size  $\Delta_{TPC}$  is a layer 1 parameter which is derived from the higher-layer parameter "TPC-StepSize" which is under the control of the UTRAN. If "TPC-StepSize" has the value "dB1", then the layer 1 parameter  $\Delta_{TPC}$  shall take the value 1 dB and if "TPC-StepSize" has the value "dB2", then  $\Delta_{TPC}$  shall take the value 2 dB.

After deriving the TPC command  $TPC_{cmd}$  using one of the two supported algorithms, the UE shall adjust the transmit power of the uplink PCPCH control part with a step of  $\Delta_{PCPCH-CP}$  (in dB) which is given by:

$$\Delta_{PCPCH-CP} = \Delta_{TPC} \cdot TPC_{cmd}$$

### 5.2.1.3 Power control in compressed mode

The aim of downlink power control in uplink or/and downlink compressed mode is to recover as fast as possible a signal-to-interference ratio (SIR) close to the target SIR after each transmission gap.

The UE behaviour is the same in compressed mode as in normal mode, described in subclause 5.2.1.2.

In compressed mode, compressed frames may occur in either the uplink or the downlink or both. In compressed frames, the transmission of downlink DPDCH(s) and DPCCH shall be stopped during transmission gaps.

The power of the DPCCH and DPDCH in the first slot after the transmission gap should be set to the same value as in the slot just before the transmission gap.

In every slot during compressed mode except during downlink transmission gaps, UTRAN shall estimate the  $k$ :th TPC command and adjust the current downlink power  $P(k-1)$  [dB] to a new power  $P(k)$  [dB] according to the following formula:

$$P(k) = P(k-1) + P_{TPC}(k) + P_{SIR}(k) + P_{bal}(k),$$

where  $P_{TPC}(k)$  is the  $k$ :th power adjustment due to the inner loop power control,  $P_{SIR}(k)$  is the  $k$ -th power adjustment due to the downlink target SIR variation, and  $P_{bal}(k)$  [dB] is a correction according to the downlink power control procedure for balancing radio link powers towards a common reference power. The power balancing procedure and control of the procedure is described in [\[6\]TS 25.433](#), and an example of how  $P_{bal}(k)$  can be calculated is given in Annex B.3.

Due to transmission gaps in uplink compressed frames, there may be missing TPC commands in the uplink. If no uplink TPC command is received,  $P_{TPC}(k)$  derived by the Node B shall be set to zero. Otherwise,  $P_{TPC}(k)$  is calculated the same way as in normal mode (see sub-clause 5.2.1.2.2) but with a step size  $\Delta_{STEP}$  instead of  $\Delta_{TPC}$ .

The power control step size  $\Delta_{STEP} = \Delta_{RP-TPC}$  during RPL slots after each transmission gap and  $\Delta_{STEP} = \Delta_{TPC}$  otherwise, where:

- RPL is the recovery period length and is expressed as a number of slots. RPL is equal to the minimum value out of the transmission gap length and 7 slots. If a transmission gap is scheduled to start before RPL slots have elapsed, then the recovery period shall end at the start of the gap, and the value of RPL shall be reduced accordingly.??

??  $\Delta_{RP-TPC}$  is called the recovery power control step size and is expressed in dB.  $\Delta_{RP-TPC}$  is equal to the minimum value of 3 dB and  $2\Delta_{TPC}$ .

The power offset  $P_{SIR}(k) = P_{curr} - P_{prev}$ , where  $P_{curr}$  and  $P_{prev}$  are respectively the value of  $P$  in the current slot and the most recently transmitted slot and  $P$  is computed as follows:

$$P = \max(\Delta P1\_compression, \dots, \Delta Pn\_compression) + \Delta P1\_coding + \Delta P2\_coding$$

where  $n$  is the number of different TTI lengths amongst TTIs of all TrChs of the CCTrCh, where  $\Delta P1\_coding$  and  $\Delta P2\_coding$  are computed from uplink parameters DeltaSIR1, DeltaSIR2, DeltaSIRafter1, DeltaSIRafter2 signaled by higher layers as:

- $\Delta P1\_coding = \Delta SIR1$  if the start of the first transmission gap in the transmission gap pattern is within the current frame.
- $\Delta P1\_coding = \Delta SIRafter1$  if the current frame just follows a frame containing the start of the first transmission gap in the transmission gap pattern.
- $\Delta P2\_coding = \Delta SIR2$  if the start of the second transmission gap in the transmission gap pattern is within the current frame.
- $\Delta P2\_coding = \Delta SIRafter2$  if the current frame just follows a frame containing the start of the second transmission gap in the transmission gap pattern.
- $\Delta P1\_coding = 0$  dB and  $\Delta P2\_coding = 0$  dB in all other cases.

and  $\Delta Pi\_compression$  is defined by :

- $\Delta P_{\text{compression}} = 3 \text{ dB}$  for downlink frames compressed by reducing the spreading factor by 2.
- $\Delta P_{\text{compression}} = 10 \log (15 \cdot F_i / (15 \cdot F_i - \text{TGL}_i))$  if there is a transmission gap created by puncturing method within the current TTI of length  $F_i$  frames, where  $\text{TGL}_i$  is the gap length in number of slots (either from one gap or a sum of gaps) in the current TTI of length  $F_i$  frames.
- $\Delta P_{\text{compression}} = 0 \text{ dB}$  in all other cases.

In case several compressed mode patterns are used simultaneously, a  $\Delta P$  offset is computed for each compressed mode pattern and the sum of all  $\Delta P$  offsets is applied to the frame.

#### 5.2.1.4.4 Delivery of primary cell ID

The UE periodically sends the ID code of the primary cell via portion of the uplink FBI field assigned for SSST use (FBI S field). A cell recognises its state as non-primary if the following conditions are fulfilled simultaneously:

- the received primary ID code does not match with the own ID code;
- the received uplink signal quality satisfies a quality threshold,  $Q_{th}$ , a parameter defined by the network;
- and when the use of uplink compressed mode does not result in excessive levels of puncturing on the coded ID. The acceptable level of puncturing on the coded ID is less than  $(\frac{1}{3})N_{ID}$  symbols in the coded ID, where  $N_{ID}$  is the length of the coded ID.

Otherwise the cell recognises its state as primary.

The state of the cells (primary or non-primary) in the active set is updated synchronously. If a cell receives the last portion of the coded ID in uplink slot  $j$ , the state of cell is updated in downlink slot  $(j+1+T_{os}) \bmod 15$ , where  $T_{os}$  is defined as a constant of 2 time slots. The updating of the cell state is not influenced by the operation of downlink compressed mode.

## 7.1 Determination of feedback information

The UE uses the Common Pilot Channel (CPICH) to separately estimate the channels seen from each antenna.

Once every slot, the UE computes the phase adjustment,  $\theta$ , and for mode 2 the amplitude adjustment that should be applied at the UTRAN access point to maximise the UE received power. In non-soft handover case, that can be accomplished by e.g. solving for weight vector,  $\underline{w}$ , that maximises.

$$P = \underline{w}^H H^H H \underline{w} \tag{1}$$

where

$$H = [h_1 \ h_2]^T \text{ and } \underline{w} = [w_1, w_2]^T$$

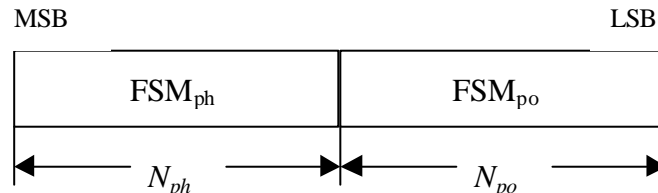
and where the column vectors  $h_1$  and  $h_2$  represent the estimated channel impulse responses for the transmission antennas 1 and 2, of length equal to the length of the channel impulse response. The elements of  $\underline{w}$  correspond to the adjustments computed by the UE.

During soft handover or SSDT power control, the antenna weight vector,  $\underline{w}$  can be, for example, determined so as to maximise the criteria function:

$$P = \underline{w}^H (H_1^H H_1 + H_2^H H_2 + \dots) \underline{w} \tag{2}$$

where  $H_i$  is an estimated channel impulse response for BS#i. In regular SHO, the set of BS#i corresponds to the active set. With SSDT, the set of BS#i corresponds to the primary base station(s).

The UE feeds back to the UTRAN access point the information on which phase/power settings to use. Feedback Signalling Message (FSM) bits are transmitted in the portion of FBI field of uplink DPCCCH slot(s) assigned to closed loop mode transmit diversity, the FBI D field (see [\[1\]25.214](#)). Each message is of length  $N_w = N_{po} + N_{ph}$  bits and its format is shown in the figure 4. The transmission order of bits is from MSB to LSB, i.e. MSB is transmitted first.  $FSM_{po}$  and  $FSM_{ph}$  subfields are used to transmit the power and phase settings, respectively.



**Figure 4: Format of feedback signalling message.  $FSM_{po}$  transmits the power setting and  $FSM_{ph}$  the phase setting**

The adjustments are made by the UTRAN Access Point at the beginning of the downlink DPCCCH pilot field. The downlink slot in which the adjustment is done is signalled to L1 of UE by higher layers. Two possibilities exist:

- 1) When feedback command is transmitted in uplink slot  $i$ , which is transmitted in a chip offset limited to 1024 ? 148 chips when compared to received downlink slot  $j$ , the adjustment is done at the beginning of the pilot field of the downlink slot  $(j+1) \bmod 15$ .
- 2) When feedback command is transmitted in uplink slot  $i$ , which is transmitted in a chip offset limited to 1024 ? 148 chips when compared to received downlink slot  $j$ , the adjustment is done at the beginning of the pilot field of the downlink slot  $(j+2) \bmod 15$ .

Thus, adjustment timing at UTRAN Access Point is either according to 1) or 2) as controlled by the higher layers.

In case a PDSCH is associated with a DPCH for which closed-loop transmit diversity is applied, the antenna weights applied to the PDSCH are the same as the antenna weights applied to the associated DPCH. The timing of the weight adjustment of the PDSCH is such that the PDSCH weight adjustment is done at the PDSCH slot border,  $N$  chips after the adjustment of the associated DPCH, where  $0 \leq N < 2560$ .