3GPP TSG RAN WG1#12 Seoul, Korea, 10-13 April 2000

Document R1-00-0537 e.g. for 3GPP use the format TP-99xxx or for SMG, use the format P-99-xxx

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		25.211	CR	051	C	Current Versi	on: 3.2.0	
GSM (AA.BB) or 3G (AA.BBB) specification number ↑ ↑ CR number as allocated by MCC support team								
For submission list expected approval	meeting # here ↑	for infor		X		strate non-strate	egic use o	nly)
Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: ftp://ftp.3gpp.org/Information/CR-Form-v2.doc Proposed change affects: (at least one should be marked with an X) The latest version of this form is available from: ftp://ftp.3gpp.org/Information/CR-Form-v2.doc X Core Network								
Source:	Siemens					Date:	7-April-2000)
Subject:	Bit value no	tation change for	PICH a	nd CSICH	1			
Work item:								
Category: (only one category shall be marked with an X)	Correspond Addition of Functional	modification of fea		rlier relea	X	Release:	Phase 2 Release 96 Release 97 Release 98 Release 99 Release 00	X
Reason for change:	The bit value modulation.	ue notation 0/1 use	ed does	not matcl	h the expe	ected +1/-1 n	otation for	
Clauses affected	d: 5.3.3.9	9, 5.3.3.10						
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Other comments:								

<----- double-click here for help and instructions on how to create a CR.

Number of PI per frame (N)	PI _p = 1	PI _p = 0
N=18	$\{b_{16p},, b_{16p+15}\} = \{4\underline{-1}, 4\underline{-1},, 4\underline{-1}\}$	$\{b_{16p},, b_{16p+15}\} = \{0+1, 0+1,, 0+1\}$
N=36	$\{b_{8p},, b_{8p+7}\} = \{4\underline{-1}, 4\underline{-1},, 4\underline{-1}\}$	$\{b_{8p},, b_{8p+7}\} = \{\underbrace{0+1}, \underbrace{0+1},, \underbrace{0+1}\}$
N=72	$\{b_{4p},, b_{4p+3}\} = \{4-1, 4-1,, 4-1\}$	$\{b_{4p},, b_{4p+3}\} = \{0+1, 0+1,, 0+1\}$
N-1//	$\int b_0 b_0 dt = \int 4.14.1$	$\int b_0 b_0 d\lambda = \int 0.10 0.1$

Table 22: Mapping of Paging Indicators (PI) to PICH bits

If a Paging Indicator in a certain frame is set to "1" it is an indication that UEs associated with this Paging Indicator should read the corresponding frame of the associated S-CCPCH.

When transmit diversity is employed for the PICH, STTD encoding is used on the PICH bits as described in section 5.3.1.1.1.

5.3.3.10 CPCH Status Indicator Channel (CSICH)

The CPCH Status Indicator Channel (CSICH) is a fixed rate (SF=256) physical channel used to carry CPCH status information.

A CSICH is always associated with a physical channel used for transmission of CPCH AP-AICH and uses the same channelization and scrambling codes. Figure 25 illustrates the frame structure of the CSICH. The CSICH frame consists of 15 consecutive access slots (AS) each of length 40 bits. Each access slot consists of two parts, a part of duration 4096 chips with no transmission, and a Status Indicator (SI) part consisting of 8 bits b_{8i} ,.... b_{8i+7} , where i is the access slot number. The modulation used by the CSICH is the same as for the PICH. The phase reference for the CSICH is the Primary CPICH.

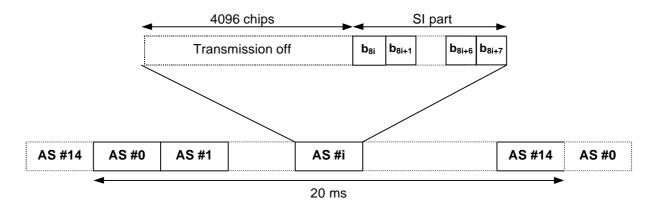


Figure 25: Structure of CPCH Status Indicator Channel (CSICH)

N Status Indicators $\{SI_0, ..., SI_{N-1}\}$ shall be transmitted in each CSICH frame. The mapping from $\{SI_0, ..., SI_{N-1}\}$ to the CSICH bits $\{b_0, ..., b_{119}\}$ is according to table 23. The Status Indicators shall be transmitted in all the access slots of the CSICH frame, even if some signatures and/or access slots are shared between CPCH and RACH.

Table 23: Mapping of Status Indicators (SI) to CSICH bits

Number of SI per frame (N)	SI _n = 1	SI _n = 0
N=1	$\{b_0, \ldots, b_{119}\} = \{4\underline{-1}, 4\underline{-1}, \ldots, 4\underline{-1}\}$	$\{b_0,, b_{119}\} = \{0+1, 0+1,, 0+1\}$
N=3	$\{b_{40n},, b_{40n+39}\} = \{1-1,1-1,,1-1\}$	$\{b_{40n},, b_{40n+39}\} = \{0+1, 0+1,, 0+1\}$
N=5	$\{b_{24n},, b_{24n+23}\} = \{4-1, 4-1,, 4-1\}$	$\{b_{24n},, b_{24n+23}\} = \{0+1, 0+1,, 0+1\}$
N=15	$\{b_{8n},, b_{8n+7}\} = \{4-1, 4-1,, 4-1\}$	$\{b_{8n},, b_{8n+7}\} = \{0+1, 0+1,, 0+1\}$
N=30	$\{b_{4n},, b_{4n+3}\} = \{1-1, 1-1, 1-1, 1-1\}$	$\{b_{4n},, b_{4n+3}\} = \{0+1, 0+1, 0+1, 0+1\}$
N=60	$\{b_{2n}, b_{2n+1}\} = \{4-1, 4-1\}$	$\{b_{2n}, b_{2n+1}\} = \{0+1, 0+1\}$