**3GPP TSG RAN WG1 #104-e R1-210xxxx**

**e-Meeting, Jan. 25th – Feb. 5th, 2021**

**Agenda Item:** 7.2.5

**Source:** Moderator (LG Electronics)

**Title:** Summary of [104-e-NR-L1enh-URLLC-05]

**Document for:** Discussion and decision

# Introduction

According to discussion at the preparation phase, the following email thread is allocated by Chairman for further discussion:

[104-e-NR-L1enh-URLLC-05] Email discussion/approval on remaining issues on SPS enhancements – Duckhyun (LG) by Feb 3

* Issue 3: SPS PDSCH release and SPS receptions with slot aggregation
* Issue 4: PUCCH resource for SPS PDSCH HARQ-ACK and SR

To address the identified issues of the above email thread, suggestions and questions for the issues are provided in Section 2. In section 3-4, the outcome from [104-e-NR-L1enh-URLLC-05] are provided including all the agreements and all the endorsed TPs.

Other submitted issues are listed up in the summary in preparation phase [4]. It would be appreciated that companies can provide input/feedback in the next meeting in order to facilitate the discussion.

# Issues in RAN1#104-e

* 1. Issue #3 SPS PDSCH release and SPS receptions with slot aggregation

In [1][3], TP and CR is provided for the case of SPS PDSCH release and SPS repetitions. According to [3], When a SPS PDSCH is configured with aggregation factor, some of the SPS PDSCH occasions within the repetition may end before the end of the release PDCCH while some others may end after the end of the release PDCCH, as shown below. In this regard, the figure may be effectively considered as a SPS PDSCH which ends in slot nD=3 and should be supported.



**1st TP from [3]:**

|  |
| --- |
| 9.1 HARQ-ACK codebook determination \*\*\* Unchanged text is omitted \*\*\*  If a UE is configured to receive SPS PDSCHs in a slot for SPS configurations that are indicated to be released by a DCI format, and if the UE receives the PDCCH providing the DCI format in the slot where the end of a last symbol of the PDCCH reception is not after the end of a last symbol of any of the last occasions of SPS PDSCH receptions, if the last occasion is in the slot, and if HARQ-ACK information for the SPS PDSCH release and the SPS PDSCH receptions would be multiplexed in a same PUCCH, the UE does not expect to receive the SPS PDSCHs, does not generate HARQ-ACK information for the SPS PDSCH receptions, and generates a HARQ-ACK information bit for the SPS PDSCH release.  \*\*\* Unchanged text is omitted \*\*\* |

According to [1], current specification doesn’t care about slot aggregation so that restrict SPS PDSCH reception even when there is no HARQ-ACK bit collision in the type-1 HARQ-ACK codebook. To solve this problem, [1] propose following TP:

**2nd TP from [1]:**

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| 9.1 HARQ-ACK codebook determination \*\*\* Unchanged text is omitted \*\*\*  If a UE is configured to receive SPS PDSCHs in a slot for SPS configurations that are indicated to be released by a DCI format, and if the UE receives the PDCCH providing the DCI format in the slot where the end of a last symbol of the PDCCH reception is not after the end of a last symbol of any of the SPS PDSCH receptions, and if HARQ-ACK information for the SPS PDSCH release and the SPS PDSCH receptions would be multiplexed with same value of in a same PUCCH, the UE does not expect to receive the SPS PDSCHs, does not generate HARQ-ACK information for the SPS PDSCH receptions, and generates a HARQ-ACK information bit for the SPS PDSCH release.  \*\*\* Unchanged text is omitted \*\*\* |

**From FL:**

As mention in the previous meeting, what makes problem is that the SPS PDSCH and SPS release are mapped to same UCI bit in the same PUCCH. In the figure brought by [3], if they are mapped to same PUCCH, those HARQ-ACK are mapped to different UCI bits due to different K1 value. Though the specification changes may be necessary to clarify previous agreement, but it is not clear whether to consider the last occasion of SPS PDSCH.

This issue is related to previous CR. We defined the problem “HARQ-ACK bit overlapping between SPS PDSCH and SPS release” and dropped SPS PDSCH HARQ-ACK to solve the problem. Though current text had make a problem with slot aggregation, however, the principle still valid with slot aggregation. Thus, I would recommend to clarify and reflect our principle into specification text.

Meanwhile, since current text already four if statement, I wouldn’t like to recommend to add more conditioning text. If both text proposal are in the light of our previous principle and work, I suggest to adopt second text proposal.

**FL recommendation: Adopt following TP for TS 38.214 Clause 9.1**

**Proposed text proposal:**

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| --- |
| 9.1 HARQ-ACK codebook determination \*\*\* Unchanged text is omitted \*\*\*  If a UE is configured to receive SPS PDSCHs in a slot for SPS configurations that are indicated to be released by a DCI format, and if the UE receives the PDCCH providing the DCI format in the slot where the end of a last symbol of the PDCCH reception is not after the end of a last symbol of any of the SPS PDSCH receptions, and if HARQ-ACK information for the SPS PDSCH release and the SPS PDSCH receptions would be multiplexed into same value of in a same PUCCH, the UE does not expect to receive the SPS PDSCHs, does not generate HARQ-ACK information for the SPS PDSCH receptions, and generates a HARQ-ACK information bit for the SPS PDSCH release.  \*\*\* Unchanged text is omitted \*\*\* |

**Comment:**

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| --- | --- |
| Company | Comment |
| Samsung | Not support.  is only for Type-1 HARQ-ACK codebook. The timeline restriction applies to both Type-1 and Type-2 HARQ-ACK codebooks.  Further, even for Type-1 HARQ-ACK codebook, the proposed TP may not work for joint release as the SPS PDSCHs could map to different . Take the following case as an example,  The SPS PDSCHs #0 and #1 would be multiplexed with, SPS PDSCHs #2 and #3 would be multiplexed with. The release PDCCH would be multiplexed at the location of SPS config#0, i.e. Then the condition in the proposed TP by LG does not hold. Therefore, the intended behavior that UE should only generate one A/N for the release PDCCH and not receive the SPS PDSCHs is not specified.    1st TP from [3] should be adopted. BTW, there is a typo for the reference [2] which should be [3] instead. |
| CATT | Basically we do not see the motivation to send a release DCI in the subsequent slot(s) when slot aggregation is configured for SPS.  If it is allowed, then if the release DCI is sent in the last slot of SPS repetition, both HARQ-ACK for SPS release and SPS PDSCH would occupy the same HARQ-ACK bit if the HARQ-ACKs are multiplexed in a same HARQ-ACK codebook. In this case, if UE missed the release DCI but correctly decoded the SPS PDSCH, UE would send ACK to the gNB and gNB may think that release DCI is correctly received by the UE. Although both HARQ-ACK for release DCI in other slots except the last slot of SPS repetition and HARQ-ACK for SPS PDSCH can be transmitted in a same HARQ-ACK codebook, in order to have a unified solution and considering that the use case is unclear, we propose to not support that as well.  In summary, for SPS PDSCH repetition, it is proposed that the release DCI can only be transmitted in the first slot of SPS PDSCH repetitions and the timeline requirement in the slot applies. |
| Nokia, NSB | Do not support the current proposed TP – agree with the comments by Samsung, namely:   1. The timeline applies to both Type 1 & Type 2 CB 2. The issue of the bit mapping only applies to Type 1 CB 3. Multiplexing in the same only solves the problem for single SPS release (but not joint SPS release) |
| OPPO | We share view with CATT |
| HW/HiSi | Do not support the TP.  The UE should stop all the remaining PDSCH receptions after it has received the release DCI, including the repetition in the same slot with the release DCI. We don’t think it is reasonable for the UE to decode the SPS PDSCH after it detects the release DCI. According to our understanding the current TP seems to enforce this? Or do we miss anything here? |
| ZTE | Samsung’s proposal is preferable. |
| Qualcomm | Do not support the TP.  Neither first TP from [3] and second TP from [1] are needed. For [1], we don’t see a use case for the network to send the release DCI at the last slot of the repetition (since UE still needs to decode the previous SPS PDSCH repetitions, but does not send any HARQ-ACK feedback...). |
| Apple | It seems further discussion is needed. One question on the text from Samsung (copied below):  Does the text marked in yellow refer to SPS receptions for different SPS configurations?  Not sure what the text marked in cyan (“the last occasion is in the slot”) establishes.  If a UE is configured to receive SPS PDSCHs in a slot for SPS configurations that are indicated to be released by a DCI format, and if the UE receives the PDCCH providing the DCI format in the slot where the end of a last symbol of the PDCCH reception is not after the end of a last symbol of any of the last occasions of SPS PDSCH receptions, if the last occasion is in the slot, and if HARQ-ACK information for the SPS PDSCH release and the SPS PDSCH receptions would be multiplexed in a same PUCCH, the UE does not expect to receive the SPS PDSCHs, does not generate HARQ-ACK information for the SPS PDSCH receptions, and generates a HARQ-ACK information bit for the SPS PDSCH release. |

* 1. Issue #4 PUCCH resource for SPS PDSCH HARQ-ACK and SR

According [2], when a SPS PDSCH HARQ-ACK PUCCH overlaps with a SR PUCCH, the SPS PDSCH HARQ-ACK PUCCH is used as the result PUCCH in general. However, in the current spec, PUCCH resource determination of *SPS-PUCCH-AN-List* only includes case of SPS PDSCH HARQ-ACK, the case of SPS PDSCH HARQ-ACK and SR is not included.

**Proposed changes from [2]:**

***Proposal 1: The UCI should include SPS PDSCH HARQ-ACK and SR, if any, when determining a PUCCH resource in SPS-PUCCH-AN-List. The following TP should be adopted.***

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| If the UE is provided *SPS-PUCCH-AN-List* and transmits UCI information bits that include only HARQ-ACK information bits in response to one or more SPS PDSCH receptions and SR, if any, the UE determines a PUCCH resource to be  - a PUCCH resource provided by *sps-PUCCH-AN-ResourceID* obtained from the first entry in *sps-PUCCH-AN-List* if , or  - a PUCCH resource provided by *sps-PUCCH-AN-ResourceID* obtained from the second entry in *sps-PUCCH-AN-List*, if provided, if where is either provided by *maxPayloadSize* obtained from the second entry in *sps-PUCCH-AN-List* or is otherwise equal to 1706, or  - a PUCCH resource provided by *sps-PUCCH-AN-ResourceID* obtained from the third entry in *sps-PUCCH-AN-List*, if provided, if where is either provided by *maxPayloadSize* obtained from the third entry in *sps-PUCCH-AN-List* or is otherwise equal to 1706, or  - a PUCCH resource provided by *sps-PUCCH-AN-ResourceID* obtained from the fourth entry in *sps-PUCCH-AN-List*, if provided, if where is equal to 1706. |

**From FL:**

For my understanding, it is true that PUCCH for SPS can carry S R if they overlap in time. Adding “and SR, if any” would be simple solution for this problem.

**FL recommendation: Adopt above TP for TS 38.213 Clause 9.2.1**

**Comment:**

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| --- | --- |
| Company | Comment |
| Samsung | Support the proposal. |
| CATT | Fine with the TP. |
| Nokia, NSB | Support the TP / proposal |
| OPPO | Support the proposal. |
| HW/HiSi | Support the TP |
| ZTE | Already covered by specification, no need to change.  See 38.213 g40 section 9.2.5.1:  If a UE would transmit a PUCCH with  HARQ-ACK information bits in a resource using PUCCH format 2 or PUCCH format 3 or PUCCH format 4 in a slot, as described in Clauses 9.2.1 and 9.2.3,  bits representing a negative or positive SR, in ascending order of the values of *schedulingRequestResourceId* anda *schedulingRequestResourceId* associated with *schedulingRequestID-BFR-SCell*, are appended to the HARQ-ACK information bits and the UE transmits the combined  UCI bits in a PUCCH using a resource with PUCCH format 2 or PUCCH format 3 or PUCCH format 4 that the UE determines as described in Clauses 9.2.1 and 9.2.3. If one of the SRs is a positive LRR, the value of the  bits indicates the positive LRR. An all-zero value for the  bits represents a negative SR value across all SRs. |
| Qualcomm | Fine with the TP.  To ZTE: The spec cited above does explain that UE picks up a PUCCH resource as described in 9.2.1. However, the TP is still needed for the UE to select a PUCCH resource based on the **total payload** of HARQ-ACK +SR, which is the same principle as the procedure in 9.2.3. Without the TP, the current spec implies that, the UE determines the PUCCH resource only based on the SPS HARQ-ACK payload, which is not correct. |
| Apple | The TP is okay. |

# Final outcome from [102-e-NR-L1enh-URLLC-IIoTenh-01]

# Text proposals

From the discussion in the email thread, following has been agreed:

# References

1. R1-2100899 Remaining issues of other aspects for URLLC/IIOT LG Electronics
2. R1-2101178 Maintanence on SPS PDSCH Samsung
3. R1-2101179 Draft CR on SPS release for PDSCH with aggregation Samsung
4. R1-210xxxx, Summary on Others for URLLC and IIOT, Moderator (LG Electronics)