**3GPP TSG RAN WG1 #102-e R1-200xxxx**

**E-Meeting, 17th** **August – 28th August, 2020**

Agenda: 7.2.11

Source: Moderator (Samsung)

Title: Discussions on [102-e-NR-UEFeature-LS] on RAN1 reply to interpretation of the support of TMPI(s) for lower configurations in mode-2 operation

**Document for:** **Discussion and Decision**

# **Introduction**

RAN1 received a LS [1] from RAN2 on Rel-16 RAN1 UE feature lists for NR. In the LS, RAN2 request to RAN1 that clarification on topic 2.1 as below:

**2.1 Interpretation of the support of TMPI(s) for lower configurations in mode-2 operation**

RAN2 wonders whether the UL full power mode-2 supporting TPMIs for the lower configuration of coherency/port config can be deduced from the reported set of TPMIs, or does the UE need to explicitly report supported TPMIs for each coherency/port config the UE can support as part of it’s capability?

During preparation phase of RAN1 #102-e LSs, companies agreed to discuss this issue in the current 7.2.11 agenda [3]. This summary is to collect companies’ views on above question and potential draft LS to RAN2.

# **Discussion**

Please provide your input to questions below.

**Question**: Regarding the question for clarification in [1], what is your understanding?

* **Interpretation#1)** the UL full power mode-2 supporting TPMIs for the lower configuration of coherency/port config can be deduced from the reported set of TPMIs
* **Interpretation#2)** the UE need to explicitly report supported TPMIs for each coherency/port config the UE can support as part of it’s capability

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| Company | Comments |
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# **Conclusion**

[TBD]

# **References**

1. R1-2004936 (R2-2006030), Reply LS on Rel-16 UE feature lists, Intel
2. RAN1#102-e preparation phase on LSs, 3GPP TSG RAN1 Chairman, [link](https://www.3gpp.org/ftp/tsg_ran/WG1_RL1/TSGR1_102-e/Inbox/drafts/5/draft%20R1-200xxxx%20RAN1%23102-e%20preparation%20phase%20on%20LSs%20v015.zip)