**3GPP TSG-RAN WG1 #102-e R1-200xxxx**

**e-Meeting, Aug 17- Aug 28, 2020**

**Source: Moderator (Ericsson)**

**Title: Email discussion [102-e-NR-MRDC-CA-Dormancy-02]**

**Agenda item:** **7.2.10**

**Document for:** **Discussion and Decision**

# 1 Introduction

This document provides summary of email discussion [102-e-NR-MRDC-CA-Dormancy-02]on following issues discussed during preparation phase of RAN1#102-eMeeting

Below are the topics identified in [R1-2006995](file:///C:\Users\wanshic\OneDrive%20-%20Qualcomm\Documents\Standards\3GPP%20Standards\Meeting%20Documents\TSGR1_102\Docs\R1-2006995.zip) [16]

[102-e-NR-MRDC-CA-Dormancy-02] Email discussion/approval of the following from [R1-2006995](file:///C:\Users\wanshic\OneDrive%20-%20Qualcomm\Documents\Standards\3GPP%20Standards\Meeting%20Documents\TSGR1_102\Docs\R1-2006995.zip) until 8/21; if necessary, endorse remaining TPs by 8/27 – Ravi (Ericsson)

* Topic 2-1: Starting point for bwpInactivityTimer for an SCell when DCI format 2\_6 indicates dormant to non-dormant BWP switch for that SCell – [1]
* Topic 2-2: Handling SCell dormancy indication bits in DCI format 2\_6 when wake-up bit=0 – [1], [6]
* Topic 2-3: UE ignores dormancy indication in DCI format 2\_6 if it is too close to on duration – [5]
* Topic 2-4: Clarifications related to “BWP indicator field” not allowed to indicate a dormant BWP when detected in SCell DCI formats (including 0\_1, 0\_2) – [5],[6],[14]
* Topic 2-5: RRC parameter name alignment – [2], [7], [10], [12]

# 2. Discussion

### 2.1 Topic 2-1

Please provide your input to below question Q1 on this topic, preferably by 08/19 (evening PST).

#### Question 1

Q1. Is it OK to agree to below proposal from section 2.1 of [R1-2005359](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005359.zip)?

* Proposal : In the case that the time gap between the last monitoring occasion of DCI 2\_6 and the start of DRX ON is larger than SCell dormancy/non-dormancy switching time,
  + The starting point of BWP switching time and bwpInactivityTimer are n slots prior to DRX ON, where n is the BWP switching time of SCells.
  + Discuss further TP (if any) to clarify this

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Yes/No** | **Comments (Topic 2-1, Q1)** |
| Samsung | Yes |  |
| Intel | Yes |  |

### 2.2 Topic 2-2

Please provide your input to below question Q1 on this topic, preferably by 08/19 (evening PST).

#### Question 1

Q1. Do you think additional spec change is needed to handle the SCell dormancy indication bits in DCI format 2\_6 when wake-up bit=0 ?

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Yes/No** | **Comments (Topic 2-2, Q1)** |
| Samsung | Yes | We prefer to preclude meaningless code point. For example, when wake-up bit = 0, the SCell dormancy indication bits should not indicate the non-dormant BWP for a SCell. |
| Intel | Yes | Agree with Samsung comments |

### 2.3 Topic 2-3

Please provide your input to below question Q1 on this topic, preferably by 08/19 (evening PST).

#### Question 1

Q1. Do you think additional spec change is needed to handle the case when the time gap between the last monitoring occasion of DCI 2\_6 and the start of DRX ON is smaller than SCell dormancy/non-dormancy switching time ?

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Yes/No** | **Comments (Topic 2-3, Q1)** |
| Samsung | Yes | To determine the valid monitoring occasions for DCI format 2\_6, only the minimum time gap X (specified for minimum preparation period before DRX on duration) is considered in the current spec. We think SCell dormancy/non-dormancy switching delay should be considered together with time gap X to determine the valid occasions for 2\_6. |
| Intel | Yes | A valid occasion of DCI 2\_6 should allow enough switching time before the start of DRX ON |

### 2.4 Topic 2-4

Please provide your input to below question Q1 on this topic, preferably by 08/19 (evening PST).

#### Question 1

Q1. Do you think additional spec change is needed to clarify that “BWP indicator field” is not allowed to indicate a dormant BWP when detected in SCell DCI formats (including 0\_1,0\_2) ?

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Yes/No** | **Comments (Topic 2-4, Q1)** |
| Samsung | Yes | Based on the conclusion made in RAN1#101-e, BWP indicator field still includes a code point for dormant DL BWP and a UE does not expect the BWP indicator field in DCI 1\_1, 1\_2 is set to the ID of dormant DL BWP.  For TDD, since the DL and UL BWPs are linked, the UL BWP indicator field in DCI format 0\_1, 0\_2 should not indicate the ID of UL BWP associated with the dormant DL BWP.  For FDD, since the DL and UL BWPs are not linked, the UL BWP indicator field in DCI format 0\_1, 0\_2 can indicate any of BWPs.  We prefer to capture above things in the spec for clarity of UE behavior. |
| Intel | Yes | It makes clear specification and avoids any confusion if we could capture the behavior in specification. |

### 2.5 Topic 2-5

Please provide your input to below questions Q1,Q2,Q3 on this topic, preferably by 08/19 (evening PST).

#### Question 1

Q1. Is it OK to agree to the following proposal for RRC parameter alignment from section 2 of [R1-2005421](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005421.zip)?

* Update the following RRC parameter names in TS38.213 to align with those defined in TS 38.331.

|  |  |
| --- | --- |
| RAN1（38.213 10.3） | RAN2（38.331） |
| *Scell-groups-for-dormancy-outside-active-time* | *dormancyGroupOutsideActiveTime* |
| *Scell-groups-for-dormancy-within-active-time* | *dormancyGroupWithinActiveTim* |
| *first-non-dormant-BWP-ID-for-DCI-outside-active-time* | *firstOutsideActiveTimeBWP-Id* |
| *first-non-dormant-BWP-ID-for-DCI-inside-active-time* | *firstWithinActiveTimeBWP-Id* |

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Yes/No** | **Comments (Topic 2-5, Q1)** |
| Samsung | Yes |  |
| Intel | Yes |  |

#### Question 2

Q2. Which option do you prefer for subclause 10.3 of TS 38.213?

* Option 1 : TP for TS 38.213 from Annex of [R1-2005958](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005958.zip)
* Option 2 : Text Proposal #2 for TS38.213 from section 2 of [R1-2006552](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006552.zip)

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Preferred Option(s)**  **If multiple, list most preferred first** | **Comments (Topic 2-5, Q2) including any possible alternate TP** |
| Samsung | Option 2 |  |
| Intel | Option 2 |  |

#### Question 3

Q3. Which option do you prefer for subclauses 7.3.1.1.2, 7.3.1.2.2, 7.3.1.3.7 for 38.212?

* Option 1a : TP for TS 38.212 from Annex of [R1-2005958](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005958.zip)
* Option 1b : Text Proposal1 for TS38.212 from section 5.2 of [R1-2006285](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006285.zip)
* Option 1c : Text Proposal #1 for TS38.212 from section 2 of [R1-2006552](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006552.zip)

Companies are requested to indicate their view about the above question in the Table below.

|  |  |  |
| --- | --- | --- |
| **Company Name** | **Preferred Option(s)**  **If multiple, list most preferred first** | **Comments (Topic 2-5, Q3) including any possible alternate TP** |
| Samsung | Option 1c |  |
| Intel | Option 1b or 1c | It seems the difference between 1b and 1c is the use of *dormancyGroupWithinActiveTime-r16* and *dormancyGroupWithinActiveTime.* A RRC parameter with or without a postfix for release are both used in some other place in the specification.  Which one is the better way? |

# 3 Conclusions

TBU

# 4 References

1. [R1-2005359](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005359.zip) Remaining issues on Scell dormancy like behavior vivo
2. [R1-2005421](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005421.zip) Remaining Issues of SCell Dormancy and Cross-carrier Scheduling ZTE
3. [R1-2005626](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005626.zip) Remaining issues on Rel-16 carrier aggregation MediaTek Inc.
4. [R1-2005665](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005665.zip) PDCCH location for SCell dormancy CATT
5. [R1-2005788](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005788.zip) Remaining issues on CA Huawei, HiSilicon
6. [R1-2005856](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005856.zip) Remaining issues on MR-DC & eCA Intel Corporation
7. [R1-2005958](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2005958.zip) TP on SCell dormancy for alignment NEC
8. [R1-2006035](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006035.zip) Remaining issues for Scell dormancy OPPO
9. [R1-2006123](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006123.zip) On maintenance of Scell dormancy and CCS with different SCSs Samsung
10. [R1-2006285](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006285.zip) Remaining issues on Multi-RAT Dual-Connectivity and Carrier Aggregation enhancements Spreadtrum Communications
11. [R1-2006430](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006430.zip) Remaining issues on Efficient CA design Nokia, Nokia Shanghai Bell
12. [R1-2006552](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006552.zip) Corrections for SCell Dormancy Sharp
13. [R1-2006663](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006663.zip) Maintenance for reduced latency Scell management for NR CA Ericsson
14. [R1-2006786](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_102-e/Docs/R1-2006786.zip) Remaining issues on SCell dormancy Qualcomm Incorporated
15. [R1-2001419](http://www.3gpp.org/ftp/TSG_RAN/WG1_RL1/TSGR1_100_e/Docs/R1-2001419.zip) Text proposals from email discussion [100e-NR-LTE\_NR\_DC\_CA\_enh-ScellDormancy-01] Ericsson
16. [R1-2006995](file:///C:\Users\wanshic\OneDrive%20-%20Qualcomm\Documents\Standards\3GPP%20Standards\Meeting%20Documents\TSGR1_102\Docs\R1-2006995.zip) Summary of efficient and low latency serving cell configuration/activation/setup, RAN1#102-e, August 2020.