3GPP TSG RAN WG1 #102 R1-200xxxx

e-Meeting, August 17th – 28th, 2020

**Agenda item: 7.2.10**

**Source: Moderator (Nokia)**

**Title:** **[102-e-NR-MRDC-CA-Cross-CC-Unaligned-CA] email discussion summary**

**Document for: Discussion and Decision**

# 1 Introduction

Pre-meeting email discussion summaries are recorded as follows:

* Cross-CC scheduling: R1-2006975
* Cross-CC A-CSI-RS triggering: [R1-2006974](file:///C%3A%5CUsers%5Cwanshic%5COneDrive%20-%20Qualcomm%5CDocuments%5CStandards%5C3GPP%20Standards%5CMeeting%20Documents%5CTSGR1_102%5CDocs%5CR1-2006974.zip)
* Unaligned CA: [FL\_summary\_DRAFT v6](https://www.3gpp.org/ftp/tsg_ran/WG1_RL1/TSGR1_102-e/Inbox/drafts/7.2.10/Unaligned%20CA/R1-20XXXX-FL%20summary%20on%20support%20of%20unaligned%20frame%20boundary%20for%20R16%20NR%20inter-band%20CA_v6_ZTE_Huawei.zip)

Email thread [102-e-NR-MRDC-CA-Cross-CC-Unaligned-CA] combines the discussion identified by the first two, while the third one did not trigger any activities for RAN1#102.This email thread is tasked work on the following topics:

* Cross-CC scheduling ([R1-2006975](file:///C%3A%5CUsers%5Cwanshic%5COneDrive%20-%20Qualcomm%5CDocuments%5CStandards%5C3GPP%20Standards%5CMeeting%20Documents%5CTSGR1_102%5CDocs%5CR1-2006975.zip)), Issues 1, 2, 4 and 5 to be considered while issue #3 is deferred to AI 7.2.11
	+ Issue #1: Scheduling DCI and a BWP change DCI in the same MO (Proposal 1 of ‘5360 and proposal 2 of ‘5421)
	+ Issue #2: DAI counting order for the DCI not scheduling PDSCH (Proposal 3 of ‘6123 and Proposal 1 of ‘6297 if Proposal 1 is not addressed in [102-e-NR-MRDC-CA-Dormancy-01])
	+ Issue #4: Reference SCS for dynamic grant overriding SPS PDSCH timeline (Proposal 5 of ‘6123)
	+ Issue# 5: TCI state for X-carrier scheduled PDSCH scheduled without a TCI field present (Proposal 5 of ‘6753)
	+ Note: Defer Issue #3 (Proposals 1-3 of ‘5626) to AI 7.2.11 and do not consider it in this email thread
* Cross-CC A-CSI-RS triggering ([R1-2006974](file:///C%3A%5CUsers%5Cwanshic%5COneDrive%20-%20Qualcomm%5CDocuments%5CStandards%5C3GPP%20Standards%5CMeeting%20Documents%5CTSGR1_102%5CDocs%5CR1-2006974.zip))
	+ Consider section 2.2 and proposal 3 of [[R1-2005360](file:///C%3A%5CUsers%5Cwanshic%5COneDrive%20-%20Qualcomm%5CDocuments%5CStandards%5C3GPP%20Standards%5CMeeting%20Documents%5CTSGR1_102%5CDocs%5CR1-2005360.zip)] and the related TP to TS38.214

in the following the issue numbering has been updated so that the Cross-CC scheduling issues are denoted as A-[issue#] while the A-CSI-RS triggering is denoted as B-1

# 2 Summary of issues addressed in the Tdocs

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| **Issue #** | **Description** | **Source** |
| A-1 | Proposal 1: If a DCI indicates BWP change for a scheduled cell, there is no other DCI for that cell in the same PDCCH monitoring occasion (i.e., only one DCI – the BWP changing DCI – for one scheduled cell is allowed in a monitoring occasion). | vivo [1] |
| Proposal 2: If more than one DCI is transmitted within the same MO with K1 indicating the same PUCCH slot, PDSCH starting time in addition to the existing MO index and Cell index is applied to determine the “last DCI” for PUCCH resource determination. | ZTE [2] |
| A-2 | Proposal #1: Adopt the following TP for TS38.213 spec to handle the DCI not scheduling PDSCH transmission in terms of determining DAI counting order.A value of the counter downlink assignment indicator (DAI) field in DCI formats denotes the accumulative number of {serving cell, PDCCH monitoring occasion}-pair(s) in which PDSCH reception(s) or SPS PDSCH release associated with the DCI formats is present up to the current serving cell and current PDCCH monitoring occasion, first if the UE indicates support for *PDSCH-Number-perMOperCell* in increasing order of the PDSCH reception starting time indicated in the DCI for the same {serving cell, PDCCH monitoring occasion} pair, second in ascending order of serving cell index, and then in ascending order of PDCCH monitoring occasion index $m$, where $m$ $0\leq m<M$. | LGE [5] |
| Proposal 3: If UE is configured with one serving cell in the DL and UE indicates to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell, T-DAI should be included in DCI format 1\_1. | Samsung [4] |
| A-4 | Proposal 5: The symbol duration for timeline for dynamic grant PDSCH overriding SPS PDSCH is based on the smallest SCS between the scheduling and scheduled cell. | Samsung [4] |
| A-5 | Based on current TS 38.214, it is unclear which TCI state is used for a PDSCH is cross carrier scheduled by a DCI without TCI field present and the offset between the DCI and the PDSCH is equal to or greater than the threshold.Proposal: fix the issue by limiting the Rel-15 behavior to “same carrier scheduling”. | ASUS [6] |
| B-1 | 1. RAN1#101 agreed on UE capability for beamswitchtiming for cross-carrier A-CSI-RS triggering. However the behavior was only clarified for the same numerology case. For X-numerology CSI-RS triggering the related UE behavior should also be updated based on the latest agreement to apply to different numerology cases.
2. The RRC parameter used in current TS 38.213 (*aperiodicTriggeringOffsetExt-r16*) is not aligned with that defined in TS 38.331: *aperiodicTriggeringOffset-r16*, which is updated together in the following TP.
 | vivo [7] |

# 3 Discussion on the scope of the RAN1#102

## #A-1 Scheduling DCI and a BWP change DCI in the same MO [1,2]

In this case, if one of the scheduling DCIs triggering BWP switching, there may be some problem. For example, if the DCI-1 indicates a BWP change of cell-1, it is not clear how to derive the DCI size of DCI-2, because the active BWP of DCI-2 is different from that of DCI-1. Another problem is how to determine the HARQ codebook if one of these DCIs scheduling the same cell indicates a BWP change, especially considering up to four DCIs can be detected in a monitoring occasion.

**Proposal 1**: If a DCI indicates BWP change for a scheduled cell, there is no other DCI for that cell in the same PDCCH monitoring occasion (i.e., only one DCI – the BWP changing DCI – for one scheduled cell is allowed in a monitoring occasion). [1]

**Proposal 2:** If more than one DCI is transmitted within the same MO with K1 indicating the same PUCCH slot, PDSCH starting time in addition to the existing MO index and Cell index is applied to determine the “last DCI” for PUCCH resource determination. [2]

**FL pre-meeting discussion conclusion in [8]:** Samsung view appears to essentially that the gNB does what the proposal #1 is suggesting to specify as an explicit restriction. I am suggesting to take the issue #1 (Proposal 1 of ‘5360 and proposal 2 of ‘5421) up in the RAN1#102e with the understanding that one possible outcome is that no explicit spec restriction is needed.

**Please add your company comments on the issue:**

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| **Company** | **Comments** |
| ZTE | This issue also exists for Rel-15. Thus, we don’t think we need to specify any restriction in Rel-16 and it can be handled by network implementation.With FG3-5b (copied below), UE is allowed to receive two unicast DL DCIs or UL DCIs in the same span/MO since Rel-15. If one of the two DCIs in the same MO is BWP switching DCI, then the same issue happens.*• Processing one unicast DCI scheduling DL and two unicast DCI scheduling UL per scheduled CC across this set of monitoring occasions for TDD**• Processing two unicast DCI scheduling DL and one unicast DCI scheduling UL per scheduled CC across this set of monitoring occasions for TDD* |
| Samsung | Agree with ZTE view. We do not think any specification support is needed. This should be handled by network implementation. |
| Intel | Agree with ZTE and Samsung. This issue can be leave to implementation |
| MTK | We prefer proposal 1. There should be only one DCI – the BWP changing DCI – for one scheduled cell is allowed in a monitoring occasion. |
| Nokia, NSB | Agree with ZTE, Samsung and Intel. Proposal #1 is in spirit acknowledging this, but then also goes beyond that and suggests to explicitly write this in the specification. We don’t see the need for spec change.  |
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## #A-2 DAI counting order for the DCI not scheduling PDSCH [4, 5]

**LGE [5]:**

For the support of cross-carrier scheduling with different numerology, it was agreed in RAN1#101-e that “the time of the first symbol of the PDSCH” is used to determine the DAI counting order (used for the HARQ-ACK bit ordering on Type-2 dynamic HARQ-ACK codebook) among the multiple DCIs, which are to schedule a same serving cell and transmitted in a same PDCCH MO. However, **with current TS38.213 specification, it is unclear how to determine the DAI counting order for the DCI not scheduling PDSCH transmission, for example, SPS PDSCH release DCI.**

One simple way to address this issue is to refer the PDSCH starting symbol timing indicated by the TDRA field in the DCI just for the purpose of determining DAI counting order, even though the PDSCH is not actually scheduled/transmitted. With this consideration, the part “the PDSCH reception starting time” in current spec can simply be updated as “the PDSCH reception starting time indicated in the DCI”.

**Proposal #1:** Adopt the following TP for TS38.213 spec to handle the DCI not scheduling PDSCH transmission in terms of determining DAI counting order.

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| A value of the counter downlink assignment indicator (DAI) field in DCI formats denotes the accumulative number of {serving cell, PDCCH monitoring occasion}-pair(s) in which PDSCH reception(s) or SPS PDSCH release associated with the DCI formats is present up to the current serving cell and current PDCCH monitoring occasion, first if the UE indicates support for *PDSCH-Number-perMOperCell* in increasing order of the PDSCH reception starting time indicated in the DCI for the same {serving cell, PDCCH monitoring occasion} pair, second in ascending order of serving cell index, and then in ascending order of PDCCH monitoring occasion index $m$, where $m$ $0\leq m<M$. |

**Samsung [4];**

Another issue regarding Type-2 HARQ-ACK codebook is the last DCI miss detection for UE indicating to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell. If UE is configured with one serving cell in the DL, the last DCI miss detection issue can be avoided/alleviated by introducing T-DAI in the DCI. If UE receives at least one DCI format including T-DAI in the last PDCCH MO, HARQ-ACK codebook size misalignment issue can be avoided by using T-DAI to determine the size of HARQ-ACK codebook. A similar issue was discussed under MIMO MRTP scenario where for MTRP joint HARQ-ACK feedback and UE is configured with one serving cell in the DL, UE can receive more than one DL DCI in a same PDCCH MO for a same serving cell. It has been agreed that if UE is configured with one serving cell in the DL, T-DAI is included in DCI format 1\_1/1\_2 for joint HARQ-ACK feedback. Same solution can be used for UE indicating to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell.

**Proposal 3:** If UE is configured with one serving cell in the DL and UE indicates to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell, T-DAI should be included in DCI format 1\_1. [TP2 in the Annex]

**FL pre-meeting discussion conclusion in [8]:** Take the issue #2 (Proposal 1 of ‘6297 and proposal 3 of ‘6123 up in the RAN1#102e.

**Please add your company comments on the issue:**

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| **Company** | **Comments** |
| ZTE | We have detailed analysis (copied below) on the first issue in our tdoc x5421. Based on our understanding, this issue can be handled by gNB implementation, we don’t need to specify anything for this.

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| As the PDSCH starting time is applied to order the HARQ-ACK feedback and to determine the “last DCI”, another remaining issue is how to handle the HARQ-ACK for SPS release and HARQ-ACK for dormancy indication, which don’t schedule PDSCH at all. It is well understood that the SPS release and dormancy indication are not frequently happened in the practical network. In this sense, it may be unnecessary to enhance this specifically for HARQ-ACK for SPS release and HARQ-ACK for dormancy indication, just similar as what we did in Rel-15 for SPS release. The order of HARQ-ACK feedback for SPS release and dormancy indication can be left to implementation, e.g., network can try to avoid transmitting other DCIs in the same MO with SPS release with K1 indicating the same PUCCH slot. Similarly, network can avoid the ambiguity of “last DCI” determination for SPS release and dormancy indication, e.g., indicating the same PRI value in the concerned DCIs.***Observation 1****: If more than one DCI including at least one DCI indicating SPS release or SCell dormancy is transmitted within the same MO with K1 indicating the same PUCCH slot, how to order the HARQ-ACK for SPS release or SCell dormancy indication and how to determine the “last DCI” can be left to network implementation.* |

Regarding the second issue, we understand that the miss detection issue of last DCI is a common issue for all the cases, which is not specific to >1 DCI in the same MO. Further, at the late stage of Rel-16, it may be too late to introduce thi`s enhancement especially considering that it may introduce spec impacts for both TS 38.211, TS 38.213 and UE feature. |
| Samsung | We support the first proposal. We think it is too much restriction if the issue is handled by just network implementation. We support the second proposal. We think using T-DAI is most generic functionality to handle the cases for the miss-detection of the last DCI. We beilive that this feature should be supported across all the features without any exceptions casuing the same issue. Therefore, we regard the second proposal as a correction for including a missing case not for introducing new functionality. |
| Intel | We think both proposals are kind of optimization, especially in this late stage. For the proposal 1 in [5], assuming DCI for SPS release is in slot n and K0>0 indicated by TDRA, is it the intention that HARQ-ACK is in slot n+K1, or n+K0+K1. If n+K1 is used, it means only part of TDRA information is used. On the other hand, I assume it should not be n+K0+K1 since it is a different feedback timing from legacy NR. For the proposal 3 in [4], I understand the technical benefit on robust codebook size. However, it also means fixed 2 more bits in DCI for one serving cell case. The potential benefit may not justify the overhead increase.  |
| MTK | We agree with Intel that Proposal #1 and Proposal #3 seem like optimizations. We are open to take majority view if majority companeis want the corresponding optimization. In the mean time, Proposal 3 may have a higher bar to be agreed since it adds 2 more bits in DCI as constant overhead to optimize the last DCI miss detection case. |
| Nokia, NSB | LGE proposal: We think this is an optimisation, but if solution for infrequent transmissions of PDSCH-less DCIs is deemed needed, we could place DL SPS release, TYPE-3 CB trigger and Dormancy in specification before or after DCI scheduling PDSCH.Samsung proposal: We think this is unnecessary optimisation at the late CR phase that adds to the DCI overhead when the system is not broken. For the purpose of cross-SCS and cross-carrier scheduling from FR1 to FR2, there is no issue.  |
| LG | We think the first proposal is necessary in terms of determining DAI counting for all the DCI types, and it does not require any additional burden/implementation to the UE since the UE just need to read TDRA in the PDSCH-less DCI as for the normal DCI scheduling PDSCH.In general, we think some rule for DAI counting between PDSCH-less DCI and normal scheduling DCI is needed, and in this context, the Nokia’s approach is also acceptable.Regarding Intel’s comment, we don’t propose to change HARQ-ACK timeline for PDSCH-less DCI, and we just want to keep the current timeline for HARQ-ACK.Our proposal is to just refer TDRA in terms of K0 and SILV, just to determine DAI counting value by the PDSCH starting time based on the K0 and SILV. |
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## #A-4 Reference SCS for dynamic grant overriding SPS PDSCH timeline [4]

In current Spec. the timeline for dynamic grant PDSCH overriding SPS PDSCH is fixed and defined as 14 symbols according to TS 38.214. In Rel-16, CCS with different numerologies between the scheduling cell and the scheduled cell is supported. With different OFDM symbols durations, it needs to be specified which SCS is considered for the 14 symbols.

**Proposal 5:** The symbol duration for timeline for dynamic grant PDSCH overriding SPS PDSCH is based on the smallest SCS between the scheduling and scheduled cell.

**FL pre-meeting discussion conclusion in [8]:** Take the issue #4 (Proposal 5 of ‘6123) up in the RAN1#102e

**Please add your company comments on the issue:**

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| **Company** | **Comments** |
| ZTE | Fine to clarify this issue. |
| Samsung | We support the proposal. |
| Intel | OK for clarification |
| ASUSTeK | Fine to adopt the TP |
| MTK | Support the proposal |
| Nokia, NSB | OK with the TP |
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## #A-5 TCI state for X-carrier scheduled PDSCH scheduled without a TCI field present [6]

Based on current TS 38.214, it is unclear which TCI state is used for a PDSCH is cross carrier scheduled by a DCI without TCI field present and the offset between the DCI and the PDSCH is equal to or greater than the threshold.

Proposal: fix the issue by limiting the Rel-15 behavior to “same carrier scheduling” with the TP below

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| 5.1.5 Antenna ports quasi co-location[unchanged part omitted]If a UE is configured with the higher layer parameter *tci-PresentInDCI* that is set as 'enabled'for the CORESET scheduling the PDSCH, the UE assumes that the TCI field is present in the DCI format 1\_1 of the PDCCH transmitted on the CORESET. If a UE is configured with the higher layer parameter *tci-PresentInDCI-ForFormat1\_2* for the CORESET scheduling the PDSCH, the UE assumes that the TCI field with a DCI field size indicated by *tci-PresentInDCI-ForFormat1\_2* is present in the DCI format 1\_2 of the PDCCH transmitted on the CORESET. If the PDSCH is scheduled by a DCI format not having the TCI field present, and the time offset between the reception of the DL DCI and the corresponding PDSCH of a serving cell is equal to or greater than a threshold *timeDurationForQCL* if applicable, where the threshold is based on reported UE capability [13, TS 38.306], for determining PDSCH antenna port quasi co-location, the UE assumes that the TCI state or the QCL assumption for the PDSCH is identical to the TCI state or QCL assumption whichever is applied for the CORESET used for the PDCCH transmission within the active BWP of the serving cell.  |

**FL pre-meeting discussion conclusion in [8]:** Take the issue 5 (Proposal 5 of ‘6753) up in the RAN1#102e with the understanding that there is no consensus if anything is needed and one possible outcome is that no spec change is taken.

**Please add your company comments on the issue:**

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| **Company** | **Comments** |
| ZTE | Fine to clarify this issue. |
| Samsung | OK to clarify. Actually, we think it is already clear that the above paragraph is applicable for same-carrier scheduling only since there exist other paragraphs with the conditioning cross-carrier scheduling. Although it is clear, we are OK have above TP just for clarity of text. |
| Intel | Ok to clarify the issue |
| ASUSTeK | Fine to adopt the TP to clarify the issue and avoid conflict between texts. Note that for the case offset between DCI and PDSCH is less than threshold, same cell scheduling is clearly identified in the text, even if there are other paragraphs for cross-carrier scheduling:„Independent of the configuration of *tci-PresentInDCI* and *tci-PresentInDCI-ForFormat1\_2* in RRC connected mode, if the offset between the reception of the DL DCI and the corresponding PDSCH is less than the threshold *timeDurationForQCL*, the UE may assume that the DM-RS ports of PDSCH of a serving cell are quasi co-located with the RS(s) with respect to the QCL parameter(s) used for PDCCH quasi co-location indication of the CORESET associated with a monitored search space with the lowest *controlResourceSetId* in the latest slot in which one or more CORESETs within the active BWP of the serving cell are monitored by the UE.“ |
| MTK | Fine to have the TP to clarify the text. |
| Nokia, NSB | OK with the TP. |
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## #B-1 A-CSI-RS triggering [7]

Section 2.2 of [1] makes the following point:

The following agreement was achieved last meeting. A new RRC parameter was added to control the newly defined beam switching ehaviour in Rel-16.

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| Agreements:* Introduce a new capability ehaviour for aperiodic CSI-RS triggering with beam switching timing (FG14-7 in UE features list)
	+ Followings are clarified for FG14-7 (beamSwitchTiming-r16) in the UE features list
		- Candidate values of beamSwitchTiming-r16 include {224, 336}
* An RRC configuration parameter is added to indicate the UE ehaviour for AP-CSI-RS beam switching in Rel-16
	+ When provided, the UE ehaviour agreed in Rel-16 TEI is performed, with beamSwitchTiming-r16 as input
	+ Otherwise, the UE ehaviour specified in Rel-15 is performed, with beamSwitchTiming as input
* Adopt following TP to TS38.214
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However, above ehaviour is only clarified for the same numerology case. For X-numerology CSI-RS triggering the related UE ehaviour should also be updated based on the latest agreement.

Moreover, the RRC parameter used in current TS 38.213 (*aperiodicTriggeringOffsetExt-r16*) is not aligned with that defined in TS 38.331: *aperiodicTriggeringOffset-r16*, which is updated together in the following TP.

[detailed TP to TS38.214 provided in [1] copied in the Annex]

*Proposal 3:* *Agree the TP above for clarification on default beam switching ehaviour and alignement with RRC spec.*

**FL pre-meeting discussion conclusion in [9]:** Consider section 2.2 and proposal 3 of [1] and the related TP to TS38.214 in the RAN1#102e email discussion

**Please add your company comments on the issue:**

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| **Company** | **Comments** |
| ZTE | Fine to clarify this issue. |
| Samsung | OK to clarify |
| Intel | OK for clarification |
| ASUSTeK | Fine to adopt the TP |
| MTK | Fine to have the TP |
| Nokia, NSB | OK with the TP |
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# References

1. R1-2005360 Remaining issues on MR-DC, vivo
2. R1-2005421 Remaining Issues of SCell Dormancy and Cross-carrier Scheduling, ZTE
3. R1-2005626 Remaining issues on Rel-16 carrier aggregation, MediaTek Inc.
4. R1-2006123 On maintenance of Scell dormancy and CCS with different SCSs, Samsung
5. R1-2006297 Remaining issue on cross-carrier scheduling with different numerology, LG Electronics
6. R1-2006753 Remaining issue for cross-carrier scheduling, ASUSTEK COMPUTER
7. R1-2005360 Remaining issues on MR-DC, vivo
8. R1-2006975 FL summary on cross-carrier scheduling with different numerology, Moderator (Nokia]
9. R1-2006974 FL summary on aperiodic CSI-RS triggering with different numerology between CSI-RS and triggering PDCCH, Moderator (Nokia)

# Annex – Proposals related to cross-carrier scheduling with different SCS

## [1] R1-2005360 Remaining issues on MR-DC, vivo

In the previous meeting, it has been agreed to enable a UE detecting more than one DCI for a cell in a monitoring occasion, and to count the PDSCH starting time in addition to the cell index for counter DAI in this case.

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| Agreement:* Introduce a new FG for Type2 HARQ-ACK codebook for >1 DL DCIs in same Monitoring Occasion as follows:
	+ Components:
		- For HARQ-ACK type 2 codebook: Usage of the PDSCH starting time in addition to the existing MO and Cell index to order the HARQ-ACK feedback.
	+ Pre-requisites: 3-1
	+ FDD/TDD separation N/A
	+ FR1/FR2 differentiation: N/A
	+ Type: Per UE
	+ Mandatory/Optional: Optional with capability signalling
	+ Note: The UE capability is introduced with following assumption:
		- Specification reflects that UE behavior is modified only for UEs supporting this capability.
		- UE behavior of a UE supporting this capability is different from UE behavior of a UE not supporting this capability only for following case:
			* Type-2 HARQ-ACK codebook when HARQ-ACK feedback in a codebook corresponds to more than one DL DCI for same scheduled cell in a MO of a scheduling cell.
* FFS: check if any update is needed in the related Pseudo codeand handling of PDSCH starting time for ordering with existing functionality for multi-TRP operation

Agreement:Adopt the following text proposal for Clause 9.1.3.1 of TS 38.213:A value of the counter downlink assignment indicator (DAI) field in DCI formats denotes the accumulative number of {serving cell, PDCCH monitoring occasion}-pair(s) in which PDSCH reception(s) or SPS PDSCH release associated with the DCI formats is present up to the current serving cell and current PDCCH monitoring occasion, first, if the UE indicated support for [NEW FG] in increasing order of the time of the first symbol of the PDSCH for the same {serving cell, PDCCH monitoring occasion} pair, second in ascending order of serving cell index, and then in ascending order of PDCCH monitoring occasion index, where. |

One example is shown in Figure 1, where a UE detects DCI-1 and DCI-2 in the same PDCCH monitoring occasion scheduling cell-1.



Figure 1 Examples of more than one DCI in a monitoring occasion

In this case, if one of the scheduling DCIs triggering BWP switching, there may be some problem. For example, if the DCI-1 in Figure 1 indicates a BWP change of cell-1, it is not clear how to derive the DCI size of DCI-2, because the active BWP of DCI-2 is different from that of DCI-1. Another problem is how to determine the HARQ codebook if one of these DCIs scheduling the same cell indicates a BWP change, especially considering up to four DCIs can be detected in a monitoring occasion.

There are some approaches to avoid these problems, i.e., by introducing some scheduling restrictions.

Alt.1: BWP switching is only allowed for the last DCI (in increasing order of the time of the first symbol of the PDSCH) of the scheduled cell in a monitoring occasion. In this case, gNB can guarantee that the DCI size remains unchanged among DCIs for a scheduled cell.

Alt.2: If a DCI indicates BWP change for a scheduled cell, there is no other DCI for that cell in the same PDCCH monitoring occasion (i.e., only one DCI – the BWP changing DCI – is allowed in a monitoring occasion).

We slightly prefer Alt.2 for its simplicity.

*Proposal 1:* *If a DCI indicates BWP change for a scheduled cell, there is no other DCI for that cell in the same PDCCH monitoring occasion (i.e., only one DCI – the BWP changing DCI – for one scheduled cell is allowed in a monitoring occasion).*

The associated TP is provided below:

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| 9.1.3.1 Type-2 HARQ-ACK codebook in physical uplink control channelA UE determines monitoring occasions for PDCCH with DCI format scheduling PDSCH receptions or SPS PDSCH release on an active DL BWP of a serving cell $c$, as described in Clause 10.1, and for which the UE transmits HARQ-ACK information in a same PUCCH in slot $n$ based on- PDSCH-to-HARQ\_feedback timing indicator field values for PUCCH transmission with HARQ-ACK information in slot $n$ in response to PDSCH receptions or SPS PDSCH release- slot offsets $K\_{0}$ [6, TS 38.214] provided by time domain resource assignment field in a DCI format scheduling PDSCH receptions or SPS PDSCH release and by *pdsch-AggregationFactor* or *RepNumR16*, when provided.The set of PDCCH monitoring occasions for a DCI format scheduling PDSCH receptions or SPS PDSCH release is defined as the union of PDCCH monitoring occasions across active DL BWPs of configured serving cells. PDCCH monitoring occasions are indexed in an ascending order of start time of the search space sets associated with a PDCCH monitoring occasion. The cardinality of the set of PDCCH monitoring occasions defines a total number $M$ of PDCCH monitoring occasions.A value of the counter downlink assignment indicator (DAI) field in DCI formats denotes the accumulative number of {serving cell, PDCCH monitoring occasion}-pair(s) in which PDSCH reception(s) or SPS PDSCH release associated with the DCI formats is present up to the current serving cell and current PDCCH monitoring occasion, first if the UE indicates support for *PDSCH-Number-perMOperCell* in increasing order of the PDSCH reception starting time for the same {serving cell, PDCCH monitoring occasion} pair, second in ascending order of serving cell index, and then in ascending order of PDCCH monitoring occasion index $m$, where $m$ $0\leq m<M$. If, for an active DL BWP of a serving cell, the UE is not provided *CORESETPoolIndex* or is provided *CORESETPoolIndex* with value 0 for one or more first CORESETs and is provided *CORESETPoolIndex* with value 1 for one or more second CORESETs, and is provided *ACKNACKFeedbackMode = JointFeedback*, the value of the counter DAI is in the order of the first CORESETs and then the second CORESETs for a same serving cell index and a same PDCCH monitoring occasion index. If a UE indicates support of *PDSCH-Number-perMOperCell*, in a PDCCH monitoring occasion for each scheduled cell, the UE is not required to detect more than one DCI format, if the DCI format indicating an active DL BWP change for that scheduled cell. |

*Proposal 2:* *Agree the TP above for TS 38.213 to clarify the UE behavior.*

## [2] R1-2005421 Remaining Issues of SCell Dormancy and Cross-carrier Scheduling, ZTE

During RAN1#101bis-e meeting, the following agreements and the corresponding draft TP were endorsed. It has been agreed that the PDSCH starting time is introduced to order the HARQ-ACK codebook when more than 1 DCIs are transmitted in the same MO and the corresponding HARQ-ACKs would be constructed in the same UL slot. In this clause, some remaining issues on this issue are further analysed.

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| Agreement:        Introduce a new FG for Type2 HARQ-ACK codebook for >1 DL DCIs in same Monitoring Occasion as follows:        Components:        For HARQ-ACK type 2 codebook: Usage of the PDSCH starting time in addition to the existing MO and Cell index to order the HARQ-ACK feedback.        Pre-requisites: 3-1        FDD/TDD separation N/A        FR1/FR2 differentiation: N/A        Type: Per UE        Mandatory/Optional: Optional with capability signalling        Note: The UE capability is introduced with following assumption:        Specification reflects that UE behavior is modified only for UEs supporting this capability.        UE behavior of a UE supporting this capability is different from UE behavior of a UE not supporting this capability only for following case:        Type-2 HARQ-ACK codebook when HARQ-ACK feedback in a codebook corresponds to more than one DL DCI for same scheduled cell in a MO of a scheduling cell.        FFS: check if any update is needed in the related Pseudo codeand handling of PDSCH starting time for ordering with existing functionality for multi-TRP operation Agreement:Adopt the following text proposal for Clause 9.1.3.1 of TS 38.213:A value of the counter downlink assignment indicator (DAI) field in DCI formats denotes the accumulative number of {serving cell, PDCCH monitoring occasion}-pair(s) in which PDSCH reception(s) or SPS PDSCH release associated with the DCI formats is presentup to the current serving cell and current PDCCH monitoring occasion, first, if the UE indicated support for [NEW FG] in increasing order of the time of the first symbol of the PDSCH for the same {serving cell, PDCCH monitoring occasion} pair, second in ascending order of serving cell index, and then in ascending order of PDCCH monitoring occasion index, where. |

In Rel-15, the MO index and Cell index are applied to order the HARQ-ACK feedback and also applied to order the DCIs to determine the “last DCI” to determine the PUCCH resource index. According to the above agreements, the PDSCH starting time in addition to the existing MO index and Cell index is applied to order the HARQ-ACK feedback, then in this case, more than one DCI can be transmitted with the same MO with K1 indicting the same PUCCH slot. However, if the DSCH starting time in addition to the existing MO index and Cell index is not applied to determine the “last DCI”, then network has to indicate the same PRI in these different DCIs in this MO. This kind of restriction on network implementation has no justification. Thus, it is preferred that the PDSCH starting time in addition to the existing MO index and Cell index can also be applied to determine the “last DCI”.

***Proposal 2****: If more than one DCI is transmitted within the same MO with K1 indicating the same PUCCH slot, PDSCH starting time in addition to the existing MO index and Cell index is applied to determine the “last DCI” for PUCCH resource determination. (Note: The corresponding TP can be found in TP2 below.)*

***TP2****: {38.213, 9.1.3.1 Type-2 HARQ-ACK codebook in physical uplink control channel}*

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| For a PUCCH transmission with HARQ-ACK information, a UE determines a PUCCH resource after determining a set of PUCCH resources for  HARQ-ACK information bits, as described in Clause 9.2.1. The PUCCH resource determination is based on a PUCCH resource indicator field [5, TS 38.212], if present, in a last DCI format, among the DCI formats that have a value of a PDSCH-to-HARQ\_feedback timing indicator field, if present, or a value of *dl-DataToUL-ACK*, or a value of *dl-DataToUL-ACKForDCIFormat1\_2* for DCI format 1\_2, indicating a same slot for the PUCCH transmission, that the UE detects and for which the UE transmits corresponding HARQ-ACK information in the PUCCH where, for PUCCH resource determination, detected DCI formats are first indexed in increasing order of the PDSCH reception starting time for the same serving cell and PDCCH monitoring occasion if the UE indicates support for *PDSCH-Number-perMOperCell*, second in an ascending order across serving cells indexes for a same PDCCH monitoring occasion and are then indexed in an ascending order across PDCCH monitoring occasion indexes. For indexing DCI formats within a serving cell for a same PDCCH monitoring occasion, if the UE is not provided *CORESETPoolIndex* or is provided *CORESETPoolIndex* with value 0 for one or more first CORESETs and is provided *CORESETPoolIndex* with value 1 for one or more second CORESETs on an active DL BWP of a serving cell, and with *ACKNACKFeedbackMode* = *JointFeedback* for the active UL BWP, detected DCI formats from PDCCH receptions in the first CORESETs are indexed prior to detected DCI formats from PDCCH receptions in the second CORESETs. |

As the PDSCH starting time is applied to order the HARQ-ACK feedback and to determine the “last DCI”, another remaining issue is how to handle the HARQ-ACK for SPS release and HARQ-ACK for dormancy indication, which don’t schedule PDSCH at all. It is well understood that the SPS release and dormancy indication are not frequently happened in the practical network. In this sense, it may be unnecessary to enhance this specifically for HARQ-ACK for SPS release and HARQ-ACK for dormancy indication, just similar as what we did in Rel-15 for SPS release. The order of HARQ-ACK feedback for SPS release and dormancy indication can be left to implementation, e.g., network can try to avoid transmitting other DCIs in the same MO with SPS release with K1 indicating the same PUCCH slot. Similarly, network can avoid the ambiguity of “last DCI” determination for SPS release and dormancy indication, e.g., indicating the same PRI value in the concerned DCIs.

***Observation 1****: If more than one DCI including at least one DCI indicating SPS release or SCell dormancy is transmitted within the same MO with K1 indicating the same PUCCH slot, how to order the HARQ-ACK for SPS release or SCell dormancy indication and how to determine the “last DCI” can be left to network implementation.*

## [3] R1-2005626 Remaining issues on Rel-16 carrier aggregation, MediaTek Inc.

In the latest RAN1 UE feature list [R1-2005110], the following UE capabilities structure for DL/UL cross carrier scheduling is applied:



It can be seen that the enhanced DCI processing capability (component 2 highlighted in yellow) is still in bracket and not stable yet. Since FG 18-5 and FG 18-5b are basic features for cross-carrier scheduling, and with the late state in Rel-16, we do not think the enhanced DCI processing capability should be included inside the basic features.

**Proposal 1: Remove Component 2 from FG 18-5 and FG 18-5b.**

In the latest RAN1 UE feature list [R1-2005110], a new UE feature FG 18-9 “**Type2 HARQ-ACK codebook for >1 DL DCIs in same Monitoring Occasion**” is defined in RAN1 #101e [Chairman’s Notes, RAN1 #101e] as shown below:



To our understanding, in the feature group description, the “**DL DCI**” should be modified to be “**unicast DL DCI**”.

During RAN1 online email discussion in RAN1 #101e, the original FL proposal is:

* **Feature lead proposal: introduce a resolution based on option 1:**
	+ If the maximum number of unicast DCIs per MO per scheduled cell is increased to larger than one, the PDSCH starting time in addition to the existing MO and Cell index is introduced to order the HARQ-ACK feedback.
	+ Introduce separate UE capability as being discussed under the UE feature session

After a lengthy discussion, the “unicast” is lost due to no reason. Thus, the DCI mentioned in FG 18-9 here should be “unicast” DCI.

**Proposal 2: In FG 18-9 “Type2 HARQ-ACK codebook for >1 DL DCIs in same Monitoring Occasion”, modify “DL DCI” to be “unicast DL DCI” for clarification.**

In the latest RAN1 UE feature list [R1-2005110], the candidate values for FG 18-5a and FG 18-6a are still FFS:



We thus have the following proposal:

**Proposal 3: For FG 18-5a and FG 18-6a, the candidate value should be {same numerology, different numerology, both}.**

## [4] R1-2006123 On maintenance of Scell dormancy and CCS with different SCSs, Samsung

Another issue regarding Type-2 HARQ-ACK codebook is the last DCI miss detection for UE indicating to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell. If UE is configured with one serving cell in the DL, the last DCI miss detection issue can be avoided/alleviated by introducing T-DAI in the DCI. If UE receives at least one DCI format including T-DAI in the last PDCCH MO, HARQ-ACK codebook size misalignment issue can be avoided by using T-DAI to determine the size of HARQ-ACK codebook. A similar issue was discussed under MIMO MRTP scenario where for MTRP joint HARQ-ACK feedback and UE is configured with one serving cell in the DL, UE can receive more than one DL DCI in a same PDCCH MO for a same serving cell. It has been agreed that if UE is configured with one serving cell in the DL, T-DAI is included in DCI format 1\_1/1\_2 for joint HARQ-ACK feedback. Same solution can be used for UE indicating to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell.

**Proposal 3: If UE is configured with one serving cell in the DL and UE indicates to support receiving more than one DL DCI in a same PDCCH MO for a same serving cell, T-DAI should be included in DCI format 1\_1.**

**Proposed TP2 for 38.212 Section 7.3.1.2.2.**

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| 7.3.1.2.2 Format 1\_1…- Downlink assignment index – number of bits as defined in the following- 6 bits if more than one serving cell are configured in the DL and the higher layer parameter *NFI-TotalDAI-Included-r16 = enable*. The 4 MSB bits are the counter DAI and the total DAI for the scheduled PDSCH group, and the 2 LSB bits are the total DAI for the non-scheduled PDSCH group.- 4 bits if only one serving cell are configured in the DL and the higher layer parameter *NFI-TotalDAI-Included-r16 = enable.* The 2 MSB bits are the counter DAI for the scheduled PDSCH group, and the 2 LSB bits are the total DAI for the non-scheduled PDSCH group;- 4 bits if more than one serving cell are configured in the DL, the higher layer parameter *pdsch-HARQ-ACK-Codebook=dynamic* or *pdsch-HARQ-ACK-Codebook=enhancedDynamic-r16*, and *NFI-TotalDAI-Included-r16* is not configured, where the 2 MSB bits are the counter DAI and the 2 LSB bits are the total DAI;- 4 bits if one serving cell is configured in the DL, and the higher layer parameter *pdsch-HARQ-ACK-Codebook=dynamic*, and the UE is not provided *CORESETPoolIndex* or is provided *CORESETPoolIndex* with value 0 for one or more first CORESETs and is provided *CORESETPoolIndex* with value 1 for one or more second CORESETs, and is provided *ACKNACKFeedbackMode = JointFeedback*, where the 2 MSB bits are the counter DAI and the 2 LSB bits are the total DAI;- 4 bits if one serving cell is configured in the DL, and the higher layer parameter *pdsch-HARQ-ACK-Codebook=dynamic* or *pdsch-HARQ-ACK-Codebook=enhancedDynamic-r16*, and the UE indicates support for *PDSCH-Number-perMOperCell*, where the 2 MSB bits are the counter DAI and the 2 LSB bits are the total DAI;- 2 bits if only one serving cell is configured in the DL, the higher layer parameter *pdsch-HARQ-ACK-Codebook=dynamic* or *pdsch-HARQ-ACK-Codebook=enhancedDynamic-r16*, and *NFI-TotalDAI-Included-r16* is not configured, when the UE is not configured with *CORESETPoolIndex* or the value of *CORESETPoolIndex* is the same for all CORESETs if *CORESETPoolIndex* is provided or the UE is not configured with *ACKNACKFeedbackMode = JointFeedback*, where the 2 bits are the counter DAI;- 0 bits otherwise.  |

In current Spec. the timeline for dynamic grant PDSCH overriding SPS PDSCH is fixed and defined as 14 symbols according to TS 38.214. In Rel-16, CCS with different numerologies between the scheduling cell and the scheduled cell is supported. With different OFDM symbols durations, it needs to be specified which SCS is considered for the 14 symbols. One option is to consider the smallest SCS between the scheduling and scheduled cell to provide UE with the most time to drop processing of the SPS PDSCH, so we propose the TP below based on this. Alternatively, to reduce potential inefficiency with this option, an extra offset can be considered on top of the 14 symbols acknowledging extra PDCCH decoding delay on the scheduling cell.

**Proposal 5: The symbol duration for timeline for dynamic grant PDSCH overriding SPS PDSCH is based on the smallest SCS between the scheduling and scheduled cell.**

**Proposed TP4 for 38.214 Section 5.1**

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| The UE is not expected to decode a PDSCH scheduled in a serving cell with C-RNTI or MCS-C-RNTI and another PDSCH scheduled in the same serving cell with CS-RNTI if the PDSCHs partially or fully overlap in time except if the PDCCH scheduling the PDSCH with C-RNTI or MCS-C-RNTI ends at least 14 symbols before the start of the PDSCH with CS-RNTI without the corresponding DCI, where the symbol duration is based on the smallest numerology between the scheduling PDCCH and the PDSCH, in which case the UE shall decode the PDSCH scheduled with C-RNTI or MCS-C-RNTI. |

## [5] R1-2006297 Remaining issue on cross-carrier scheduling with different numerology, LG Electronics

For the support of cross-carrier scheduling with different numerology, it was agreed in RAN1#101-e that “the time of the first symbol of the PDSCH” is used to determine the DAI counting order (used for the HARQ-ACK bit ordering on Type-2 dynamic HARQ-ACK codebook) among the multiple DCIs, which are to schedule a same serving cell and transmitted in a same PDCCH MO. However, with current TS38.213 specification, it is unclear how to determine the DAI counting order for the DCI not scheduling PDSCH transmission, for example, SPS PDSCH release DCI.

One simple way to address this issue is to refer the PDSCH starting symbol timing indicated by the TDRA field in the DCI just for the purpose of determining DAI counting order, even though the PDSCH is not actually scheduled/transmitted. With this consideration, the part “the PDSCH reception starting time” in current spec can simply be updated as “the PDSCH reception starting time indicated in the DCI”.

**Proposal #1: Adopt the following TP for TS38.213 spec to handle the DCI not scheduling PDSCH transmission in terms of determining DAI counting order.**

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| A value of the counter downlink assignment indicator (DAI) field in DCI formats denotes the accumulative number of {serving cell, PDCCH monitoring occasion}-pair(s) in which PDSCH reception(s) or SPS PDSCH release associated with the DCI formats is present up to the current serving cell and current PDCCH monitoring occasion, first if the UE indicates support for *PDSCH-Number-perMOperCell* in increasing order of the PDSCH reception starting time indicated in the DCI for the same {serving cell, PDCCH monitoring occasion} pair, second in ascending order of serving cell index, and then in ascending order of PDCCH monitoring occasion index $m$, where $m$ $0\leq m<M$. |

## [6] R1-2006753 Remaining issue for cross-carrier scheduling, ASUSTEK COMPUTER

In [38.214], the following are used to derive the new default beam in Rel-16 for cross-carrier scheduling [38.2141]:

If the PDCCH carrying the scheduling DCI is received on one component carrier, and the PDSCH scheduled by that DCI is on another component carrier and the UE is configured with [*enableDefaultBeamForCCS*]:

- The *timeDurationForQCL* is determined based on the subcarrier spacing of the scheduled PDSCH. If µPDCCH < µPDSCH an additional timing delay is added to the *timeDurationForQCL*, where *d* is defined in 5.2.1.5.1a-1, otherwise *d* is zero;

- For both the cases, when the offset between the reception of the DL DCI and the corresponding PDSCH is less than the threshold *timeDurationForQCL,* and when the DL DCI does not have the TCI field present, the UE obtains its QCL assumption for the scheduled PDSCH from the activated TCI state with the lowest ID applicable to PDSCH in the active BWP of the scheduled cell.

In other words, as long as a PDSCH is cross carrier scheduled by a DCI without TCI field present, the new default beam , an activated TCI state with the lowest ID would be used for PDSCH reception, irrespective of the offset between the DCI and the PDSCH.

However, this situation is also covered by an old Rel-15 behavior, when TCI field is absent and the offset between the DCI and the PDSCH is equal to or greater than the threshold :

“ If the PDSCH is scheduled by a DCI format not having the TCI field present, and the time offset between the reception of the DL DCI and the corresponding PDSCH is equal to or greater than a threshold *timeDurationForQCL* if applicable, where the threshold is based on reported UE capability [13, TS 38.306], for determining PDSCH antenna port quasi co-location, the UE assumes that the TCI state or the QCL assumption for the PDSCH is identical to the TCI state or QCL assumption whichever is applied for the CORESET used for the PDCCH transmission.”

In other words, the above two paragraphs could cover a same case, i.e. “ a PDSCH is cross carrier scheduled by a DCI without TCI field present and the offset between the DCI and the PDSCH is equal to or greater than the threshold”, while different TCI states for the PDSCH are defined. Note that for Rel-15, a cross-carrier scheduling DCI shall comprise TCI state, so that the second paragraph does not cover the case of cross-carrier scheduling. While this restriction is removed in Rel-16, the confusion/conflict arises.

**Observation: Based on current TS 38.214, it is unclear which TCI state is used for a PDSCH** **a PDSCH is cross carrier scheduled by a DCI without TCI field present and the offset between the DCI and the PDSCH is equal to or greater than the threshold.**

Therefore, we propose to fix the issue with the following TP, by limiting the Rel-15 behavior to “same carrier scheduling”.

**Proposal: Adopt the following TP for Section 5.1.5 of TS 38.214**

\*\*\*\*\*\*\*\*\*\*\*Beginning of TP\*\*\*\*\*\*\*\*\*\*\*

5.1.5 Antenna ports quasi co-location

[unchanged part omitted]

If a UE is configured with the higher layer parameter *tci-PresentInDCI* that is set as 'enabled'for the CORESET scheduling the PDSCH, the UE assumes that the TCI field is present in the DCI format 1\_1 of the PDCCH transmitted on the CORESET. If a UE is configured with the higher layer parameter *tci-PresentInDCI-ForFormat1\_2* for the CORESET scheduling the PDSCH, the UE assumes that the TCI field with a DCI field size indicated by *tci-PresentInDCI-ForFormat1\_2* is present in the DCI format 1\_2 of the PDCCH transmitted on the CORESET. If the PDSCH is scheduled by a DCI format not having the TCI field present, and the time offset between the reception of the DL DCI and the corresponding PDSCH of a serving cell is equal to or greater than a threshold *timeDurationForQCL* if applicable, where the threshold is based on reported UE capability [13, TS 38.306], for determining PDSCH antenna port quasi co-location, the UE assumes that the TCI state or the QCL assumption for the PDSCH is identical to the TCI state or QCL assumption whichever is applied for the CORESET used for the PDCCH transmission within the active BWP of the serving cell.

[unchanged part omitted]

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*End of TP\*\*\*\*\*\*\*\*\*\*\*\*\*

## [7] R1-2005360 Remaining issues on MR-DC, vivo

The following agreement was achieved last meeting. A new RRC parameter was added to control the newly defined beam switching behavior in Rel-16.

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| Agreements:* Introduce a new capability signaling for aperiodic CSI-RS triggering with beam switching timing (FG14-7 in UE features list)
	+ Followings are clarified for FG14-7 (beamSwitchTiming-r16) in the UE features list
		- Candidate values of beamSwitchTiming-r16 include {224, 336}
* An RRC configuration parameter is added to indicate the UE behavior for AP-CSI-RS beam switching in Rel-16
	+ When provided, the UE behavior agreed in Rel-16 TEI is performed, with beamSwitchTiming-r16 as input
	+ Otherwise, the UE behavior specified in Rel-15 is performed, with beamSwitchTiming as input
* Adopt following TP to TS38.214
 |

However, above behavior is only clarified for the same numerology case. For X-numerology CSI-RS triggering the related UE behavior should also be updated based on the latest agreement.

Moreover, the RRC parameter used in current TS 38.213 (*aperiodicTriggeringOffsetExt-r16*) is not aligned with that defined in TS 38.331: *aperiodicTriggeringOffset-r16*, which is updated together in the following TP.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 5.2.1.5.1 Aperiodic CSI Reporting/Aperiodic CSI-RS when the triggering PDCCH and the CSI-RS have the same numerology<Unchanged part ommited>When aperiodic CSI-RS is used with aperiodic reporting, the CSI-RS offset is configured per resource set by the higher layer parameter *aperiodicTriggeringOffset* or *~~aperiodicTriggeringOffsetExt-r16~~ aperiodicTriggeringOffset-r16*. The CSI-RS triggering offset has the values of {0, 1, 2, 3, 4, 5, 6, …, 15, 16, 24} slots. If the UE is not configured with *minimumSchedulingOffsetK0* for any DL BWP or *minimumSchedulingOffsetK2* for any UL BWP and if all the associated trigger states do not have the higher layer parameter *qcl-Type* set to 'QCL-TypeD' in the corresponding TCI states , the CSI-RS triggering offset is fixed to zero. The aperiodic triggering offset of the CSI-IM follows offset of the associated NZP CSI-RS for channel measurement.<Unchanged part ommited>5.2.1.5.1a Aperiodic CSI Reporting/Aperiodic CSI-RS when the triggering PDCCH and the CSI-RS have different numerologiesWhen the triggering PDCCH and the triggered aperiodic CSI-RS are of different numerologies, the behavior defined in 5.2.1.5.1 for the case where the numerologies are the same applies with the following exceptions:Beam switch timing:-  If the scheduling offset between the last symbol of the PDCCH carrying the triggering DCI and the first symbol of the aperiodic CSI-RS resources in a *NZP-CSI-RS-ResourceSet* configured without higher layer parameter *trs-Info* is smaller than the UE reported threshold *beamSwitchTiming* + *d* $∙2^{μ\_{CSIRS}}/2^{μ\_{PDCCH}}$ in CSI-RS symbols*,* as defined in [13, TS 38.306], when the reported value is one of the values of {14, 28, 48} and*enableBeamSwitchTiming-r16*is not provided,, or is smaller than 48+ $d∙2^{μ\_{CSIRS}}/2^{μ\_{PDCCH}}$ in CSI-RS symbolswhen the reported value of *beamSwitchTiming-r16* is one of the values of {224, 336} and *enableBeamSwitchTiming-r16* is provided, where if the µPDCCH < µCSIRS, the beam switching timing delay *d* is defined in Table 5.2.1.5.1a-1, else *d* is zero- if one of the associated trigger states has the higher layer parameter *qcl-Type* set to 'QCL-TypeD',- if there is any other DL signal with an indicated TCI state in the same symbols as the CSI-RS, the UE applies the QCL assumption of the other DL signal also when receiving the aperiodic CSI-RS. The other DL signal refers to PDSCH scheduled with offset larger than or equal to the threshold *timeDurationForQCL,* as defined in [13, TS 38.306], aperiodic CSI-RS scheduled with offset larger than or equal to the UE reported threshold *beamSwitchTiming* + *d* $∙2^{μ\_{CSIRS}}/2^{μ\_{PDCCH}}$ in CSI-RS symbols when the reported value is one of the values {14,28,48} and*enableBeamSwitchTiming-r16*is not provided, aperiodic CSI-RS scheduled with offset larger than or equal to 48+ $d∙2^{μ\_{CSIRS}}/2^{μ\_{PDCCH}}$ in CSI-RS symbols when the reported value of *beamSwitchTiming-r16* is one of the values {224, 336} and *enableBeamSwitchTiming-r16* is provided, periodic CSI-RS, semi-persistent CSI-RS;- else,- if at least one CORESET is configured for the BWP in which the aperiodic CSI-RS is to be received, when receiving the aperiodic CSI-RS, the UE applies the QCL assumption used for the CORESET associated with a monitored search space with the lowest *controlResourceSetId* in the latest slot in which one or more CORESETs within the active BWP of the serving cell are monitored.- else if the UE is configured with [*enableDefaultBeamForCCS*], when receiving the aperiodic CSI-RS, the UE applies the QCL assumption of the lowest-ID activated TCI state applicable to the PDSCH within the active BWP of the cell in which the CSI-RS is to be received. - If the scheduling offset between the last symbol of the PDCCH carrying the triggering DCI and the first symbol of the aperiodic CSI-RS resources is equal to or greater than the UE reported threshold *beamSwitchTiming* + *d* $∙2^{μ\_{CSIRS}}/2^{μ\_{PDCCH}}$ in CSI-RS symbols, when the reported value is one of the values of {14,28,48}and*enableBeamSwitchTiming-r16*is not provided,or is equal to or greater than 48+$d∙2^{μ\_{CSIRS}}/2^{μ\_{PDCCH}}$ in CSI-RS symbols when the reported value of *beamSwitchTiming-r16* is one of the values of {224, 336} and *enableBeamSwitchTiming-r16* is provided where if the µPDCCH < µCSIRS, the beam switching timing delay *d* is defined in Table 5.2.1.5.1a-1, else *d* is zero, the UE is expected to apply the QCL assumptions in the indicated TCI states for the aperiodic CSI-RS resources in the CSI triggering state indicated by the CSI trigger field in DCI.Table 5.2.1.5.1a-1: Additional beam switching timing delay *d*

|  |  |
| --- | --- |
| ***µPDCCH*** | ***d* [PDCCH symbols]** |
| 0 | 8 |
| 1 | 8 |
| 2 | 14 |

Aperiodic CSI-RS timing:- When the aperiodic CSI-RS is used with aperiodic CSI reporting, the CSI-RS triggering offset *X* is configured per resource set by the higher layer parameter *aperiodicTriggeringOffset* or *~~aperiodicTriggeringOffsetExt-r16~~ aperiodicTriggeringOffset-r16,* including the case that the UE is not configured with *minimumSchedulingOffsetK0-r16* for any DL or UL BWP and all the associated trigger states do not have the higher layer parameter *qcl-Type* set to 'QCL-TypeD' in the corresponding TCI states. The CSI-RS triggering offset has the values of {0, 1, …, 31} slots when the µPDCCH < µCSIRS and {0, 1, 2, 3, 4, 5, 6, …, 15, 16, 24} when the µPDCCH > µCSIRS.. The aperiodic CSI-RS is transmitted in a slot , if UE is configured with ca-SlotOffset for at least one of the triggered and triggering cell, and *Ks* = , otherwise, and where*- n* is the slot containing the triggering DCI, *X* is the CSI-RS triggering offset in the numerology of CSI-RS according to the higher layer parameter *aperiodicTriggeringOffset* or *~~aperiodicTriggeringOffsetExt-r16~~ aperiodicTriggeringOffset-r16*,- $μ\_{CSIRS}$ and $μ\_{PDCCH}$ are the subcarrier spacing configurations for CSI-RS and PDCCH, respectively,- $N\_{slot, offset, PDCCH}^{CA}$ and $μ\_{offset,PDCCH}$are the$ N\_{slot, offset}^{CA}$ and the, respectively, which are determined by higher-layer configured ca-SlotOffset for the cell receiving the PDCCH respectively, $N\_{slot, offset, CSIRS}^{CA} $and  $μ\_{offset,CSIRS}$ are the$ N\_{slot, offset}^{CA}$ and the, respectively, which are determined by higher-layer configured ca-SlotOffset for the cell transmitting the CSI-RS respectively, as defined in [4, TS 38.211] clause 4.5- If the µPDCCH < µCSIRS, the UE is expected to be able to measure the aperiodic CSI RS, if the CSI-RS starts no earlier than the first symbol of the CSI-RS carrier's slot that starts at least *Ncsirs* PDCCH symbols after the end of the PDCCH triggering the aperiodic CSI-RS.- If the µPDCCH > µCSIRS, the UE is expected to be able to measure the aperiodic CSI RS, if the CSI-RS starts no earlier than at least *Ncsirs* PDCCH symbols after the end of the PDCCH triggering the aperiodic CSI-RS.Table 5.2.1.5.1a: *Ncsirs* as a function of the subcarrier spacing of the triggering PDCCH

|  |  |
| --- | --- |
| ***µPDCCH*** | ***Ncsirs* [symbols]** |
| 0 | 4 |
| 1 | 5 |
| 2 | 10 |
| 3 | [14] |

<Unchanged part ommited> |

*Proposal 3:* *Agree the TP above for clarification on default beam switching behavior and alignement with RRC spec.*