**3GPP TSG RAN WG1 Meeting #102-eR1-2007300**

**E-meeting, August 17–28, 2020**

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| *CR-Form-v12.0* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **36.213** | **CR** | **1354** | **rev** | **-** | **Current version:** | **V16.2.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network | **x** | Core Network |  |

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| ***Title:*** | Clarifications for PUR Repetition Adjustment | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Moderator (Sierra Wireless) | | | | | | | | | |
| ***Source to TSG:*** | R1 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | LTE\_eMTC5 -Core | | | | |  | ***Date:*** | | | 2020-08-26 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-16 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) Rel-12 (Release 12)* *Rel-13 (Release 13) Rel-14 (Release 14) Rel-15 (Release 15) Rel-16 (Release 16)* | |
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| ***Reason for change:*** | | For PUR, the currently specified procedure to update the PUR repetitions via an L1 ACK/Fallback DCI does not support updating of the RRC PUR configuration and does not match RAN2’s requirements. | | | | | | | | |
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| ***Summary of change:*** | | Implemented RAN2’s requirements:   * The adjustment on the PUSCH repetition number provided with L1 ACK / fallback indicator updates the repetition number configuration in PUR configuration in RRC layer. * The PHY layer provides a 3-bit repetition adjustment index to higher layers so that the value can be stored in the PUR configuration in RRC. * The format of the 3-bit information is same as RRC parameter numRepetitions-r16 | | | | | | | | |
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| ***Consequences if not approved:*** | | The L1 ACK/Fall DCI repetition adjustment does not upate the RRC PUR configuration and the RAN1 and RAN2 specification would not be aligned. | | | | | | | | |
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| ***Clauses affected:*** | | 8.0 and 9.1.5.3 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | |  | | |
| ***affected:*** | |  | **X** | Test specifications | | | |  | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

8.0 UE procedure for transmitting the physical uplink shared channel

**<Unchanged parts are omitted>**

For a PUSCH transmission using preconfigured uplink resource, the UE shall use the repetition number configured by higher layers.

**<Unchanged parts are omitted>**

9.1.5.3 Preconfigured Uplink Resource ACK/fallback procedure

If a UE has initiated a PUSCH transmission using preconfigured uplink resource on a given serving cell, and upon detection of a MPDCCH with DCI format 6-0A/6-0B with CRC scrambled by PUR C-RNTI intended for the UE within the PUR search space window as defined in Subclause 9.1.5, and the corresponding DCI is for PUR ACK/fallback indication (as defined in [4]):

- the UE shall deliver the PUR ACK/fallback indication, as signalled on the MPDCCH, to the higher layers, and

- the UE shall deliver to higher layers a 3-bit PUSCH repetition adjustment according to Table 8-2b for CEModeA or Table 8-2c for CEModeB as signalled on the MPDCCH, where a bit with a value of 0 shall be prepended to the DCI field if the DCI field has a size of 2 bits.

**<Unchanged parts are omitted>**