

<b>CHANGE REQUEST</b>		Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.
<b>25.212</b> <b>25.222</b>	<b>CR</b> <b>001</b>	Current Version: <b>v3.0.0</b>
GSM (AA.BB) or 3G (AA.BBB) specification number ↑	↑ CR number as allocated by MCC support team	
For submission to: <input style="width: 100px;" type="text"/> <i>list expected approval meeting # here</i> ↑	for approval <input checked="" type="checkbox"/> for information <input type="checkbox"/>	strategic <input type="checkbox"/> non-strategic <input type="checkbox"/> (for SMG use only)
Form: CR cover sheet, version 2 for 3GPP and SMG    The latest version of this form is available from: <a href="ftp://ftp.3gpp.org/Information/CR-Form-v2.doc">ftp://ftp.3gpp.org/Information/CR-Form-v2.doc</a>		

**Proposed change affects:** (U)SIM  ME  UTRAN / Radio  Core Network   
 (at least one should be marked with an X)

**Source:** Samsung and LGIC **Date:** 12 Oct 1999

**Subject:** Clarification of functionality

**Work item:** TS25.212 and TS25.222

<b>Category:</b>	F Correction <input checked="" type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input type="checkbox"/> D Editorial modification <input type="checkbox"/>	<b>Release:</b>	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
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(only one category shall be marked with an X)

**Reason for change:** The current description of TS25.212 and TS25.222 is not clear for treatment of the bits after rate matching. So, some additional description is required to avoid any misunderstanding and ambiguities.

**Clauses affected:** 4.2.7.3 of TS25.212 and 6.2.7.2 of TS25.222

<b>Other specs affected:</b>	Other 3G core specifications <input type="checkbox"/> Other GSM core specifications <input type="checkbox"/> MS test specifications <input type="checkbox"/> BSS test specifications <input type="checkbox"/> O&M specifications <input type="checkbox"/>	→ List of CRs: → List of CRs: → List of CRs: → List of CRs: → List of CRs:	
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**Other comments:**

<----- double-click here for help and instructions on how to create a CR.

Agenda Item:

**Source:** SAMSUNG Electronics Co. and LGIC

**Title:** Text proposal for Turbo codes and rate matching in TS 25.212, TS 25.222

**Document for:** Decision

## 1. Introduction

During the discussion on the WG1 reflector it turned out that some clarification regarding treatment of the bits after rate matching puncturing for Turbo codes is required mainly in the section rate matching to avoid any misunderstanding and ambiguities. This text proposal deals with this item which is relevant for both of TS 25.212 (FDD) and TS 25.222 (TDD). Also, some additional editorial corrections were included.

## 2. Text proposal for TS 25.212

----- Start text proposal -----

### 4.2.7.3. Bit separation [and bit collection](#) for rate matching

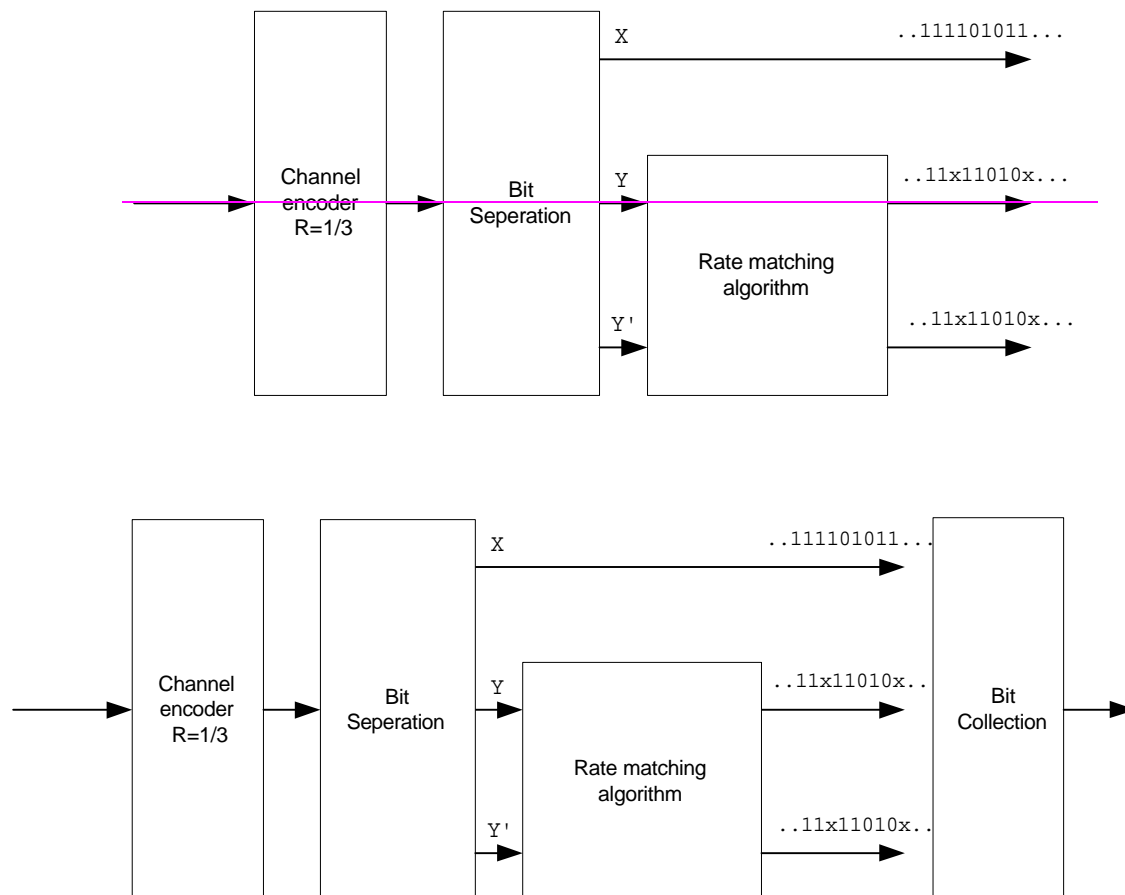


Figure 6: Overall rate matching block diagram before first interleaving where x denotes punctured bit.

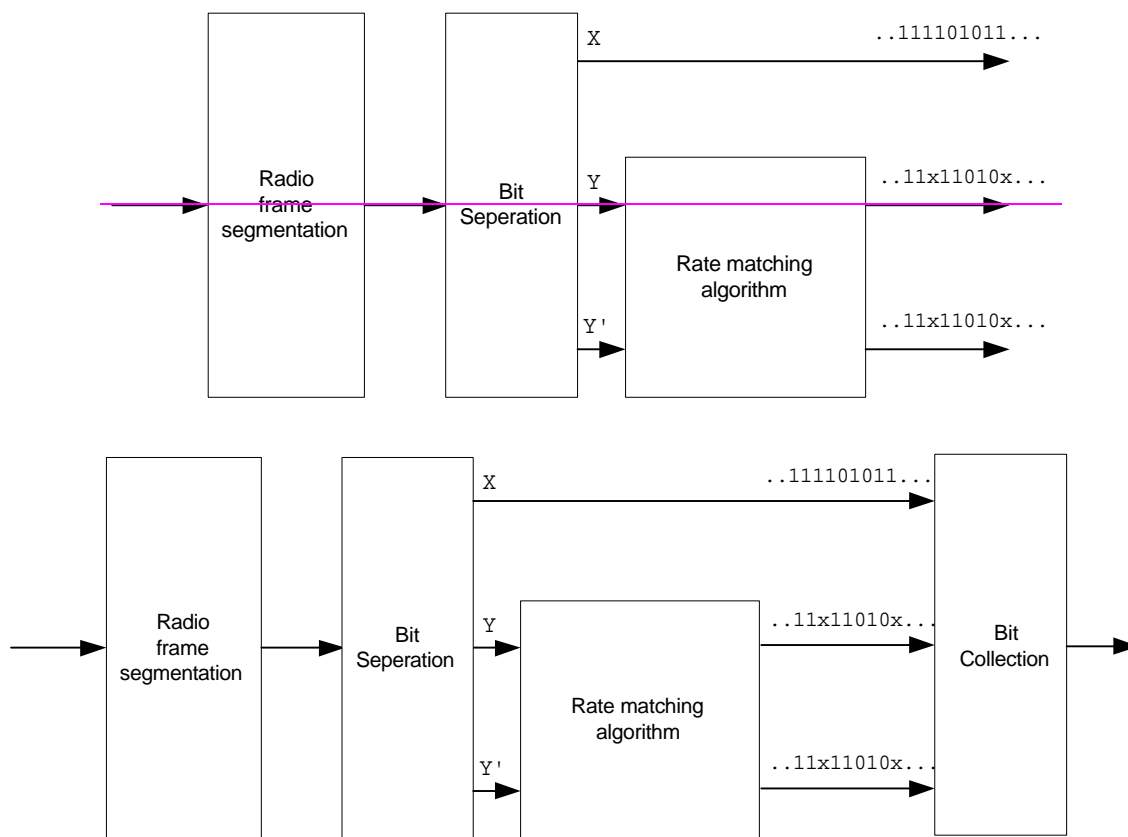


Figure 7: Overall rate matching block diagram after first interleaving where x denotes punctured bit.

Rate matching puncturing for Turbo codes in uplink is applied separately to  $Y$  and  $Y'$  sequences. No puncturing is applied to  $X$  sequence. Therefore, it is necessary to separate  $X$ ,  $Y$ , and  $Y'$  sequences before rate matching is applied.

For uplink, there are two different alternation patterns in bit stream from Radio frame segmentation according to the TTI of a TrCH as shown in Table 4.

Table 4: Alternation patterns of bits from radio frame segmentation in uplink

TTI (msec)	Alternation patterns
10, 40	... $X, Y, Y', \dots$
20, 80	... $X, Y', Y, \dots$

In addition, each radio frame of a TrCH starts with different initial parity type. Table 5 shows the initial parity type of each radio frame of a TrCH with TTI = {10, 20, 40, 80} msec.

Table 5: Initial parity type of radio frames of TrCH in uplink

TTI (msec)	Radio frame indexes ( $n_i$ )								
	0	1	2	3	4	5	6	7	
10	$X$	NA	NA	NA	NA	NA	NA	NA	NA
20	$X$	$Y$	NA	NA	NA	NA	NA	NA	NA
40	$X$	$Y'$	$Y$	$X$	NA	NA	NA	NA	NA
80	$X$	$Y$	$Y'$	$X$	$Y$	$Y'$	$X$	$Y$	

Table 4 and 5 defines a complete output bit pattern from Radio frame segmentation.

Ex. 1. TTI = 40 msec,  $n_i = 2$   
 Radio frame pattern: Y, Y', X, Y, Y', X, Y, Y', X, ...

Ex. 2. TTI = 40 msec,  $n_i = 3$   
 Radio frame pattern: X, Y, Y', X, Y, Y', X, Y, Y', X, ...

Therefore, bit separation is achieved with the alternative selection of bits with the initial parity type and alternation pattern specified in Table 4 and 5 according to the TTI and  $n_i$  of a TrCH.

Rate matching puncturing for Turbo codes in downlink is applied separately to Y and Y's sequences. No puncturing is applied to X sequence. Therefore, it is necessary to separate X, Y, and Y' sequences before rate matching is applied.

For downlink, output bit sequence pattern from Turbo encoder is always X, Y, Y', X, Y, Y', .... Therefore, bit separation is achieved with the alternative selection of bits from Turbo encoder.

After rate matching puncturing for Turbo codes, the separated sequences X, Y, and Y' are collected and delivered. For uplink, only not punctured bits in each radio frame of TrCH shall be delivered sequentially according to the order in Table 4 and Table 5 for each TTI of a TrCH.

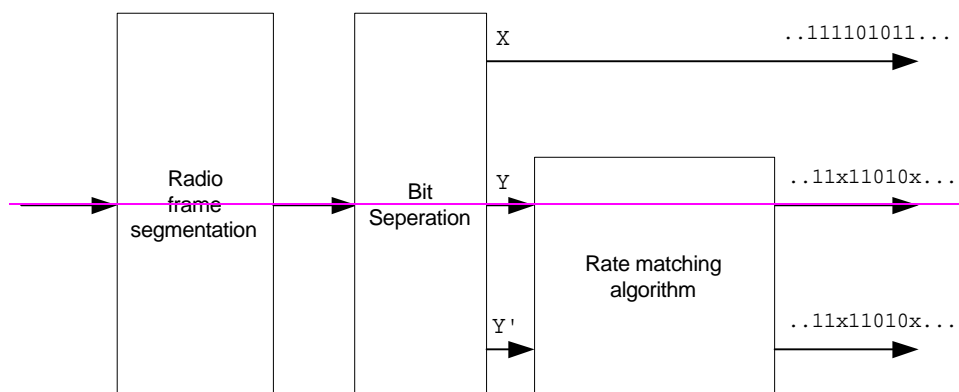
Ex. 1. TTI=40msec,  $n_i = 2$   
 If radio frame pattern: Y, Ψ, X, Y, Y', X, Ψ, Y', X, ... where Ψ and Ψ mean punctured bits.  
 Then delivered bits: Y, X, Y, Y', X, Y', X, ....

For downlink, only not punctured bits in a TTI of a TrCH shall be delivered sequentially in order of X, Y, Y', X, Y, Y',...etc.

### 3. Text proposal for TS 25.222

----- Start text proposal -----

#### 6.2.7.2. Bit separation and bit collection for rate matching



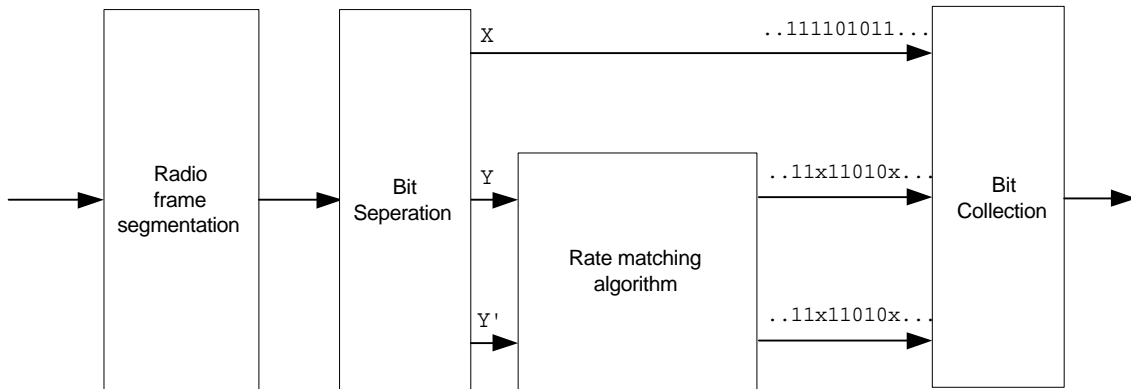


Figure 6-5. Overall rate matching block diagram after first interleaving where x denotes punctured bit.

Rate matching puncturing for Turbo codes is applied separately to  $Y$  and  $Y'$  sequences. No puncturing is applied to  $X$  sequence. Therefore, it is necessary to separate  $X$ ,  $Y$ , and  $Y'$  sequences before rate matching is applied.

There are two different alternation patterns in bit stream from Radio frame segmentation according to the TTI of a TrCH as shown in Table 6.2.7-1.

Table 6.2.7-1 Alternation patterns of bits from radio frame segmentation

TTI (msec)	Alternation patterns
10, 40	... $X, Y, Y', \dots$
20, 80	... $X, Y', Y, \dots$

In addition, each radio frame of a TrCH starts with different initial parity type. Table 6.2.7-2 shows the initial parity type of each radio frame of a TrCH with  $TTI = \{10, 20, 40, 80\}$  msec.

Table 6.2.7-2 Initial parity type of radio frames of TrCH

TTI (msec)	Radio frame indexes ( $n_i$ )							
	0	1	2	3	4	5	6	7
10	$X$	NA	NA	NA	NA	NA	NA	NA
20	$X$	$Y$	NA	NA	NA	NA	NA	NA
40	$X$	$Y'$	$Y$	$X$	NA	NA	NA	NA
80	$X$	$Y$	$Y'$	$X$	$Y$	$Y'$	$X$	$Y$

Table 6.2.7-1 and 6.2.7-2 defines a complete output bit pattern from Radio frame segmentation.

Ex. 1. TTI = 40 msec,  $n_i = 2$   
Radio frame pattern:  $Y, Y', X, Y, Y', X, Y, Y', X, \dots$

Ex. 2. TTI = 40 msec,  $n_i = 3$   
Radio frame pattern:  $X, Y, Y', X, Y, Y', X, Y, Y', X, \dots$

Therefore, bit separation is achieved with the alternative selection of bits with the initial parity type and alternation pattern specified in Table 6.2.7-1 and 6.2.7-2 according to the TTI and  $n_i$  of a TrCH.

After rate matching puncturing for Turbo codes, the separated sequences  $X$ ,  $Y$ , and  $Y'$  are collected and delivered. The only not punctured bits in each radio frame of TrCH shall be delivered sequentially according to the order in Table 6.2.7-1 and Table 6.2.7-2 for each TTI of a TrCH.

Ex. 1. TTI=40msec,  $n_i = 2$   
If radio frame pattern:  $Y, \text{✂}, X, Y, Y', X, \text{✂}, Y', X, \dots$  where  $\text{✂}$  and  $\text{✂}$  mean punctured bits.

Then delivered bits:  $Y, X, Y, Y', X, Y', X, \dots$

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## 4. References

- [1] TS 25.212 (V3.0.0): “Multiplexing and channel coding (FDD)”
- [2] TS 25.222 (V3.0.0): “Multiplexing and channel coding (TDD)”