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Source: TSG-RAN WG2

To: TSG-RAN WG1

Cc: TSG-RAN WG3

**Title: Response to LS (R1-001146) on TFCI in the case of invalid set of transport
             blocks and during DPCH synchronisation**

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TSG-RAN WG2 would like to thank TSG-RAN WG1 for the LS on TFCI in the case of invalid set of transport blocks and during DPCH synchronisation. TSG-RAN WG2 have discussed the two issues raised in the LS.

Invalid set of transport blocks delivered to Layer 1

TSG-RAN WG2 concluded that this is a rare case and having a special TFCI value for that case was not agreed according to the response sent by TSG-RAN WG3. Since, DTX of TFCI then should be used in the Node-B in case of invalid set of transport blocks, TSG-RAN WG2 do not see a need to modify anything in the TSG-RAN WG2 specifications related to this issue.

Initialisation of DPCH in FDD

TSG-RAN WG2 understood that TSG-RAN WG1 suggested two different alternatives that could be used in the initialization phase. If a zero rate TFCI is configured this can also be used in the initialization phase. However, if a zero rate TFCI is not present a special TFCI would be required instead.

Use of Special TFCI in TDD

It is also understood that a zero rate TFCI is used in TDD for the Special Burst [25.224 section 4.5]. Similar to the FDD initialization case if a zero rate TFCI is not present a special TFCI would also be required for the TDD Special Burst.

Conclusion on Special TFCI

To avoid having several solutions TSG-RAN WG2 propose to always use the special TFCI value in the initialization phase. For FDD this special TFCI will only be used during this phase and not afterwards. However, for TDD since the Special Burst is used during DTX, this special TFCI can also be used anytime during the physical channel connection.

TSG-RAN WG2 also see a need to reflect the use of this special TFCI in TSG-RAN WG2 specifications.

If TSG-RAN WG1 share the understanding of TSG-RAN WG2, TSG-RAN WG2 will do required changes to our specifications.