3GPP TSG RAN WG1#12 Seoul, Korea, 10-13 April 2000

Document R1-00-0533 e.g. for 3GPP use the format TP-99xxx or for SMG, use the format P-99-xxx

| | CHANC | SE REQUEST | Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly. |
|--|---|---|--|
| | 25.2 | 212 CR 067 | Current Version: 3.2.0 |
| GSM (AA.BB) or 3G (AA.BBB) specification number ↑ | | | |
| For submission to: RAN# for approval list expected approval meeting # here for information for information Sometimes are strategic for information for informa | | | |
| Proposed change affects: (U)SIM ME X UTRAN / Radio X Core Network (at least one should be marked with an X) | | | |
| Source: | Siemens | | <u>Date:</u> 7-April-2000 |
| Subject: Mapping of TFCI in uplink compressed mode | | | |
| Work item: | | | |
| Category: (only one category shall be marked with an X) F A C D | Correction Corresponds to a corre Addition of feature Functional modification Editorial modification | ection in an earlier releas | Release: Phase 2 Release 96 Release 97 Release 98 Release 99 Release 00 |
| Comment noted that CR25.212-048 corrected a fault in downlink compressed mode which also needs to be addressed in uplink compressed mode. | | | |
| Clauses affected: 4.3.5.2.1 | | | |
| affected: | Other 3G core specificati Other GSM core specifications MS test specifications BSS test specifications O&M specifications | ons \longrightarrow List of 0 \rightarrow List of 0 | CRs: CRs: |
| Other comments: | | | |

<----- double-click here for help and instructions on how to create a CR.

4.3.5.2.1 Uplink compressed mode

For uplink compressed mode, the slot format is changed so that no TFCI bits are lost. The different slot formats in compressed mode do not match the exact number of TFCI bits for all possible TGLs. Repetition of the TFCI bits is therefore used.

Denote the number of bits available in the TFCI fields of one compressed radio frame by D and the number of bits in the TFCI field in a slot by N_{TFCI} . Denote by E the first bit to be repeated, $E = N_{first}N_{TFCI}$.

$E = N_{first}N_{TFCI}$, if $N_{first} + TGL \le 15$, else E = 0

If the transmission gap does not extend to the end of the frame $N_{lass} \neq 14$, then E corresponds to the number of the first TFCI bit in the slot directly after the TG. The following relations then define the mapping.

 $d_k = b_{k \bmod 32}$

where k = 0, 1, 2, ..., min (31, D-1).

If D > 32, the remaining positions are filled by repetition (in reversed order):

 $d_{D-k-1} = b_{(E+k) \mod 32}$

where k = 0, ..., D-33.