

San Diego, CA, U.S.A., February 29 – March 3, 2000

Source : Nortel Networks
Title : Revision of downlink power control section in 25.214
Document for : Decision

1. Introduction

According to the decision of the RRM Ad Hoc, RAN WG1 was suggested to move the downlink power control for FDD to an informative annex. The rationale behind this decision was that RAN WG4 would set some tests to verify that the overall performance of the UE downlink power control is meeting operators' objectives. These tests will be based on a reference UE behaviour as modelled in RAN WG1 specification i.e. inner loop plus outer loop power control based on SIR estimation but the precise inner loop power control algorithm is left to the UE manufacturers.

The changes contained in the CR are the following :

- Annex B is renamed downlink power control to include power control timing as in the existing specification and additionally the description of the inner loop power control based on SIR estimation and the adjustment loop.
- section 5.2.1.2.1 : the general description if SIR-based inner loop power control is moved to Annex B.2 "Example of implementation in the UE". The way TPC commands are sent by the UE is kept because it is mandatory. DPC_MODE is kept as it also impacts the behaviour of the UE.
- section 5.2.1.2.2 : adjustment loop is moved to Annex B.3 as it impacts the node B behaviour which is considered as not needing full standardisation.

CHANGE REQUEST		Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.	
25.214	CR	080	Current Version: 3.1.1
GSM (AA.BB) or 3G (AA.BBB) specification number ↑		↑ CR number as allocated by MCC support team	
For submission to: TSG-RAN#7 <i>list expected approval meeting # here</i> ↑	for approval for information	<input checked="" type="checkbox"/> <input type="checkbox"/>	strategic <input type="checkbox"/> non-strategic <input type="checkbox"/> (for SMG use only)

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Proposed change affects: (U)SIM ME UTRAN / Radio Core Network
(at least one should be marked with an X)

Source: Nortel Networks **Date:** 2000-02-24

Subject: Downlink power control

Work item: _____

Category:	F Correction <input type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input type="checkbox"/> D Editorial modification <input checked="" type="checkbox"/>	Release:	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
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(only one category shall be marked with an X)

Reason for change: According to the decision of the RRM Ad Hoc downlink power control in the UE will not be specified in terms of inner loop + outer loop power control. The implementation of the power control is up to the UE manufacturers, inner loop and outer loop power control are given as an example of implementation.

Clauses affected: 5.2, Annex B

Other specs affected:	Other 3G core specifications <input type="checkbox"/> Other GSM core specifications <input type="checkbox"/> MS test specifications <input type="checkbox"/> BSS test specifications <input type="checkbox"/> O&M specifications <input type="checkbox"/>	→ List of CRs: → List of CRs: → List of CRs: → List of CRs: → List of CRs:	
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Other comments: _____



<----- double-click here for help and instructions on how to create a CR.

If $A_{C,j} \leq 1$, then $\mathbf{b}_{d,C,j} = \lceil A_{C,j} \rceil$ and $\mathbf{b}_{e,C,j} = 1.0$, where $\lceil \bullet \rceil$ means rounding to closest higher quantized β -value.

The quantized β -values is defined in TS 25.213 section 4.2.1, table 1.

Appropriate scaling of the output power shall be performed by the UE, so that the output DPCCH power follows the inner loop power control with power steps of $\pm\Delta_{\text{TPC}}$ dB ($\pm\Delta_{\text{RP-TPC}}$ dB during the recovery period) with an additional power offset during a compressed frame of $N_{\text{pilot,N}} / N_{\text{pilot,C}}$.

5.1.3 PCPCH

This section describes the power control procedures for the PCPCH. The CPCH access procedure is described in section 6.2.

5.1.3.1 Power control in the message part

The uplink inner-loop power control adjusts the UE transmit power in order to keep the received uplink signal-to-interference ratio (SIR) at a given SIR target, $\text{SIR}_{\text{target}}$, which is set by the higher layer outer loop.

The network should estimate the signal-to-interference ratio SIR_{est} of the received PCPCH. The network then generates TPC commands and transmits the commands once per slot according to the following rule: if $\text{SIR}_{\text{est}} > \text{SIR}_{\text{target}}$ then the TPC command to transmit is "0", while if $\text{SIR}_{\text{est}} < \text{SIR}_{\text{target}}$ then the TPC command to transmit is "1".

The UE derives a TPC command, TPC_{cmd} , for each slot. Two algorithms shall be supported by the UE for deriving a TPC_{cmd} , as described in subclauses 5.1.2.2.2.1 and 5.1.2.2.3.1. Which of these two algorithms is used is a higher-layer parameter under the control of the UTRAN.

The step size Δ_{TPC} is a higher-layer parameter under the control of the UTRAN, that can have the values 1 dB or 2 dB.

After deriving the TPC command TPC_{cmd} using one of the two supported algorithms, the UE shall adjust the transmit power of the uplink PCPCH with a step of Δ_{TPC} dB according to the TPC command. If TPC_{cmd} equals 1 then the transmit power of the uplink PCPCH shall be increased by Δ_{TPC} dB. If TPC_{cmd} equals -1 then the transmit power of the uplink PCPCH shall be decreased by Δ_{TPC} dB. If TPC_{cmd} equals 0 then the transmit power of the uplink PCPCH shall be unchanged.

Any power increase or decrease shall take place immediately before the start of the pilot field on the PCPCH control channel.

5.1.3.2 Power control in the power control preamble

The UE commences the power control preamble using the same power level as was used for the CD preamble.

The initial power control step size used in the power control preamble differs from that used in the message part: if inner loop power control algorithm 1 is to be used in the message part, then the initial step size in the power control preamble is $\Delta_{\text{TPC-init}}$, where $\Delta_{\text{TPC-init}}$ is equal to the minimum value out of 3 dB and $2\Delta_{\text{TPC}}$, where Δ_{TPC} is the power control step size used for the message part. If inner loop power control algorithm 2 is to be used in the message part, then inner loop power control algorithm 1 is used initially in the power control preamble, with a step size of 2dB. In either case, the power control algorithm and step size revert to those used for the message part as soon as the sign of the TPC commands reverses for the first time.

5.2 Downlink power control

The transmit power of the downlink channels is determined by the network. In general the ratio of the transmit power between different downlink channels is not specified and may change with time. However, regulations exist as described in the following sub-clauses.

5.2.1 DPCCH/DPDCH

5.2.1.1 General

The downlink transmit power control procedure controls simultaneously the power of a DPCCH and its corresponding DPDCHs. The power control loop adjusts the power of the DPCCH and DPDCHs with the same amount, i.e. the relative power difference between the DPCCH and DPDCHs is not changed.

The relative transmit power offset between DPCCH fields and DPDCHs is determined by the network. The TFCI, TPC and pilot fields of the DPCCH are offset relative to the DPDCHs power by PO1, PO2 and PO3 dB respectively. The power offsets may vary in time.

5.2.1.2 Ordinary transmit power control

5.2.1.2.1 General

~~The downlink inner loop power control adjusts the network transmit power in order to keep the received downlink SIR at a given SIR target, SIR_{target} . A higher layer outer loop adjusts SIR_{target} independently for each connection.~~

~~The UE should estimate the received downlink DPCCH/DPDCH power of the connection to be power controlled. Simultaneously, the UE should estimate the received interference. The obtained SIR estimate SIR_{est} is then used by the UE to generate TPC commands according to the following rule: if $SIR_{est} > SIR_{target}$ then the TPC command to transmit is "0", requesting a transmit power decrease, while if $SIR_{est} < SIR_{target}$ then the TPC command to transmit is "1", requesting a transmit power increase.~~

The UE shall generate TPC commands to control the network transmit power and send them in the TPC field of the uplink DPCCH. An example on how to derive the TPC commands is given in Annex B.2

When the UE is not in soft handover the TPC command generated is transmitted in the first available TPC field in the uplink DPCCH.

When the UE is in soft handover it should check the downlink power control mode (DPC_MODE) before generating the TPC command

- if DPC_MODE = 0 : the UE sends a unique TPC command in each slot and the TPC command generated is transmitted in the first available TPC field in the uplink DPCCH
- if DPC_MODE = 1 : the UE repeats the same TPC command over 3 slots and the new TPC command is transmitted such that there is a new command at the beginning of the frame.

The DPC_MODE parameter is a UE specific parameter controlled by the UTRAN.

As a response to the received TPC commands, UTRAN may adjust the downlink DPCCH/DPDCH power. The average power of transmitted DPDCH symbols over one timeslot shall not exceed Maximum_DL_Power(dBm), nor shall it be below Minimum_DL_Power (dBm). Transmitted DPDCH symbol means here a complex QPSK symbol before spreading which does not contain DTX.

NOTE: It should still be clarified whether Maximum_DL_Power and Minimum_DL_Power are defined for one code or for one CCTrCH

Changes of power shall be a multiple of the minimum step size $\Delta_{TPC,min}$ dB. It is mandatory for UTRAN to support $\Delta_{TPC,min}$ of 1 dB, while support of 0.5 dB is optional.

When ~~SIR measurements~~ TPC commands cannot be ~~performed~~ generated in the UE due to downlink out-of-synchronisation, the TPC command transmitted shall be set as "1" during the period of out-of-synchronisation.

5.2.1.2.2 Adjustment loop

~~UTRAN may further employ adjustment loop, in which they change their calculated transmission powers $P(i)$ in every slot according to the following equation:~~

$$P(i+1) = P(i) + S_{INNER}(i) + S_{ADJ}(i)$$

$$S_{ADJ}(i) = \text{sign}\{(1-r)(P_{REF} - P(i))\} \cdot \min\{|(1-r)(P_{REF} - P(i))|, S_{ADJ_MAX}\}$$

where

$P(i)$: calculated transmission power of UTRAN access point in dBm,

$S_{INNER}(i)$: inner loop control in dB,

$S_{ADJ}(i)$: adjustment loop control in dB,

$\text{sign}(x)$: sign function of the value x , i.e. +1 when $x > 0$, 0 when $x = 0$, and -1 when $x < 0$,

r : convergence coefficient ($0 \leq r \leq 1$),

P_{REF} : reference transmission power in dBm,

S_{ADJ_MAX} : maximum power change limit by adjustment loop in dB.

The actual change in the transmitted power level due to the adjustment loop is a value which is the nearest allowed TPC step to $S_{ADJ}(i)$. The parameters, r , P_{REF} , and S_{ADJ_MAX} shall be signalled by higher layers. S_{ADJ_MAX} shall be a multiple of the minimum step size TPC_{\min} dB.

5.2.1.32 Power control in compressed mode

The aim of downlink power control in uplink or/and downlink compressed mode is to recover as fast as possible a signal-to-interference ratio (SIR) close to the target SIR after each transmission gap.

The UE behaviour is the same in compressed mode as in normal mode, described in subclause 5.2.1.2, i.e. TPC commands should be generated based on the estimated received SIR.

The UTRAN behaviour during compressed mode is not specified. As an example, the algorithm can be similar to uplink power control in downlink compressed mode as described in sub-clause 5.1.2.3.

In downlink compressed mode or in simultaneous downlink and uplink compressed mode, the transmission of downlink DPCCH and DPDCH(s) is stopped.

5.2.1.43 Site selection diversity transmit power control

5.2.1.43.1 General

Site selection diversity transmit power control (SSDT) is an optional macro diversity method in soft handover mode.

Operation is summarised as follows. The UE selects one of the cells from its active set to be 'primary', all other cells are classed as 'non primary'. The main objective is to transmit on the downlink from the primary cell, thus reducing the interference caused by multiple transmissions in a soft handover mode. A second objective is to achieve fast site selection without network intervention, thus maintaining the advantage of the soft handover. In order to select a primary cell, each cell is assigned a temporary identification (ID) and UE periodically informs a primary cell ID to the connecting cells. The non-primary cells selected by UE switch off the transmission power. The primary cell ID is delivered by UE to the active cells via uplink FBI field. SSDT activation, SSDT termination and ID assignment are all carried out by higher layer signalling.

5.2.1.43.1.1 Definition of temporary cell identification

Each cell is given a temporary ID during SSDT and the ID is utilised as site selection signal. The ID is given a binary bit sequence. There are three different lengths of coded ID available denoted as "long", "medium" and "short". The network decides which length of coded ID is used. Settings of ID codes for 1-bit and 2-bit FBI are exhibited in table 3 and table 4, respectively.

Table 3: Settings of ID codes for 1 bit FBI

ID label	ID code		
	"long"	"medium"	"short"
a	0000000000000000	0000000(0)	00000
b	1111111111111111	1111111(1)	11111
c	0000000011111111	0000111(1)	00011
d	1111111100000000	1111000(0)	11100
e	0000111111111000	0011110(0)	00110
f	1111000000001111	1100001(1)	11001
g	0011110000111110	0110011(0)	01010
h	110000111100001	1001100(1)	10101

Table 4: Settings of ID codes for 2 bit FBI

ID label	ID code (Column and Row denote slot position and FBI-bit position.)		
	"long"	"medium"	"short"
a	0000000(0)	000(0)	000
	0000000(0)	000(0)	000
b	1111111(1)	111(1)	111
	1111111(1)	111(1)	111
c	0000000(0)	000(0)	000
	1111111(1)	111(1)	111
d	1111111(1)	111(1)	111
	0000000(0)	000(0)	000
e	0000111(1)	001(1)	001
	1111000(0)	110(0)	100
f	1111000(0)	110(0)	110
	0000111(1)	001(1)	011
g	0011110(0)	011(0)	010
	0011110(0)	011(0)	010
h	1100001(1)	100(1)	101
	1100001(1)	100(1)	101

ID must be terminated within a frame. If FBI space for sending a given ID cannot be obtained within a frame, hence if the entire ID is not transmitted within a frame but must be split over two frames, the last bit(s) of the ID is(are) punctured. The relating bit(s) to be punctured are shown with brackets in table 3 and table 4.

5.2.1.43.2 TPC procedure in UE

The TPC procedure of the UE in SSdT is identical to that described in subclause 5.2.1.2 or 5.2.1.3 in compressed mode.

5.2.1.43.3 Selection of primary cell

The UE selects a primary cell periodically by measuring the RSCP of CPICHs transmitted by the active cells. The cell with the highest CPICH RSCP is detected as a primary cell.

5.2.1.43.4 Delivery of primary cell ID

The UE periodically sends the ID code of the primary cell via portion of the uplink FBI field assigned for SSdT use (FBI S field). A cell recognises its state as non-primary if the following conditions are fulfilled simultaneously:

- the received primary ID code does not match with the own ID code,
- the received uplink signal quality satisfies a quality threshold, Q_{th} , a parameter defined by the network.
- and, when the uplink link compressed mode, does not results in excessive levels of puncturing on the coded ID. The acceptable level of puncturing on the coded ID is less than $(int)N_{ID}/3$ symbols in the coded ID (where N_{ID} is the length of the coded ID).

Otherwise the cell recognises its state as primary.

The state of the cells (primary or non-primary) in the active set with update synchronous. If a cell receives the last portion of the coded ID in uplink slot #j, the state of cell is updated in downlink slot# $\{(j+1+T_{os}) \bmod 15\}$. Where T_{os} is defined as a constant of 2 time slots. The updating of cell state is unchanged by the operation of downlink compressed mode.

At the UE, the primary ID code to be sent to the cells is segmented into a number of portions. These portions are distributed in the uplink FBI S-field. The cell in SSDT collects the distributed portions of the primary ID code and then detects the transmitted ID. Period of primary cell update depends on the settings of code length and the number of FBI bits assigned for SSDT use as shown in table 5

Table 5: Period of primary cell update

code length	The number of FBI bits per slot assigned for SSDT	
	1	2
"long"	1 update per frame	2 updates per frame
"medium"	2 updates per frame	4 updates per frame
"short"	3 updates per frame	5 updates per frame

5.2.1.43.5 TPC procedure in the network

In SSDT, a non-primary cell can switch off its DPDCH output (i.e. no transmissions).

The cell manages two downlink transmission power levels, P1, and P2. Power level P1 is used for downlink DPCCH transmission power level and this level is updated as the same way specified in 5.2.1.2 or 5.2.1.3 in compressed mode regardless of the selected state (primary or non-primary). The actual transmission power of TFCH, TPC and pilot fields of DPCCH is set by adding P1 and the offsets PO1, PO2 and PO3, respectively, as specified in 5.2.1.1. P2 is used for downlink DPDCH transmission power level and this level is set to P1 if the cell is selected as primary, otherwise P2 is switched off. The cell updates P1 first and P2 next, and then the two power settings P1 and P2 are maintained within the power control dynamic range. Table 6 summarizes the updating method of P1 and P2.

Table 6: Updating of P1 and P2

State of cell	P1 (DPCCH)	P2 (DPDCH)
non primary	Updated by the same way as specified in 5.2.1.2 or 5.2.1.3 in compressed mode	Switched off
primary		= P1

5.2.2 Power Control with DSCH

The DSCH power control can be based on the following solutions, which are selectable, by the network.

- Inner-loop power control based on the power control commands sent by the UE on the uplink DPCCH.
- Slow power control.

5.2.3 AICH

The UE is informed about the relative transmit power of the AICH (measured as the power per transmitted acquisition indicator) compared to the primary CPICH transmit power by the higher layers.

5.2.4 PICH

The UE is informed about the relative transmit power of the PICH compared to the primary CPICH transmit power by the higher layers.

Annex B (Informative): Downlink Power-power control timing

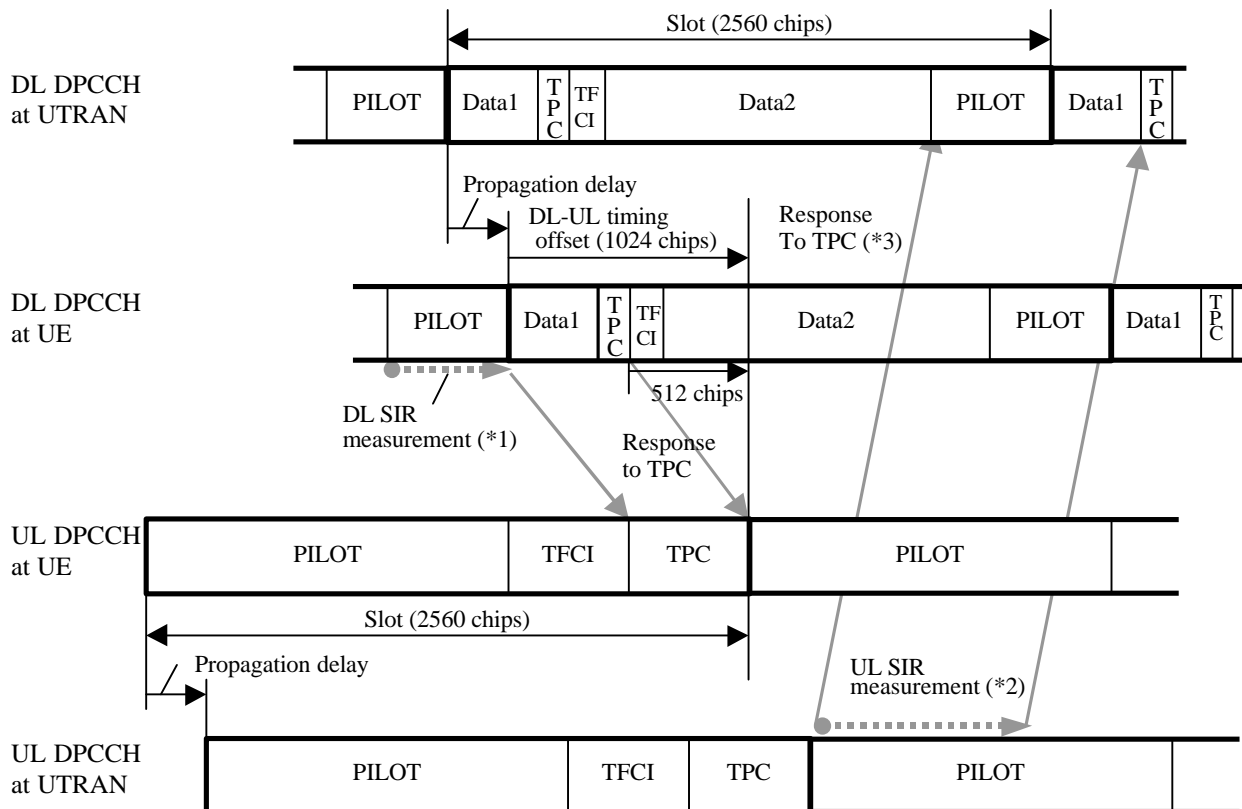
B.1 Power control timing

The power control timing described in this annex should be seen as an example on how the control bits have to be placed in order to permit a short TPC delay.

In order to maximise the cell radius distance within which one-slot control delay is achieved, the frame timing of an uplink DPCH is delayed by 1024 chips from that of the corresponding downlink DPCH measured at the UE antenna.

Responding to a downlink TPC command, the UE shall change its uplink DPCH output power at the beginning of the first uplink pilot field after the TPC command reception. Responding to an uplink TPC command, the UTRAN access point shall change its DPCH output power at the beginning of the next downlink pilot field after the reception of the whole TPC command. Note that in soft handover, the TPC command is sent over one slot when DPC_MODE is 0 and over three slots when DPC_MODE is 1. Note also that the delay from the uplink TPC command reception to the power change timing is not specified for UTRAN. The UE shall decide and send TPC commands on the uplink based on the downlink SIR measurement. The TPC command field on the uplink starts, when measured at the UE antenna, 512 chips after the end of the downlink pilot field. The UTRAN access point shall decide and send TPC commands based on the uplink SIR measurement. However, the SIR measurement periods are not specified either for UE nor UTRAN.

Figure B-1 illustrates an example of transmitter power control timings.



- *1,2 The SIR measurement periods illustrated here are examples. Other ways of measurement are allowed to achieve accurate SIR estimation.
- *3 If there is not enough time for UTRAN to respond to the TPC, the action can be delayed until the next slot.

Figure B-1: Transmitter power control timing

B.2 Example of implementation in the UE

The downlink inner-loop power control adjusts the network transmit power in order to keep the received downlink SIR at a given SIR target, SIR_{target} . A higher layer outer loop adjusts SIR_{target} independently for each connection.

The UE should estimate the received downlink DPCCH/DPDCH power of the connection to be power controlled. Simultaneously, the UE should estimate the received interference. The obtained SIR estimate SIR_{est} is then used by the UE to generate TPC commands according to the following rule: if $SIR_{est} > SIR_{target}$ then the TPC command to transmit is "0", requesting a transmit power decrease, while if $SIR_{est} < SIR_{target}$ then the TPC command to transmit is "1", requesting a transmit power increase.

B.3 Adjustment loop

In case of soft handover, UTRAN may employ adjustment loop, in which they change their calculated transmission powers $P(i)$ in every slot according to the following equation:

$$P(i+1) = P(i) + S_{INNER}(i) + S_{ADJ}(i)$$

$$S_{ADJ}(i) = \text{sign}\{(1 - r)(P_{REF} - P(i))\} \min\{|(1 - r)(P_{REF} - P(i))|, S_{ADJ_MAX}\}$$

where

$P(i)$: calculated transmission power of UTRAN access point in dBm,

$S_{INNER}(i)$: inner loop control in dB.

$S_{ADJ}(i)$: adjustment loop control in dB.

$sign(x)$: sign function of the value x , i.e. +1 when $x>0$, 0 when $x=0$, and -1 when $x<0$.

r : convergence coefficient ($0 \leq r \leq 1$).

P_{REF} : reference transmission power in dBm.

S_{ADJ_MAX} : maximum power change limit by adjustment loop in dB.

The actual change in the transmitted power level due to the adjustment loop is a value which is the nearest allowed TPC step to $S_{ADJ}(i)$. The parameters, r , P_{REF} , and S_{ADJ_MAX} shall be signalled by higher layers. S_{ADJ_MAX} shall be a multiple of the minimum step size $\Delta_{TPC, min}$ dB.