

**3GPP Meeting RAN WG1 meeting #11
San Diego, USA, 29 Feb - 03 Mar 2000**

Document R1-000303

e.g. for 3GPP use the format TP-99xxx
or for SMG, use the format P-99-xxx

<h2 style="margin: 0;">CHANGE REQUEST</h2>		Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.
25.212	CR 058	Current Version: V3.1.0
GSM (AA.BB) or 3G (AA.BBB) specification number ↑	↑ CR number as allocated by MCC support team	
For submission to: RAN #7 <small>list expected approval meeting # here ↑</small>	for approval for information <input checked="" type="checkbox"/>	strategic <input type="checkbox"/> non-strategic <input type="checkbox"/> <small>(for SMG use only)</small>

Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: <ftp://ftp.3gpp.org/Information/CR-Form-v2.doc>

Proposed change affects: (U)SIM ME UTRAN / Radio Core Network
(at least one should be marked with an X)

Source: Panasonic **Date:** 25 Feb 2000

Subject: Editorial removal of unnecessary box of table1: Error Correction Coding Parameters

Work item: TS25.212

Category:	F Correction <input type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input type="checkbox"/> D Editorial modification <input checked="" type="checkbox"/>	Release:	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
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(only one category shall be marked with an X)

Reason for change: In 25.212CR026r1, Table 1 is updated. But there remains unnecessary box. So editorial CR is need.

Clauses affected: [Empty box]

Other specs affected:	Other 3G core specifications <input type="checkbox"/> → List of CRs: Other GSM core specifications <input type="checkbox"/> → List of CRs: MS test specifications <input type="checkbox"/> → List of CRs: BSS test specifications <input type="checkbox"/> → List of CRs: O&M specifications <input type="checkbox"/> → List of CRs:	
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Other comments: [Empty box]

<----- double-click here for help and instructions on how to create a CR.

4.2.1 4.2.3 Channel coding

Code blocks are delivered to the channel coding block. They are denoted by $O_{ir1}, O_{ir2}, O_{ir3}, \dots, O_{irK_i}$, where i is the TrCH number, r is the code block number, and K_i is the number of bits in each code block. The number of code blocks on TrCH i is denoted by C_i . After encoding the bits are denoted by $y_{ir1}, y_{ir2}, y_{ir3}, \dots, y_{irY_i}$. The encoded blocks are serially multiplexed so that the block with lowest index r is output first from the channel coding block. The bits output are denoted by $c_{i1}, c_{i2}, c_{i3}, \dots, c_{iE_i}$, where i is the TrCH number and $E_i = C_i Y_i$. The output bits are defined by the following relations:

$$c_{ik} = y_{i1k} \quad k = 1, 2, \dots, Y_i$$

$$c_{ik} = y_{i,2,(k-Y_i)} \quad k = Y_i + 1, Y_i + 2, \dots, 2Y_i$$

$$c_{ik} = y_{i,3,(k-2Y_i)} \quad k = 2Y_i + 1, 2Y_i + 2, \dots, 3Y_i$$

...

$$c_{ik} = y_{i,C_i,(k-(C_i-1)Y_i)} \quad k = (C_i - 1)Y_i + 1, (C_i - 1)Y_i + 2, \dots, C_i Y_i$$

The relation between O_{irk} and y_{irk} and between K_i and Y_i is dependent on the channel coding scheme.

The following channel coding schemes can be applied to TrCHs:

- Convolutional coding
- Turbo coding
- No channel coding

The values of Y_i in connection with each coding scheme:

- Convolutional coding, 1/2 rate: $Y_i = 2 * K_i + 16$; 1/3 rate: $Y_i = 3 * K_i + 24$
- Turbo coding, 1/3 rate: $Y_i = 3 * K_i + 12$
- No channel coding, $Y_i = K_i$

Table 1: Error Correction Coding Parameters

Transport channel type	Coding scheme	Coding rate
BCH	Convolutional code	1/2
PCH		
RACH		1/3, 1/2
CPCH, DCH, DSCH, FACH		
	Turbo Code	1/3
	No coding	

<u>Transport channel type</u>	<u>Coding scheme</u>	<u>Coding rate</u>
<u>BCH</u>	<u>Convolutional code</u>	<u>1/2</u>
<u>PCH</u>		
<u>RACH</u>		<u>1/3, 1/2</u>
<u>CPCH, DCH, DSCH, FACH</u>	<u>Turbo Code</u>	<u>1/3</u>
	<u>No coding</u>	

If no code blocks are input to the channel coding ($C_i = 0$), no bits shall be output from the channel coding, i.e. $E_i = 0$.