**3GPP TSG RAN WG1 #103-e R1-200xxxx**

**e-Meeting, October 26th – November 13th, 2020**

**Agenda item:** 8.8.2.2

**Source:** Moderator (Qualcomm)

**Title:** FL summary of PUCCH coverage enhancement

**Document for:** Discussion/Decision

# 1 Introduction

In this document, a summary of companies’ view on potential techniques for PUCCH coverage enhancement is provided.

# 2 Summary of study on prioritized schemes

## 2.1 DTX detection for PUCCH

**Necessity of DTX detection**

In the email discussion, 4 companies expressed the view that DTX detection is important for PUCCH carrying HARQ-ACK. One company express the view that DTX detection is not important for HARQ-ACK with medium/large payload size due to the rarity of DTX in this case.

**Previous agreement** (made in RAN1 101e)

* For link level simulation, adopt the following table for PUCCH for FR1.

|  |  |
| --- | --- |
| Parameters | Values |
| PUCCH format type | Format 1, 2bits UCI.  Format 3, [4bits (3 bits A/N + 1 bit SR)]/11/22 bits UCI |
| BLER for PUCCH | For PUCCH format 1:  DTX to ACK probability: 1%. NACK to ACK probability: 0.1%.  ACK missed detection probability: 1%.  For PUCCH format 3:  BLER for Ack/Nack, SR: 1%  FFS: BLER for CSI (10% or 1%) |

**RAN4 requirement**

According to RAN4 requitements in Section 8.3.1.2 in TS 38.104, The DTX to ACK probability shall not exceed 1% for all PUCCH formats carrying ACK/NACK bits.

According to RAN 4 requirements in Section 8.3.3.2 in TS 38.104, The ACK missed detection probability shall not exceed 1% at the SNR given in table 8.3.3.2.2-1 and in table 8.3.3.2.2-2.

According to RAN 4 requirements in Section 8.3.3.1 in TS 38.104, The NACK to ACK probability shall not exceed 0.1% at the SNR given in table 8.3.3.1.2-1 and table 8.3.3.1.2-2.

**Submitted simulation results with DTX detection**

For DMRS-less PUCCH:

* With 2 bits UCI in PUCCH format 1, 3 companies submitted simulation results with DTX detection (with requirements of 1% FA, 1% ACK miss, 0.1% NACK->ACK error)
* With 3 bits UCI in PUCCH format 1, 1 company submitted simulation results with DTX detection (with requirements of 1% FA, 1% BLER, without showing 0.1% NACK->ACK and 1% ACK miss performance)
* With 6 bits UCI in PUCCH format 1, 1 company submitted simulation results with DTX detection (with requirements of 1% FA, 1% BLER, without showing 0.1% NACK->ACK and 1% ACK miss performance)
* With 11 bits UCI in PUCCH format 1, 1 company submitted simulation results with DTX detection (with requirements of 1% FA, 1% BLER, without showing 0.1% NACK->ACK and 1% ACK miss performance)

For other three prioritized PUCCH coverage enhancement scheme, i.e., type B PUCCH repetition, dynamic PUCCH repetition factor indication, DMRS bundling cross PUCCH repetition, no company submitted simulation results with DTX detection.

By the way, for PUCCH coverage baseline study performed in 8.8.1.1 and 8.8.1.2, no company submitted PUCCH format 3 simulation results with DTX detection.

In summary, the status of DTX related study can be summarized as following

* Except for the DMRS-less PUCCH, no simulation results with DTX was submitted for other PUCCH coverage enhancement schemes.
* In the study of DMRS-less PUCCH performance
  + For 2 bits UCI, the performance of FA, ACK miss, and NACK to ACK error were studied.
  + For more than 2 bits UCI, only the performance of FA and BLER were studied. The performance of ACK miss and NACK to ACK error were not studied.

**Proposal 1: For coverage enhancement study for PUCCH with >2 bits UCI, in addition to the 1% BLER performance metric agreed in RAN1 101e, the following performance metric can be considered:**

* **For UCI with HARQ-ACK payload, the performance metric for HARQ-ACK is 1% DTX to ACK error rate, 1% ACK miss detection (including ACK to NACK and ACK to DTX) error rate, and 0.1% NACK to ACK error rate.**
* **For UCI with CSI/SR payload, the performance metric for CSI/SR (if exist) is 1% false alarm rate, 1% BLER [or 10% BLER], [5% undetectable error rate for <=11 bits CSI/SR, and 2% undetectable error rate for >11 bits CSI/SR]**

**Note 1: In addition to the results already submitted to RAN1 103e which does not consider DTX detection, for any PUCCH coverage enhancement scheme especially the four prioritized schemes, companies are encouraged to submit more simulation results by 11/10/2020 with DTX detection, considering the above performance metric. Both results with and without DTX detection will be captured in the TR.**

**Note 2: false alarm rate is the probability that DTX is detected as a correct payload.**

**[Note 3: undetectable error rate = # instances that a UCI payload is declared as correct when the UCI payload is in error / Total # instances that UCI payloads are in error, where a UCI payload is declared as correct if it passes the error detection check.]**

Agreements:

(Working assumption): For coverage enhancement study for PUCCH with >2 bits UCI, in addition to the 1% BLER performance metric agreed in RAN1 101e, the following performance metric can be considered:

* For UCI with HARQ-ACK payload (with or without CSI/SR payload), the performance metric for HARQ-ACK is 1% DTX to ACK error rate, 1% ACK miss detection (including ACK to NACK and ACK to DTX) error rate, and 0.1% NACK to ACK error rate.
  + The payload size is 3 and 11 bits for HARQ-ACK. Other payload sizes can be evaluated and if so, reported by each individual company
* For UCI with HARQ-ACK and CSI/SR payload, the performance metric for CSI/SR is 1% false alarm rate, 1% BLER [or 10% BLER], 5% undetectable error rate for <=11 bits, and 2% undetectable error rate for >11 bits
  + The payload size is 11 bits or 22 bits, where 4 and 8 bits for HARQ-ACK, respectively. Other payload sizes can be evaluated and if so, reported by each individual company

Note 1: In addition to the results already submitted to RAN1 103e which does not consider DTX detection, for any PUCCH coverage enhancement scheme especially the four prioritized schemes, companies are encouraged to submit more simulation results by 11/10/2020 with DTX detection, considering the above performance metric. Both results with and without DTX detection will be captured in the TR.

Note 2: false alarm rate is the probability that DTX is detected as a correct payload.

Note 3: undetectable error rate = # instances that a UCI payload is declared as correct when the UCI payload is in error / Total # instances that UCI payloads are in error, where a UCI payload is declared as correct if it passes the error detection check (with details up to each company, and to be reported)

Comments to the above FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| Ericsson | For Rel-15 PUCCH, we simulated (3 bits A/N + 1 bit SR), which we think can be relevant for TDD in a coverage scenario. However, it is not clear to us why larger payloads than 4 bits carrying A/N are needed in a coverage scenario. We also note that few companies simulated >2 bits A/N as a baseline, which then means that there may not be so much consensus for >2 bits A/N in this study (although again our view is that 3 A/N + 1 SR is suitable for TDD).  Our suggestion is that if results on DTX detection are to be requested, that they be for coverage use cases, and so for no more than 4 bits UCI carrying A/N. Also, sufficient time should be allowed for companies to produce these results. Doing so within 2 or 3 days (depending on time zones) is a lot to ask, and so we don’t think the deadline of Nov 6 should be used. |
| Intel | We think we need to consider two cases: 1) when PUCCH is used to carry HARQ-ACK payload. 2) when PUCCH is used to carry other UCI type including HARQ-ACK feedback + CSI report/SR.  For case 1), we think 1% DTX to ACK error rate, 1% ACK miss detection error rate, and 0.1% NACK to ACK error rate should be considered as in the proposal. We also like to clarify ACK miss detection error rate is from ACK to NACK and ACK to DTX.  For case 2), we think 1% false alarm (or whether signal is present rather than DTX to ACK) should be considered for performance evaluation. We suggest the following update:  **For PUCCH performance evaluation, in addition to the 1% BLER performance metric agreed in RAN1 101e, the following performance metric can be considered to evaluate any PUCCH enhancement scheme especially the 4 prioritized schemes:**   * **For PUCCH with HARQ-ACK payload, 1% DTX to ACK error rate, 1% ACK miss detection error rate (ACK->NACK and ACK->DTX), and 0.1% NACK to ACK error rate** * **For PUCCH with CSI/SR and/or HARQ-ACK payload, BLER performance with 1% false alarm rate**   Regarding the deadline, we share similar view as Ericsson that Nov. 6 is reasonable. |
| ZTE | We are fine to consider additional metrics as defined in RAN4. But, it seems different metrics are defined for different PUCCH formats in RAN4. For PUCCH format 1, the metrics are the ones in the proposal. For PUCCH format 3, it seems only ‘1% DTX to ACK error rate and 1% BLER’ are defined. Correct me if I am wrong.  So, maybe we can either simply to say using the metric defined in RAN4 or list different metrics for different PUCCH formats.  In addition, we think it is also typical to carry medium or even large HARQ-ACK payload in PUCCH for TDD. For TDD configuration ‘DDDDDDDSUU’ as agreed for 2.6GHz, it is typical for gNB to transmit all HARQ-ACK bits corresponding to PDSCH in ‘D’ slots in PUCCH in one of the ‘U’ slots, if gNB wants to avoid UCI multiplexing in PUSCH as scheduled in anther ‘U’ slot. Even we don’t consider multiple TB transmission or CBG transmission, the payload could be reach to 7 or 8 HARQ-ACK bits + one SR bit. |
| Samsung | Consideration of additional metrics is fine, although there might not be enough time to generate new results.  To align with RAN4 specification, DTX performance requirement can be associated with UCI payload size.  **Proposal 1: For PUCCH with HARQ-ACK payload with UCI bit size <= 4, in addition to the 1% BLER performance metric agreed in RAN1 101e, the following performance metric can be considered to evaluate any PUCCH enhancement scheme ~~especially the 4 prioritized schemes~~:**   * **1% DTX to ACK error rate, 1% ACK miss detection error rate, and 0.1% NACK to ACK error rate** |
| Nokia/NSB | We have similar view as Intel. However, and as we said in our comments to Proposal 2, we think that the false alarm rate is an important metric to test regardless of the content of the UCI payload. Please note that this was also the understanding RAN1 achieved in RAN1 #88, when the following conclusion on how to test the performance of channel code for very small block code lengths was endorsed.  **Conclusion:**  For very small block lengths:   * For evaluations to be submitted to RAN1#88bis of channel code for very small block lengths, evaluate both BLER and error detection capability for comparison   + FFS the error detection targets * FFS whether the receiver knows in each case whether a codeword is transmitted and the format thereof * FFS whether the coding scheme is the same on control and data physical channels * FFS the details of the selection criteria   As we discussed in our previous comments, introducing a new PUCCH formats which cannot guarantee at least the same error detection performance as existing formats, for same payload, would have severe impact at gNB. For this reason, FAR defined as follows should be considered:  FAR = #number of instances that a UCI payload is declared as correct when the UCI payload is in error / Total #UCI payloads in error  Where a UCI payload is declared as correct if it passes the error detection check.  Now, FAR performance that current PUCCH formats can deliver depends on payload size, e.g., X. This is one additional reason why bounding X in proposal 2 of the FL summary is important. According to numbers companies produced during Rel-15 WI, the following could be a good starting point for the discussion   * FAR<=5%, when X<=11 * FAR<=2%, when 11< X<=22   We are open to consider other numbers if different opinions exist in RAN1. |
| Huawei, HiSilicon | To meet the performance metric from RAN4, a clarification is needed for further simulations that how many A/N bits are assumed for PUCCH format 3, because the number of A/N bits will affect the baseline performance obviously.  For coverage limited user, we think a small number of A/N bits should be used, e.g. ≤2bits, which is also commented by other companies.  Furthermore, the deadline is a bit too close leaving limited time for further simulations. We should focus on the simulation results for PUCCH format 3 with DMRS-less based detection and payload size bits.  Moreover, it is necessary to clarify the performance metric for each simulated PUCCH format. In TS 38.104,   * For PUCCH format 1, “1% DTX to ACK error rate, 1% ACK miss detection error rate, and 0.1% NACK to ACK error rate” are required. * For PUCCH format 3, “1% DTX to ACK error rate” and “1% block error probability” are required. |
| OPPO | We agree that the requirement of “1% DTX to ACK error rate, 1% ACK miss detection error rate, and 0.1% NACK to ACK error rate” should be used for format 3, especially for the smaller payload case, which may be critical for transmitting HARQ-ACK. It could be used for the case of TDD UL/DL configuration.  The consideration is not only simply on RAN4 specification, it is on some real need.  However, larger payload should not be considered, also due to the comments made by Huawei about the timeline.  However, for the coverage limited case the 2 bits PUCCH use case is most important to us.  We can consider use the above requirement for <=11 bits. |
| EURECOM | In our opinion, consideration of a DTX event with more than 2 ACK/NAK bits in the PUCCH performance evaluation will not yield any useful insight. Probabilities of DTX events vanish very quickly with the number of ACK/NAK bits in a single PUCCH transmission. |

## 2.2 Summary of new sim results with DTX detection

Table 0: New simulation results with DTX detection

|  |  |  |  |
| --- | --- | --- | --- |
| Company | Observed performance gain | Key simulation assumptions | Tdoc # |
| vivo | 1-2dB: (NACK->Ack) | Scenario 1: **3** bits UCI, w/ DTX detection, performance metric: 1% DTX to ACK error rate, 1% ACK miss detection, and 0.1% NACK to ACK, 1% BLER  Receiver for Rel-15/16 PUCCH: coherent detection, DTX detection is performed on union of PUCCH DMRS and UCI symbols, as shown in our updated contribution R1-2009648.  Receiver for PUCCH enhancement scheme: non-coherent ML detection. | R1-2009648 |
| ~ 4dB: (NACK->Ack) | Scenario 2: **11** bits UCI, w/ DTX detection, performance metric: 1% DTX to ACK error rate, 1% ACK miss detection, and 0.1% NACK to ACK, 1% BLER  Receiver for Rel-15/16 PUCCH: coherent detection, DTX detection is performed on union of PUCCH DMRS and UCI symbols, as shown in our updated contribution R1-2009648.  Receiver for PUCCH enhancement scheme: non-coherent ML detection. |
|  | FYI: We find that we did not provide the simulation assumptions correctly in Table 2 of our updated contribution R1-2009648  , some attributes are updated as follows. Sorry for the confusion.   |  |  | | --- | --- | | Number of symbols | Config 1: 4 symbols  Config 2: 14 symbols | | Number of UCI bits | Config 1: 3 bits  Config 2: 11 bits | | DMRS pattern | * Config 1   1 DMRS symbol for PF3 w/o frequency hopping  1 DMRS symbol in each hop for PF3 w/ frequency hopping   * Config 2   4 DMRS symbols for PF3 w/o frequency hopping  2 DMRS symbols in each hop for PF3 w/ frequency hopping | |
| ZTE | 3.8dB | 11 bits UCI, w/ DTX detection, performance metric: 1% DTX to ACK error rate, 1% ACK miss detection, and 0.1% NACK to ACK.  Receiver for Rel-15/16 PUCCH: DTX detection is based on 4 DMRS symbols, ML coherent receiver.  Receiver for PUCCH enhancement scheme: ML noncoherent sequence detector | R1-2009696 |
| Intel | 0-0.3dB | 3-10/11 bits UCI, w/ DTX detection, performance metric: 1% false alarm probability, 1% BLER  Receiver for Rel-15/16 PUCCH: PUCCH format 3 with both coherent/non-coherent receiver. Non-coherent receiver can provide better performance than coherent receiver.  Receiver for PUCCH enhancement scheme:  4 schemes were considered in the updated tdoc. 1) Rel-15 PUCCH format 3 with DMRS and removing the 1st column of RM codeword 2) DMRS-less with Gold sequence based 3) DMRS-less with removing the 1st column of RM codeword 4) DMRS-less with enhanced scrambling sequence: note that all 4 schemes have similar performance for all range of 3-10/11 bits.  Non-coherent receiver | R1-2009602 |
| Qualcomm | 4 dB | Results with Error detection (Fig. 20 in updated Tdoc):  Setup: 11 bit UCI, 1 RB, 14 OFDM symbols, freq hopping enabled, 4Rx, TDL-C-300, 11 Hz. 4 DMRS symbols for NR PUCCH ( 2 per hop).  Targets: 1% BLER, and 5% undetectable error rate  Receiver:  Coherent ML receiver for NR PUCCH with error detection capability based on duo metric (as suggested in R1-1705863).  Non coherent ML detector for seq-based PUCCH with correlation metric and/or duo metric for error detection.  Sequence design: m-sequence with initialization based on UCI payload. | R1-2009711 |
| Qualcomm | 4 dB | Results with DTX detection (Fig. 18 in updated Tdoc):  Setup: 11 bit UCI, 1 RB, 14 OFDM symbols, freq hopping enabled, TDL-C-300, 11 Hz. 4 DMRS symbols for NR PUCCH (2 per hop).  Targets: 1% BLER (ACK miss detection), 1% DTX->ACK rate, 0.1% NACK->ACK rate.  Receiver:  Coherent ML receiver for NR PUCCH with DTX detection based on correlation metric (uses DMRS + re-encoded UCI)  Non coherent ML detector for seq-based PUCCH with DTX detection based on correlation metric  Sequence design: m-sequence with initialization based on UCI payload. | R1-2009711 |
| Ericsson | 0.5 dB higher required SNR from N->A errors in Rel-15 baseline vs. DTX  0.8 dB higher required SNR from (DTX + N->A) vs. BLER in Rel-15 baseline | Rel-15/16 PUCCH format 3 using conventional coherent receiver  Setup:   * 11 bits (9+2) UCI: * Part 1 UCI: 4 bits HARQ-ACK + 5 bits CSI part 1 * Part 2: 2 bits CSI part 2 * CSI reporting: Type I wideband, 4 port CSI-RS, 1 bit RI * 700 MHz, 2Rx, TDL-C Medium correlation, 300ns delay spread, 3kmph, 14 Symbols, 4 DMRS, No repetition, Frequency hopping   **Required SNR**   * 1% BLER: -2.3 dB * 0.1% N->A error: -1.5 dB * 1% DTX/FAR: -2.0 dB   **Observations**:   * N->A error has 0.5 dB tighter SNR requirement than DTX, and so does not seem critical to the performance of Rel-15 PF3 in these conditions * DTX detection with N->A error increases required SINR by 0.8 dB in these conditions, and so has a modest impact on PF3 coverage. | Details on remaining simulation parameters in  R1-2008343  No tdoc number yet for new results |
| Ericsson | 0.3 dB lower required SNR from N->A errors in Rel-15 baseline vs. DTX  1.7 dB higher required SNR from (DTX + N->A) vs. BLER in Rel-15 baseline | Rel-15/16 PUCCH format 3 using conventional coherent receiver  Setup:   * 3 bits HARQ-ACK * 700 MHz, 2Rx, TDL-C Medium correlation, 300ns delay spread, 3kmph, 14 Symbols, 4 DMRS, No repetition, Frequency hopping   **Required SNR**   * 1% BLER: -7.6 dB * 0.1% N->A error: -5.9 dB * 1% DTX/FAR: -5.6 dB   **Observations**:   * N->A error has 0.3 dB looser SNR requirement than DTX, and so DTX detection is more important to the performance of Rel-15 PF3 in these conditions than N->A error * DTX detection with N->A error increases required SINR by 1.7 dB in these conditions, and so has a notable impact on PF3 coverage. | Details on remaining simulation parameters in  R1-2008343  No tdoc number yet for new results |
|  |  |  |  |

## 2.3 DMRS-less PUCCH

Ten companies have provided LLS results for this scheme. The following table is firstly extracted from R1-2007483 “[102-e-Post-NR-CovEnh-02] Phase 3: initial collection of simulation results for enhancements” [23], followed by adding new results submitted to RAN103e in [1][10].

Table 1: Performance gain observed for DMRS-less PUCCH

|  |  |  |
| --- | --- | --- |
| Company | Observed performance gain | Key simulation assumptions |
| ZTE | 2 ~ 3 dB SNR gain | **Scenario 5: 11 bits UCI,** w/o DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: ML coherent receiver  Receiver for sequence based PUCCH: ML noncoherent sequence detector |
| Intel | -1.0 ~ 0.2 dB SNR gain | **Scenario 2: 3 bits UCI**, w/ DTX detection, 1% FA, 1% BLER  **Scenario 5: 11 bits UCI**, w/ DTX detection, 1% FA, 1% BLER  Receiver for Rel-15/16 PUCCH: ML coherent receiver  Receiver for sequence based PUCCH: ML noncoherent sequence detector/correlator |
| Qualcomm | 3 ~ 4 dB SNR gain  3.5dB PAPR gain w/ QPSK  0.5dB PAPR gain w/ Pi/2 BPSK | **Scenario 1: 2 bits UCI**, w/ DTX detection, 1% FA, 1% ACK miss, 0.1% NACK->ACK error  **Scenario 3: 4 bits UCI,** w/o DTX detection, 1% BLER  **Scenario 5: 11 bits UCI**, w/o DTX detection, 1% BLER  **Scenario 5: 11 bits UCI**, w/ DTX detection, 1% FA, 1% BLER  Receiver for Rel-15/16 PUCCH: ML coherent receiver  Receiver for sequence based PUCCH: ML noncoherent receiver (correlator with 2D-FFT or fast Hadamard transform) |
| Sharp | 3 dB | **Scenario 3: 4 bits UCI,** w/o DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: MMSE channel estimation (with genie Doppler and delay spread) + ML coherent detection  Receiver for sequence based PUCCH: ML noncoherent sequence detector/correlator |
| CMCC | 1 ~ 2.7dB | **Scenario 5: 11 bits UCI**, w/o DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: ML coherent receiver  Receiver for sequence based PUCCH: ML noncoherent sequence detector/correlator |
| vivo | 0.3 ~ 0.5dB | **Scenario 4: 6 bits UCI**, w/ DTX detection, 1% FA, 1% BLER  Receiver for Rel-15/16 PUCCH: ML noncoherent detector  Receiver for sequence based PUCCH: ML noncoherent sequence detector/correlator |
| Ericsson | 0 ~ 0.2dB | **Scenario 5: 11 bits UCI**, w/o DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: conventional and ML noncoherent  receiver  Receiver for sequence based PUCCH: ML noncoherent receiver |
| EURECOM | Coding gain: 1.5 ~ 2.1dB  4.8 dB PAPR gain over DFT-S-OFDM with π/2-BPSK  6.3 dB PAPR gain over DFT-S-OFDM with QPSK | **Scenario 3: 4 bits UCI**, w/o DTX detection, 1% BLER  **Scenario 5: 11 bits UCI**, w/o DTX detection, 1% BLER  **Scenario 6: 22 bits UCI,** w/o DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: advanced receivers for <=11 bits(non-coherent ML), conventional receiver for 22 bits (LS channel esimtation + MMSE/MRC)  Receiver for sequence based PUCCH: ML noncoherent sequence detector/correlator for 4/11 bit case; non-coherent LLR unit adapted to 3GPP polar code for 22-bit case. Also simulated low-complexity receiver for 11-bit UCI case. |
| Huawei, HiSi | 3 ~ 4dB  4.5dB (PAPR gain) | **Scenario 1: 2 bits UCI**, w/ DTX detection, 1% FA, 1% ACK miss, 0.1% NACK->ACK error  **Scenario 5: 11 bits UCI**, w/o DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: 2D-Wiener filter based channel estimation + MMSE equalization  Receiver for sequence based PUCCH: CHIRRUP algorithm based sequence detection |
| OPPO | ~3dB | **Scenario 1: 2 bits UCI**, w/ DTX detection, 1% FA, 1% ACK miss, 0.1% NACK->ACK error.  Receiver for Rel-15/16 PUCCH: LMMSE-IRC receiver.  Receiver for sequence based PUCCH: ML correlation. |

Besides the LLS simulations to study the gain of the scheme, a few other aspects of the schemes are also discussed/studied:

* The spec impact of the scheme is discussed in [1][4][6]
* The receiver complexity with the scheme is studied/discussed in [1][15][18][19]
* The receiver sensitivity to time and frequency error is studied in [18]

In the email discussion, the upper bound of UCI payload size for the DMRS-less PUCCH is discussed. One company prefer an upper of <=11 bits, another company prefer an upper bound of 24 bits which accommodate L1 beam report in FR2 that carries information regarding the best two beams.

Based on the feedback received, the proposal is updated to:

**Proposal 2: the number of UCI info bits (excluding CRC bits) that the DMRS-less PUCCH can support is up to X bits. Down select from the following two options for X.**

* **Option 1: X=24**
* **Option 2: X=11**

Comments to the above FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| OPPO | For number of bits we also think 11 bits is reasonable for coverage enhancement. For the FR2 coverage, it would be further noted that the coverage target of PUCCH would be smaller than FR1. |
| vivo | Option 2 is preferred.  For sequence based detection, more UCI bits may lead to high detection complexity as described in proposal 3. And more UCI bits seems aim to enhance CSI report, which is less critical compared with HARQ-Ack. Hence, we suggest to focus on UCI <= 11bits. |
| Nokia/NSB | Option 2. We’d like to add two further comments:   1. Is this scheme actually aim at replacing PF3 entirely? We are not sure this is the intention (any clarification in this regard is welcome). If it is not, then use case for Option 1 in the context of coverage shortage is not so clear. Conversely, use case for Option 2 is quite clear. It also seems to gather very large majority of companies’ preference and was studied by almost all companies. 2. Current option 2 states x<=11. Would X=11 be a reasonable middle ground between the two options?   We are really concerned by the presence of the FFS, we believe RAN should strive to resolve it before the end of RAN1 #103-3. According to the above, the solution seems rather straightforward…  Finally, we would like to ask a clarification to FL. We know have X defined as number of UCI info bits. Would it be possible to clarify what FL means by this, as opposed to the original formulation? We are not objecting the formulation but simply trying to understand it better. |
| ZTE | Fine with the proposal and we prefer Option 2 with X = 11bits. |
| Intel | Option 2 |
| Ericsson | Option 2 |
| Huawei, HiSilicon | Option 2 is preferred. To be more specific, we suggest X=11. |
| Apple | Given that current evaluations show PF0 is not a coverage bottleneck channel, we propose 2<X<12. |

Based on the input from companies in Section 4.1, the following proposal is made.

**Proposal 3-1: For DMRS-less PUCCH, capture the following in the TR**

**Use case:** Aim to enhance coverage of PUCCH with small and medium UCI size

**Restriction of the scheme:** up to X UCI info bits where X is FFS

**Prerequisite of the scheme:** None

**Performance gain:** captured in Table 1, where Table 1 is subject to change based on new simulation results

**Agreements: For DMRS-less PUCCH, capture the following in the TR**

**Potential Spec impact:**

* A new PUCCH format would need need to be specified, including the power control of the new PUCCH format. The new PUCCH format would be an addition to existing PUCCH formats.
* Two approaches to generate sequence for DMRS-less PUCCH (i.e., reuse Rel-15/16 CGS/ZC/Gold/m-sequence or design new sequences) were studied. The potential spec impacts include:
  + If reusing Rel-15/16 CGS/ZC/Gold/m-sequence of the same length being supported by the current Rel-15/16 specification, no new sequences need to be specified.
  + If new sequences (including new sequence type or same type as in Rel-15/16 but with different length) or sequences based on modification of NR Rel-15/16 UCI encoding scheme are adopted, the new sequences or the modification of NR Rel-15/16 UCI encoding scheme need to be specified.
* UCI to sequence mapping and sequence to RE mapping need to be specified
* [UCI info bits size (X) needs to be specified]
* [New RAN4 MPR requirement needs to be defined, if new sequences other than Rel-15/16 CGS/ZC/Gold/m-sequences are adopted]
* [UCI multiplexing for this new PUCCH format need to be specified]

**Proposal 3-3a: For DMRS-less PUCCH, capture the following in the TR**

**Impact to receiver:**

* Need to implement a non-coherent sequence detector/correlator for the new PUCCH format.
  + In the non-coherent sequence detector, changes to existing implementation for DTX detection may be necessary if the existing implementation relies on the presence of DMRS. To determine the DTX detection threshold, depends on gNB implementation, instantaneous noise power estimation may or may not needed.
* The new PUCCH format does not require channel and noise covariance matrix estimation to be received.
* Computation efficient implementations of the receiver for the new PUCCH format have been studied. Their complexity can be lower or higher than the decoder for existing NR PUCCH coherent receiver depending on the adopted sequence, on the UCI payload size and on the implementation of the considered coherent receiver.

**Proposal 3-3b: For DMRS-less PUCCH, capture the following in the TR**

**Impact to receiver:**

* Receiver implementation for the new PUCCH format is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool. The size of the sequence pool over which the receiver for the new PUCCH format needs to perform correlation increases exponentially with the number of UCI bits.
* Similar to PUCCH format 0, the new PUCCH format does not have DMRS for interference suppression and tracking loops. Two companies raised concern that absence of DMRS in the new PUCCH format may hinder feasibility of advanced interference suppression and tracking loops.
  + Regarding the inter-cell interference suppression, one company pointed out the sequence based PUCCH can be resistant to inter-cell interference by properly choosing sequences across cells. One company compared the performance of the new PUCCH format with NR PF3 and observed that the new format is more robust to inter-cell interference than NR PF3.
  + Regarding the time and frequency tracking loops, one company compared the performance of the new PUCCH format with NR PF3 and observed that the new format is more robust to timing error and frequency error than NR PF3.

**Proposal 3-4: For DMRS-less PUCCH, capture the following in the TR**

**Impact to UE implementation**

* UE does not need to make use of existing channel encoder for the new PUCCH format.
* UE needs to implement a UCI to sequence mapping for the new PUCCH format
* Rel-15/16 CGS/ZC/Gold/m-sequences implementation can be reused in the new PUCCH format implementation, if Rel-15/16 CGS/ZC/Gold/m-sequences is adopted to support the new PUCCH format.

**[Impact to system]**

* [FFS the impact to system]

Comments to the above FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| Ericsson | From a gNB receiver perspective, the UCI encoding scheme is new, so I think it is correct to call it a new encoding scheme.  Our comments on the difficultly to suppress interference due to lack of DMRS and the inability to use DMRS for channel tracking need to be taken into account in ‘impact to receiver’. Suggest:   * Interference suppression may be infeasible due to lack of DMRS. * gNB is unable to use DMRS for channel tracking   How the gNB does DTX detection will also change with this approach, so that should be added as an impact to the receiver. |
| Qualcomm | Some comments on DMRS-less PUCCH based on the discussion in the FL summary and in the email discussions:   1. It is not clear to us what “disables existing coding scheme” means or even why this is problematic. We have conceded that sequence-based design, if agreed, would be a new format. With a new format certain changes, restrictions, etc are to be expected. There is no free lunch. Note that this format is in addition to the existing formats, so if a current scheme utilizes a certain encoding/decoding scheme, it can continue to do so. 2. From a performance evaluation standpoint, for baseline coverage characterization we all seemed fine with using BLER targets (i.e., 1%, please see R1-101e agreements) to characterize coverage. But now, when it comes to enhancements, additional targets are being proposed. We clearly can’t have one goal post for baseline and another for enhancements. We also cannot pick and choose the constraints we wish to impose. We would like to know first if we should redo our baseline coverage characterization based on any new constraints. Until an answer to this question is arrived at, we do not wish to capture any arguments in this regard in the TR. 3. We also do not wish to make broad statements that control channel changes have a broad impact on system design. Barring exact specifics, preferably backed by some evidence, please leave such overly broad statements out of the TR. Regarding the lack of DMRS, we note that PF0 also doesn’t have DMRS, and we hope that the gNB design philosophy adopted for PF0 can be repurposed. 4. Regarding UCI payload size, until a design is finalized, we will not be able to identify this range accurately. Rather than pursuing preciseness, it will be good to have some room to further define this in the WI phase, assuming we pursue this enhancement. 5. Regarding “performance gain” please see earlier comment. Request clarification on baseline coverage characterization first. We don’t want to give the impression that RAN1 did not sufficiently study the performance of this enhancement. Many companies are still in the process of aligning their simulation results and we don’t want to make any premature statements in this regard. 6. Regarding alignment across companies, given the diverse set of results, can we urge companies to use one of the agreed baseline PUCCH configurations for ease of comparison (for e.g., payloads 4/11/22 bits, PF3, 1RB allocation etc). We have noticed that the results can vary depending on the choice of sequence, so it helps to align on the configuration first, before going deeper on sequence design. It will be great if we can align on BLER performance first, before moving to other considerations (if necessary and agreed upon). 7. Regarding receiver/detection complexity, our analysis indicates that the overall computations for the non-coherent approach can be fewer than that required for coherent detection of NR PUCCH. This is however dependent on payload size. We therefore do not wish to declare that this method necessarily results in an increase in receiver/detection complexity. We are happy to see any complexity analysis that indicates otherwise. This complexity trade-off may influence our decision on the UCI payload size limits we place on this design. 8. PF0 sets a clear precedent on how gNBs can implements receivers for sequence detection. While the implementation may not be directly reusable, it does offer a blueprint on how a gNB can function under such a scheme. We wish to see it recorded that this scheme is in effect an extension of the principles used in PF0, and some of the design principles can be reused. |
| Samsung | Some of the comments captured in the FL proposal are not meaningful. For example, the “simple UE Tx implementation without channel encoder” is not meaningful as a Rel-15/16 UE already has such encoder (and it will remain present). For example, although the proposed DMRS-less PUCCH is in principle same as PUCCH format 0, it is not true that gNB/UE implementation can be reduced since existing ones for PUCCH format 0 cannot support the new PUCCH format. New transmitter/receiver blocks will be required.  The system impact needs to also be considered. Given that the percentage of UEs requiring PUCCH coverage enhancements will be small, the overall system impact from possibly increasing a number of repetitions for a legacy format needs to be assessed in order to determine a benefit from introducing a new PUCCH format. |
| Intel | * Regarding “use case”   + Depending on the simulation results presented so far, we suggest to modify the observations as “Some companies claimed that use case of DMRS-less PUCCH is to enhance coverage of PUCCH with small and medium UCI size. Some other companies claimed that there is no use case of DMRS-less PUCCH for coverage enhancement.” * Reusing existing sequence   + We are not sure if the mentioned sequences of Rel-15/16 CGS/ZC/Gold/m-sequence can generate sufficient number of sequences to deliver the message of X bits. It should be removed or stated as observations from different companies. * Regarding “Prerequisite of the scheme”,   + Similar the comment as above, we would like to consider long PUCCH format as Prerequisite of the scheme. * Regarding the “impact to receiver”   + DTX detection for impact to receiver should be added as it is typical implementation not only for HARQ-ACK detection but for CSI on dropping rule and UCI piggybacking on PUSCH. With DMRS-less PUCCH scheme, it is clear that new DTX detection algorithm needs to be implemented.   + For receiver complexity, we share similar view as Nokia the email that this is highly dependent on specific implementation of current NR PUCCH receiver at base station. The claim that ML non-coherent sequence detection has smaller complexity than conventional PUCCH coherent receiver should not be accurate. For instance, with conventional receiver with coherent detection, Fast Hadamard Transform can be used for RM decoding. We need to conduct comprehensive study before we can make such a statement. We suggest to remove this statement.   + For “Receiver implementation for the new PUCCH format can leverage from PUCCH format 0 receiver”, we are not sure whether this is correct statement. In particular, when relatively large UCI payload size is considered, the receiver implementation is significantly different from that for PF0 (e.g. whether sequence is inserted in time or frequency domain). For instance, for UCI payload size of 11 bits, it is expected 2^11 correlator is needed to detect the correct sequence, which has huge difference compared to the receiver for PF0. We suggest to remove this statement or to state opinions from different companies.     - On noise estimation, it must be implemented for DTX detection anyway.     - On “[Receiver sensitivity to time/frequency error: ML non-coherent sequence detector is more robust to timing and frequency than conventional NR PUCCH coherent receiver]”, this statement is not true given that coherent detection is more robust in residual timing error (i.e. phase ramping in frequency domain) which is being compensated in channel estimation/equalization. Rather, non-coherent detection is generally vulnerable to residual time/frequency error since we need partial correlator or differential correlator in frequency domain or multiple hypothesis in time domain. Therefore, this should be removed. * Regarding impact to UE implementation   + For “Simple UE Tx implementation without channel encoder”, we are not sure whether this is correct statement as this is also highly dependent on UE implementation. For current RM code, RM encoded symbols are also another type of sequence.   + For “UE implementation effort can be reduced by reusing Rel-15/16 CGS/ZC/Gold/m-sequences”, we are not sure what is the baseline to compare with. Compared to existing PUCCH scheme, implementation effort is increased.   Therefore, we suggest the following modifications from FL summary:  **Proposal 1: For DMRS-less PUCCH, capture the following in the TR**  RAN1 discussed option of DMRS-less PUCCH for coverage enhancement with the following observations.  **Use case:** Some companies claimed that use case of DMRS-less PUCCH is to enhance coverage of PUCCH with small and medium UCI size. Some other companies claimed that there is no use case of DMRS-less PUCCH for coverage enhancement.  **Restriction of the scheme:** Some companies proposed to consider up to X UCI bits for further discussion on DMRS-less PUCCH where X is FFS.  **Prerequisite of the scheme:** long PUCCH format as prerequisite for further discussion on DMRS-less PUCCH.  **Performance gain:** Different companies have observed performance gain/loss as captured in Table 1.  **Spec impact if DMRS-less PUCCH is introduced:**   * A new PUCCH format needs to be specified, including the power control of the new PUCCH format. * There are proposals to consider to reuse Rel-15/16 CGS/ZC/Gold/m-sequence. If new sequences or new scrambling procedure with NR Rel-15/16 UCI encoding scheme are adopted in order to increase the information bits, the new sequences or the new scrambling procedure need to be specified. On the other hand, there is also an observation that, if we reuse existing RM coding table, there is no need to introduce any other new sequence. * Sequence to RE mapping need to be specified * Upper bound of supported UCI size (X) needs to be specified * [New RAN4 MPR requirement needs to be defined, if new sequences other than Rel-15/16 CGS/ZC/Gold/m-sequences are adopted]   **Impact to receiver if DMRS-less PUCCH is introduced:**   * Channel estimation block can be avoided in PUCCH receiver. There is still need to implement noise/interference estimation for DTX PUCCH detection. * Need to implement a new non-coherent sequence detector/correlator. * ML non-coherent sequence detection/correlation may increase the receiver complexity since the detector/correlator cannot leverage FHT (Fast Hadamard Transform) from existing Rel-15 RM coding. * There are some opinion that computation efficient implementations are available with certain choice of sequences to reduce receiver complexity. * There is some opinion that receiver implementation for the new PUCCH format can leverage from PUCCH format 0 receiver depending on the sequence types.   **Impact to UE implementation**   * There is a claim that simple UE Tx implementation can be achieved without channel encoder. On the other hand, there is another claim that existing Rel-15 RM encoder is nothing but another type of sequence where RM decoder is also nothing but ML decoding that can support both coherent and non-coherent detection. * There is an opinion that UE Tx implementation effort can be reduced by reusing Rel-15/16 CGS/ZC/Gold/m-sequences for DMRS-less PUCCH. There is also another opinion that there is almost no UE Tx implementation impact by reusing existing Rel-15 RM coded sequence. |
| LG | It is notable that “sequence to RE mapping” and new PUCCH format is necessary only if new sequence or new scrambling procedure are adopted. Therefore, we suggest the following modification in first to third bullets of spec impact.  Original)   * A new PUCCH format needs to be specified, including the power control of the new PUCCH format. * if reusing Rel-15/16 CGS/ZC/Gold/m-sequence, no new sequences need to be specified. If new sequences or new scrambling procedure with NR Rel-15/16 UCI encoding scheme are adopted, the new sequences or the new scrambling procedure need to be specified. * Sequence to RE mapping need to be specified   Suggesting modification)   * If reusing Rel-15/16 Rel-15/16 CGS/ZC/Gold/m-sequence, no new sequences need to be specified. As a consequences, no new PUCCH format and no sequence to RE mapping need to be specified. * If new sequence or new scrambling procedure with NR Rel-15/16 UCI encoding scheme are adopted, a new sequences or the new scrambling procedure need to be specified. As a consequence, new PUCCH format and sequence to RE mapping need to be specified. |
| ZTE | We are generally fine with the proposal, with the following minor changes:   * Adding ‘UCI to sequence mapping needs to be specified’ as one additional spec impact. * Adding ‘long PUCCH format’ as the prerequisite of the scheme.   We agree that, no matter whether new sequences would be introduced or not, a new PUCCH format should be defined. It includes both UCI to sequence mapping and sequence to RE mapping.  Regarding “use case”, we don’t think we need to add ‘some companies claimed that ...’. The proposed DMRS-less is intended for PUCCH carrying small and medium UCI size. If companies worried about the performance gain/spec impact/complexity, these worries could be added in corresponding performance gain/spec impact/complexity parts, while these aspects seem have already been well captured in the proposal. |
| Vivo | For spec impacts brought by sequence based PUCCH, as we mentioned in the 1st round, there would be some other specification impacts, which are not included in the current proposal.   * How to multiplex CSI/HARQ-Ack to a sequence based PUCCH should be considered in TS 38.213. For example, when CSI is multiplexed with HARQ-Ack, the CSI part is dropped based on the configured coding rate of PUCCH. It is necessary to clarify what is coding rate of a sequence based PUCCH if UCI multiplexing on this new PUCCH format is supported. * PUCCH format specific power adjustment component  for the new PUCCH format, should be defined in TS 38.213. |
| Nokia/NSB | We copy-paste here snippets of what we sent to the Reflector and was also referred to by other companies. Minor additional comments are also added and typos are corrected.  **Restriction of the scheme:** It is important to have a numerical value, or a range, in the TR instead of a placeholder. This should capture a correct value (in the sense what is assumed or used in the evaluations and complexity discussions) as generalizing observations listed in the following is not valid for an arbitrary value X. We are not sure  **Performance gain:** This scheme would replace the channel coding chain; however it would still have the same functional role in the system. As such, as also mentioned by Ericsson, it should be considered and studied as if it were a coding scheme. In this regard, we are not talking about correcting errors, of course, but more about the impact that specific designs of the control channel have on the system behavior/performance, beyond just coverage evaluations. Additional details related to this specific concern are below.  **Impact to receiver:**   * Efficient implementations of ML-detector were indeed studied and that should be captured in the TR. However, the current formulation of the bullet implies something stronger in our view, which would probably deserve a deeper discussion. As of now, we have not discussed this aspect, and we may very likely not do that due to lack of time. Additionally, the statement about conventional “NR PUCCH coherent receiver” is based on assumptions on gNB implementation which are hardly verifiable, clearly an implementation detail, and not agreed in RAN1 as baseline for this evaluation. Intel seems to share the same concern. While we are not completely sure about the proposed wording, we think their proposals could be used as a starting point to improve the bullet. * It is stated that receiver implementation for the new PUCCH format can leverage from PUCCH format 0 receiver. Other companies already commented on this, and we agree with their comments. Furthermore, correlators for PUCCH format 0 only check a small set of combinations and do not scale-up when the payloads are increasing.  Therefore, we do not think it is correct to claim that a larger payload can leverage the same receiver, even if the non-coherent nature of the detection applies to both. * As Intel mentioned, details on how well receiver can handle FAR/PMD when the threshold of the correlator when a 1-2 bit(s) UCI payload is received have not been studied (at least we have not found any result in this regard in the contributions submitted by companies to 8.8.2.2). We think this is very important point to consider, given that what we are discussing is not a “simple” coverage enhancement but a change to control channel which may have important impacts beyond coverage aspects (as also mentioned by Samsung). Indeed, as we know from coding discussions during Rel-15, error detection becomes a very important metric as UCI payloads get larger. This is also visible in 38.212, where it becomes more challenging to support the error detection based on the inbuild coding structure when payload sizes are increasing, and thus, additional CRC is added. * Linked to the previous point, consider what current formats allow gNB to do, e.g., identifying when a decoded UCI payload is “wrong” or not by looking at CRC for instance, and what does not seem so trivial to achieve using a non-coherent PUCCH format with payload larger than 1 bit. Having a PUCCH format which does not help gNB understanding if a PUCCH is received but wrongly decoded may significantly impact aspects related to gNB operations as compared to what can be done with other formats, such as at least, but limited to: * gNB’s downlink scheduler; * gNB’s uplink scheduler; * the performance of any downlink algorithm relying on the content of the UCI (e.g., beamforming).   Comments by Ericsson provide another example of consequences in this sense. |
| NTT DOCOMO | We are generally fine with the FL proposal. We don’t want to preclude PUCCH short format from the technique, since enhancement of PUCCH short format is also important for FR2 operation and NW configuration. |
| Qualcomm | Please find a few additional remarks:   1. With regard to use case, we wish to second ZTE’s comment. If there are concerns they can be addressed in performance gain/spec impact/complexity parts. 2. We don’t want to restrict this and other enhancements to long format PUCCH alone. We prefer to address this at a later point, once link budget analysis is complete. There appears to be strong operator interest in seeing some enhancements targeted at short PUCCH and we would like to take this input into consideration. 3. As to Nokia and Ericsson’s comment on system impact, if there are quantitative means to study this impact, let us know, we can try to study them. If this is general commentary on what is impacted, then it is not clear how to address or take this into account. Pretty much every scheme proposed here has system impact --- type B repetitions, DMRS bundling, etc. Some of these schemes require rethinking the relevance of a slot boundary --- this in our view has profound implications for system design. We don’t see similar concerns being raised in those enhancements. 4. If system impact is a concern, we can draw a separate list of concerns that apply to each of the 4 schemes being studied with higher priority. 5. We want to highlight the rarity of DTX events when a PUCCH transmission is set to carry multiple HARQ-ACK/NACK bits. Assuming each bit in the UCI payload maps to a unique downlink grant, a DTX event is triggered when a UE misses all of its downlink grants. Given that PDCCH is assumed to have a reliability of 10^-2, with even 4 HARQ-ACK/NACK bits, this event occurs with probability 10^-8, assuming independence across grant receptions. Thus, an over-emphasis on DTX detection seems unwarranted and may not reflect the needs of a practical deployment. 6. Having said the above, we are okay to investigate the performance beyond BLER as a metric. In particular we would like to consider NACK->ACK rate as well as DTX->ACK into account. We can reuse the targets we assumed for Format 1. We would also like these new benchmarks to be applied to other enhancements as well. We request the FL to establish a common set of performance criteria that applies to all PUCCH enhancements. |
| Intel | Some of our previous comments were not reflected in the updated proposal.  **Proposal 1: For DMRS-less PUCCH, capture the following in the TR**  RAN1 discussed option of DMRS-less PUCCH for coverage enhancement with the following observations.  **Use case:** Some companies claimed that use case of DMRS-less PUCCH is to enhance coverage of PUCCH with small and medium UCI size. Some other companies claimed that there is no use case of DMRS-less PUCCH for coverage enhancement.  **Restriction of the scheme:** Some companies proposed to consider up to X UCI bits for further discussion on DMRS-less PUCCH where X is FFS, if DMRS-less PUCCH is introduced.  **Prerequisite of the scheme:** long PUCCH format as prerequisite for further discussion on DMRS-less PUCCH.  **Performance gain:** Different companies have observed performance gain/loss as captured in Table 1.  **Spec impact if DMRS-less PUCCH is introduced:**   * A new PUCCH format needs to be specified, including the power control of the new PUCCH format. The new PUCCH format is an addition to existing PUCCH formats. * if reusing Rel-15/16 CGS/ZC/Gold/m-sequence, there is an opinion that no new sequences need to be specified. If new sequences or sequences based on modification of NR Rel-15/16 UCI encoding scheme are adopted, the new sequences or the modification of NR Rel-15/16 UCI encoding scheme need to be specified. On the other hand, there is also other opinion that, if we reuse existing RM coding table, there is no need to introduce any other new sequence. * UCI to sequence mapping and Sequence to RE mapping need to be specified * Upper bound of supported UCI size (X) needs to be specified * [New RAN4 MPR requirement needs to be defined, if new sequences other than Rel-15/16 CGS/ZC/Gold/m-sequences are adopted] * [CSI and HARQ-ACK multiplexing for this new PUCCH format need to be specified]   **Impact to receiver:**   * Need to implement a ML non-coherent sequence detector/correlator for the new PUCCH format. * ~~No need to implement channel and noise estimation in the receiver for the new PUCCH format~~ Channel estimation block can be avoided in PUCCH receiver. There is still need to implement noise/interference estimation for DTX PUCCH detection. * Receiver implementation for the new PUCCH format is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool. * The complexity of the ML non-coherent sequence detection/correlation increase with larger UCI size. * Computation efficient implementations are available with certain choice of sequences to reduce receiver complexity. Depends on UCI size, selected sequences, and implementation of ML noncoherent and conventional coherent receiver, ML non-coherent sequence detector may have larger or smaller complexity than conventional NR PUCCH coherent receiver. * [Receiver sensitivity to time/frequency error: ML non-coherent sequence detector is more robust to timing and frequency than conventional NR PUCCH coherent receiver] * [Similar to PUCCH format 0, the new PUCCH format does not have DMRS for interference suppression and tracking loops]   **Impact to UE implementation**   * Depending on UE implementation, UE does not need to implement channel encoder for the new PUCCH format and existing PUCCH format. * ~~UE implementation effort for this new PUCCH format can be reduced by reusing Rel-15/16 CGS/ZC/Gold/m-sequences, comparing with new PUCCH format based on introduced new sequences or modification of Rel-15/16 UCI encoding~~ |
| Nokia/NSB | Thank for the modifications. Current version of the proposal does not fully address our concerns. We propose the following modifications/additions:   * **Restriction of the scheme:** up to X UCI bits where X is FFS * We think X should be at least bounded. Several companies studied explicit payload sizes. Would it be possible to agree on X, please?   **Impact to receiver:**   * Need to implement a ML non-coherent sequence detector/correlator for the new PUCCH format. * No need to implement channel and noise estimation in the receiver for the new PUCCH format * In our view, this does not capture what has been said precisely. An alternative proposal could be: “*The new PUCCH format does not require channel and noise estimation to be decoded*” * Receiver implementation for the new PUCCH format ~~can leverage from~~ is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool. * It has been argued that the decoder of the new format would not reuse the same blocks as PF0. Hence the similarity of the two formats would be very low. On the other hand, this bullet seems to be there to capture the fact that the new format requires large sequence pool (and possibly larger memory requirement), whose size is known by construction, hence we are not sure it makes sense to refer to PF0. propose to reformulate the bullet as: “*The size of the sequence pool over which the receiver for the new PUCCH format needs to perform correlation increases exponentially with X*”. * Computation efficient implementations are available with certain choice of sequences to reduce receiver complexity. Depends on UCI size, selected sequences, and implementation of ML noncoherent and conventional coherent receiver, ML non-coherent sequence detector ~~can have smaller~~ may have larger or smaller complexity than conventional NR PUCCH coherent receiver. * This bullet seems to carry very little information content, if any. From our perspective, it states that complexity of the implementation of the new PUCCH format receiver can be larger of smaller than the conventional NR PUCCH coherent receiver. In practice we are simply excluding that the two receivers can have the same complexity. We suggest removing the bullet. * [Similar to PUCCH format 0, the new PUCCH format does not have DMRS for interference suppression and tracking loops] * Here again we are not sure it is relevant to refer to PF0, given that we do not know yet if resources used by the new format can be comparable in time/frequency to resources used by PF0. This would clearly alter the extent of the inability to extrapolate channel related information for interference suppression and tracking loops. We suggest rephrasing as: “*Absence of DMRS in the new format may hinder feasibility of advanced interference suppression and tracking loops*”.   **Impact to UE implementation**   * ~~Simple~~ UE ~~Tx implementation without~~ does not need to implement channel encoder for the new PUCCH format * This bullet does not seem to fully capture what has been said in the comments by several companies. UE needs to implement channel encoder to support other formats but does not need to use channel encoder to transmit using the new format. In other words, unless UE supports only the new format, UE implementation will have the same encoder as before and will “simply” bypass it. We suggest rephrasing the bullet as: “*UE does not need to make use of existing channel encoder for the new PUCCH format*”   Furthermore, we would appreciate if we could also capture the big concern we expressed in our previous comment about error detection at gNB when UCI payload size is larger than 2 (and ACK/NACK bits are not the only UCI component in the payload). To be even more specific, please note that we are not referring to DTX-> ACK, ACK mis-detection or NACK->ACK probabilities, but to the capability of gNB to understand when a decoded UCI payload (of size larger than 2 bits) is wrong, i.e., unreliable, after error correction. All legacy PUCCH formats supporting payload sizes larger than 2 offer this possibility. Indeed, this aspect has been heavily discussed during the corresponding Rel-15 discussions. In this context, we have that inherent RM coding structure provides means for the gNB to test UCI payload reliability when 2<X≤11, whereas CRC bits serve the same purpose (albeit in a different way) when X>11. We thus propose to add two extra bullets in the “Impact to the receiver” section, such as   * *new PUCCH format does not include CRC bits. If X>11, gNB would need to change existing implementation relying on the presence of CRC bits to test UCI payload reliability, e.g., error detection after error correction;* * *gNB would need to change existing implementation relying on the inherent RM coding structure to test UCI payload reliability when 2<X≤11*.   We note that this a further motivation for which agreeing on more specific value(s) for X is important at this stage (it determines which error detection capability the gNB is able to rely on). Finally, it may be worth observing that this observation specifically applies to the DMRS-less PUCCH format, given that all other considered enhancements in this document would still allow gNB to operate with no additional impact on the error detection after error correction. |
| Qualcomm | Please find a few additional remarks in response to Intel and Nokia’s response above.   * Use-case is to describe the scenarios for which the proposed scheme is intended for. To say that there is no use-case for a particular scheme does not make sense. Perhaps, use-case is being mixed up with the pros and cons of a proposed scheme. If it helps, we can reword as follows: “Aimed at enhancing the coverage of PUCCH with small and medium-size UCI.” The intent here is clear, and we can debate whether the proposed scheme lives up to the intent or not. @Intel, if we have misunderstood your proposed wording, please let us know. * The proposed technique is equally applicable to short and long format PUCCH. We do not support imposing a restriction on the format at this stage. We don’t support stating that long format is a prerequisite for this scheme. Whether short formats need enhancement or not is a question to be answered in sub-agendas 8.8.1.1 and 8.8.1.2 and we do not want to presuppose the outcome of that discussion in this sub-agenda. * As a few companies have already mentioned, using existing NR RM coding does not work for DMRS-less PUCCH as it generates codewords that only different in phase. A modification of the existing NR RM coding is needed. The sentence added by Intel is incorrect technically and should not be captured in the TR. * For conventional coherent receiver, noise or interference estimation is needed for an MMSE receiver. We don’t see the need for noise estimation for DMRS-less PUCCH as the receiver is just a correlator. If the “noise/interference estimation” referred by Intel is for correlation/energy metric for DTX detection, it is then already included in the DTX detector. * We prefer to retain the sentence “UE does not need to implement channel encoder for the new PUCCH format” as is. It is clear in what it conveys. Not too sure why Intel wants to edit this line. * The following sentence “UE implementation effort for this new PUCCH format can be reduced by reusing Rel-15/16 CGS/ZC/Gold/m-sequences, comparing with new PUCCH format based on introduced new sequences or modification of Rel-15/16 UCI encoding” is a crucial observation, and we would like to have it captured in the TR. * We prefer to retain the bullet on simplication of UE implementation if we use one of the sequence generation mechanisms that are already available in NR. We suggest retaining “UE implementation effort for this new PUCCH format can be reduced by reusing Rel-15/16 CGS/ZC/Gold/m-sequences, comparing with new PUCCH format based on introduced new sequences or modification of Rel-15/16 UCI encoding”. * An upperbound on X is desirable. One option is 24 bits --- this is to accommodate L1 beam report in FR2 that carries information regarding the best two beams. * Regarding the bullet on receiver complexity, we think this bullet is important. There appears to be a general perception that DMRS-less sequence based PUCCH “always” requires larger receiver complexity than existing PUCCH formats. This bullet intends to clarify that this is not always the case. Given that we may also debate the value of X, we see value in retaining this bullet. * Regarding comparisons to PF0 and gNB’s handling of DMRS-less PUCCH, we think capturing this in some form is important. It appears that today’s gNB is already able to cope with a scenario where PUCCH does not have DMRS. We are hoping that some of the overall archirectural aspects that are already part of a gNB today can also be extended to the new seq-based PUCCH. For e.g., a gNB can already handle time/freq tracking loops when PF0 is used. The solutions put in place for this purpose, can act as a starting point. * There also appears to be a perception that seq-based PUCCH hinders interference suppression. Contrary to this perception, we show in our revised tdoc (R1-2009552) that seq-based PUCCH is infact more robust to inter-cell interference than NR PF3. The primary reason appears to be poor channel estimation quality in NR PF3 that hinders effective interference suppression. Seq-based PUCCH completely sidesteps these complications. It will be good to capture this in the TR and clarify that preliminary analysis shows that seq-based PUCCH may be more robust to inter-cell interference. * Regarding changes to existing implementations for error detection, we think that any implementation that is currently in place for RM codes can be repurposed for seq-based PUCCH as well. To clarify further, we have in mind a correlation-based metric with thresholding to address the issue of erroneous decoding when using RM codes. This same approach can be applied to seq-based PUCCH. Nevertheless, this is being investigated by companies and we can revisit this once additional results are available. |
| EURECOM | * With respect to performance gains in Table 1, and in particular PAPR, we believe companies should all clearly state the modulation assumption (i.e. pi/2-BPSK or QPSK) or provide results for both. * We strongly agree with ZTE's (and Qualcomm's) comment on the use-case for DMRS-less PUSCH. Furthermore, we agree with Qualcomm's rewording. * We believe that the receiver complexity is an important issue to highlight in the TR since several companies have provided near-ML decoding low-complexity schemes for DMRS-less PUCCH. * Sequence-based PUCCH can be made very resistant to inter-cell interference by properly choosing sequences across cells. Some companies have already demonstrated or indicated this for their schemes. We think that this should be reflected in the TR. * We agree with Qualcomm's comments that an upper-bound on X should be given for DMRS-less PUCCH. We should also consider the use of more than one PRB to achieve moderate X (e.g. 24 bits as suggested). |
| Nokia/NSB | We would start by making a general statement. This proposal aims at adding the support to a new PUCCH format, for which new solutions will have to be developed at both UE and gNB. In this sense, the complexity of both gNB’s and UE’s implementation will not be reduced, regardless of the complexity of the new solutions. Hence, if on the one hand it is correct to discuss about the extent of this complexity increase, if any (and we are doing this), on the other we should also aim at being clear in every description related to this enhancement. Currently this is not the case. Many sentences would be ambiguous from the point of view of the casual reader.   * Concerning the part related to noise, interference and channel estimation, we still think the current sentence is not accurate. Indeed, we are not sure we understand the spirit of the proposal. As we said above, the goal should be to state clear concepts, without ambiguity. What we can say for sure is that the new format will not make use of existing estimation blocks. However, such blocks will have to be implemented at gNB anyway, unless support to all existing PUCCH formats is removed. This is what would happen in a gNB, and from our perspective this is what should be described for the sake of clarity. Both our previous preference and proposal still hold. * Similarly, the new PUCCH format does not make use of any of the existing channel encoders at the UE. Something else will have to be added on top of the existing implementation. This something else will not make use of the existing channel encoders. The sentence “UE does not need to implement channel encoder for the new PUCCH format” says something stronger, and it is not accurate from our perspective. Both our previous preference and proposal still hold. * We understand Qualcomm’s position on complexity and indeed there could be that risk. On the other hand, it is true that complexity depends on the considered architectures. The corresponding bullet could be rephrased as “Computation efficient implementations of the decoder for the new DMRS-less PUCCH have been studied. Their complexity can be lower or higher than the decoder for existing NR PUCCH coherent receiver depending on the adopted sequence, on the UCI payload size and on the implementation of the considered coherent receiver” * We still do not see the point of comparing PF0 and the new DRMS-less format. The new format is not an evolution/complication of the latter, but something different in structure and purpose (in terms of payload size, at the very least). There are many other blocks which will be in common between all the PUCCH formats, e.g., IDFT/DFT for waveform generation and reception, cyclic prefix insertion and removal and so on. It does not make a lot of sense to list them all (or any of them, actually). Additionally, we do not have a reference gNB architecture, with reference operations and algorithms. * Concerning the interference suppression. From our understanding the discussion about interference (as per above comments) was not related to the robustness of the format to interference, as compared to PF3. Indeed, if any statement about robustness to interference were to be made w.r.t. existing formats, maybe RAN1 should have studied other formats as well, e.g., PF4 which natively support multiple UEs sending PUCCH at the same time. Conversely, what was mentioned above is the capability of gNB to use DMRS to acquire additional information on the channel (reliable or not this information may be) which is subsequently used for other operations. In this sense, the point that should be captured is that the absence of DRMS may prevent existing solutions (mentioned above) to be used with the new format. * On the error detection, we are not sure we can state what has been stated. Wouldn’t the problem be different depending on which sequences are used for the DMRS-less format? Wouldn’t the algorithm used to detect an error change if FHT is used or not? It would be good if we could clarify this. |
| Qualcomm | Response to Nokia’s comments:  It is important to draw parallels to PF0 and this scheme. It is being projected that the lack of DMRS is a major issue for the gNB. While this may be the case, we want to make it clear that existing gNBs already know how to cope with such scenarios. We don’t see this as an unsurmountable obstacle. Not being able to leverage the exact implementation of PF0 is not what is being debated here, it is about being able to leverage the overall architecture around PF0. In this context it is important to make these connections.  Regarding interference suppression, what we are saying is that gNB is better off not pursuing interference suppression given how poor channel estimates are. gNB is doing more harm than good in pursuing such an approach. We are giving the gNB a way out. This needs to be recognized. Continuing support for existing gNB operations that are actually detrimental to performance should not be classified as a “missing feature” for the new scheme.  We see no issue with the statement “UE does not need to implement channel encoder for the new PUCCH format”. RM has an encoder, Polar has an encoder, the seq-based approach does not need one. This is what is being stated here. Its clear.  Regarding noise/interference estimation, what we are saying is that these blocks are not necessary while implementing the receiver for the new format. They may be needed for ancillary purposes such as DTX detection and this is a shared cost across formats, not something new to this scheme. |
| Nokia/NSB | There is no point in listing things that one does not need to implement if they are clear. What is of interest is whether a new feature can completely eliminate something that was there before (highly unlikely), or if there is some component that one might expect to be there, but is actually not needed (which is not the case here). Hence listing things that the new format does not imply as needed to be implemented is pointless and endless, and we strongly object taking such direction with any and all proposals. |
| Huawei, HiSilicon | We have the following comments:  1. For the use case, it is the target scenario of the scheme, which should not be overlapped with performance gain. This is also mentioned by Qualcomm and ZTE. So, we may use “To enhance coverage of PUCCH with small and medium UCI size” as the use case.  2. For the value of X, considering the detection complexity, we suggest the value of X should not be larger than 11. In fact, most company provided simulation of DMRS-less PUCCH transmission for 11 bits UCI. Maybe the value of X can be 11. In this case, gNB does not need to change existing error detection based on CRC bits for UCI>11 bits. Furthermore, for Intel’s comment, since this proposal is for DMRS-less PUCCH, we don’t need to add “if DMRS-less PUCCH is introduced” here.  3. For DMRS-less PUCCH discussion, both long and short formats are fine for us.  4. For the performance evaluation, we will provide our results with DTX detection. For the error detection, a similar approach can be applied for DMRS-less PUCCH by comparing the correlation.  5. By using existing NR RM code, it will generate some sequence pairs with a same phase difference between corresponding entries of the two sequences. Thus, using existing NR RM code does not work for DMRS-less transmission.  6. For Impact to receiver,   * For “Need to implement a ML non-coherent sequence detector/correlator for the new PUCCH format”, in fact, due to the high complexity of ML decoding for larger UCI payload, some low complexity detection methods may also be used at the receiver. Thus, we suggest not to limit the receiver to ML detector, i.e., any non-coherent sequence detector/correlator is fine for receiver. * For noise/interference estimation, we think it is not needed for DMRS-less transmission. For DTX detection mentioned in Intel’s comment, it can be done based on sequence correlation, which is the output of non-coherent sequence detector/correlator. * As mentioned above, low complexity detection may be used for larger payload size, and therefore we cannot limit the receiver implementation to an extension of the PUCCH format 0. * Although there is no DMRS for DMRS-less PUCCH, it does not means that DMRS-less PUCCH have worse performance with inter-cell interference compared to NR PUCCH with DMRS.   7. For impact to UE implementation, if Rel-15/16 CGS/ZC/Gold/m-sequence is used, number and length of sequences may be different from that in Rel-15/16. Thus, reusing the existing UE implementation of Rel-15/16 CGS/ZC/Gold/m-sequence may not be possible. So, it is not clear about the impact to UE implementation.  8. Replace “spec impact” with “potential spec impact”, because the list is not the final spec impact but an assessment of potential spec impact.  To conclude, we have the following suggested modifications for the proposal:  **Proposal 2: For DMRS-less PUCCH, capture the following in the TR**  **Use case:** To enhance coverage of PUCCH with small and medium UCI size  **Restriction of the scheme:** up to X UCI bits where X≤11, and the exact value of X is FFS  **Prerequisite of the scheme:** None  **Performance gain:** captured in Table 1  **Potential Spec impact:**   * A new PUCCH format needs to be specified, including the power control of the new PUCCH format. The new PUCCH format is an addition to existing PUCCH formats. * if reusing Rel-15/16 CGS/ZC/Gold/m-sequence, no new sequences need to be specified. If new sequences or sequences based on modification of NR Rel-15/16 UCI encoding scheme are adopted, the new sequences or the modification of NR Rel-15/16 UCI encoding scheme need to be specified. * UCI to sequence mapping and Sequence to RE mapping need to be specified * UCI size (X) needs to be specified * [New RAN4 MPR requirement needs to be defined, if new sequences other than Rel-15/16 CGS/ZC/Gold/m-sequences are adopted] * [CSI and HARQ-ACK multiplexing for this new PUCCH format need to be specified]   **Impact to receiver:**   * Need to implement a ~~ML~~ non-coherent sequence detector/correlator for the new PUCCH format. * No need to implement channel and noise estimation in the receiver for the new PUCCH format * ~~Receiver implementation for the new PUCCH format is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool.~~ * The complexity of the ~~ML~~ non-coherent sequence detection/correlation increase with larger UCI size. * Computation efficient implementations are available with certain choice of sequences to reduce receiver complexity. Depends on UCI size, selected sequences, and implementation of ~~ML~~ noncoherent and conventional coherent receiver, ~~ML n~~Non-coherent sequence detector may have larger or smaller complexity than conventional NR PUCCH coherent receiver. * [Receiver sensitivity to time/frequency error: ~~ML~~ non-coherent sequence detector is more robust to timing and frequency than conventional NR PUCCH coherent receiver] * [Similar to PUCCH format 0, the new PUCCH format does not have DMRS for interference suppression and tracking loops]   **Impact to UE implementation**   * UE does not need to implement channel encoder for the new PUCCH format * ~~UE implementation effort for this new PUCCH format can be reduced by reusing Rel-15/16 CGS/ZC/Gold/m-sequences, comparing with new PUCCH format based on introduced new sequences or modification of Rel-15/16 UCI encoding~~   **[Impact to system]**   * [FFS the impact to system] |
| OPPO | The specification impact would be small if most of existing format is just “enhanced”. Not necessarily new format. Since all the text change to the potential impact, it is ok. Just remind that we are not have to introduce new format. |
| Nokia/NSB | **Proposal 3-2**  We suggest rephrasing the first bullet as follows  A new PUCCH format would need to be specified, including the power control of the new PUCCH format. The new PUCCH format would be an addition to existing PUCCH formats.  **Proposal 3-3**  \* We keep thinking that referring to PF0 to talk about the new format is not meaningful in this context. After all:  (i) with reference to the third bullet of the proposal: the first bullet of the proposal already states that the new PUCCH format requires non-coherent detection, hence the information is given already, and the corresponding part of the third bullet is redundant. In this regard, it may also be worth reminding what was already argued earlier that some concepts are just clear and do not need to be stated explicitly. At least the first reference to PF0 seems to qualify in this sense as well, it states the obvious, whereas the focus should be on the new format.  (ii) with reference to the last bullet of the proposal: we are not sure we can claim that RAN1 performed extensive receiver architecture comparisons with PF0, including efficient architectures and so on.  Therefore, we think we should just focus on what has been studied and, more importantly, on the matter at hand, i.e., the new format as such. This would be for the sake of pragmatism and simplification.  \* Partially related to the above observations, the bullet between brackets is very convoluted and not very homogeneous. It seems to mix different aspects, e.g., at least (i) operations gNB may perform if DMRS is/are present and (ii) robustness of the format against inter-cell interference. It may be good to separate them. Besides, we are not sure the initial comment about tracking, at the very beginning of the discussion, referred to time and frequency (offset?) tracking, but rather to channel tracking. Maybe Ericsson can clarify this? |
| ZTE | Support the proposal.  For the following bullet in brackets for Proposal 3-1, we propose to change it in a more general way, since SR multiplexing with CSI/HARQ-ACK should be also considered.  [~~CSI and HARQ-ACK~~ UCI multiplexing for this new PUCCH format need to be specified] |
| Intel | For use case, depending on our simulation results, we do not see much gain of DMRS-less scheme compared to exiting PF3. Suggest to put this in [] or capture the observations from all companies as we initially suggested.  For “Prerequisite of the scheme”, as mentioned previously, we would like to consider long PUCCH format as Prerequisite of the scheme. Note that based on baseline performance study, we only agree to study long PUCCH format for analysis. It is unclear to us whether we need to consider coverage enhancement of short PUCCH format without baseline analysis.  For “if reusing Rel-15/16 CGS/ZC/Gold/m-sequence, no new sequences need to be specified.”, we are not sure whether this is correct statement. It depends on detailed sequence design. For instance, CGS is only for short length and if long sequence is considered for PF3, we need to redesign CGS sequence for long sequence. For ZC sequence, only length with integer of 12\*2^i\*3^j\*k is defined in the spec. For PF3 with duration of 7 symbols, we need to redesign the ZC sequence. For m-sequence, it is only for SS in NR. We need to design a new sequence for m-sequence of PF3 with different lengths. We suggest to remove this. Same is applied for “Rel-15/16 CGS/ZC/Gold/m-sequences implementation can be reused in the new PUCCH format implementation, if Rel-15/16 CGS/ZC/Gold/m-sequences is adopted to support the new PUCCH format.”  For “The new PUCCH format does not require channel and noise estimation to be received.” For DTX detection, we understand this can be performed based on correlation of sequence and input signal, but how to select appropriate threshold for DTX detection? This depends on SNR or noise estimation. Hence, we suggest to remove this or mention “noise/interference estimation for DTX PUCCH detection is needed”. For DMRS based algorithm, it is very clear that noise and interference estimation can be done using DMRS. however, for DMRS-less scheme, it is unclear to us how to implement this. It is good to clarify.  Further, as mentioned by Ericsson, gNB may not be able to use DMRS for channel tracking for DMRS-less PUCCH scheme. We need to also capture this in the TR.  Here is our suggestion for the update:  **Proposal 3-1: For DMRS-less PUCCH, capture the following in the TR**  **Use case:** [Aim to enhance coverage of PUCCH with small and medium UCI size]  **Restriction of the scheme:** up to X UCI info bits where X is FFS  **Prerequisite of the scheme:** ~~None~~ long PUCCH format  **Performance gain:** captured in Table 1, where Table 1 is subject to change based on new simulation results  **Proposal 3-2: For DMRS-less PUCCH, capture the following in the TR**  **Potential Spec impact:**   * A new PUCCH format needs to be specified, including the power control of the new PUCCH format. The new PUCCH format is an addition to existing PUCCH formats. * ~~if reusing Rel-15/16 CGS/ZC/Gold/m-sequence, no new sequences need to be specified.~~ If new sequences or sequences based on modification of NR Rel-15/16 UCI encoding scheme are adopted, the new sequences or the modification of NR Rel-15/16 UCI encoding scheme need to be specified. * UCI to sequence mapping and Sequence to RE mapping need to be specified * [UCI info bits size (X) needs to be specified] * [New RAN4 MPR requirement needs to be defined, if new sequences other than Rel-15/16 CGS/ZC/Gold/m-sequences are adopted] * [CSI and HARQ-ACK multiplexing for this new PUCCH format need to be specified]   **Proposal 3-3: For DMRS-less PUCCH, capture the following in the TR**  **Impact to receiver:**   * Need to implement a non-coherent sequence detector/correlator for the new PUCCH format. * ~~The new PUCCH format does not require channel and noise estimation to be received.~~ * Noise/interference estimation for DTX detection is needed for the new PUCCH format. * Receiver implementation for the new PUCCH format is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool. The size of the sequence pool over which the receiver for the new PUCCH format needs to perform correlation increases exponentially with the number of UCI bits. * Computation efficient implementations of the receiver for the new PUCCH format have been studied. Their complexity can be lower or higher than the decoder for existing NR PUCCH coherent receiver depending on the adopted sequence, on the UCI payload size and on the implementation of the considered coherent receiver. * [Similar to PUCCH format 0, the new PUCCH format does not have DMRS for interference suppression and tracking loops. Two companies raised concern that absence of DMRS in the new PUCCH format may hinder feasibility of advanced interference suppression and tracking loops. Regarding the inter-cell interference suppression, one company pointed out the sequence based PUCCH can be resistant to inter-cell interference by properly choosing sequences across cells. One company compared the performance of the new PUCCH format with NR PF3 and observed that the new format is more robust to inter-cell interference than NR PF3. Regarding the time and frequency tracking loops, one company compared the performance of the new PUCCH format with NR PF3 and observed that the new format is more robust to timing error and frequency error than NR PF3.]   **Proposal 3-4: For DMRS-less PUCCH, capture the following in the TR**  **Impact to UE implementation**   * UE does not need to make use of existing channel encoder for the new PUCCH format * ~~Rel-15/16 CGS/ZC/Gold/m-sequences implementation can be reused in the new PUCCH format implementation, if Rel-15/16 CGS/ZC/Gold/m-sequences is adopted to support the new PUCCH format.~~   **[Impact to system]**   * [FFS the impact to system] |
| Samsung | In proposal 3.2,  Consider to clarify as below, or simply remove the part in square brackets (no spec impact is needed)   * [if reusing Rel-15/16 CGS/ZC/Gold/m-sequence with same length, no new sequences need to be specified.] If new sequences or sequences based on modification of NR Rel-15/16 UCI encoding scheme are adopted, the new sequences or the modification of NR Rel-15/16 UCI encoding scheme need to be specified.   In proposal 3.3.   * Need to implement a non-coherent sequence detector/correlator for the new PUCCH format. * The new PUCCH format does not require channel and noise estimation to be received. * ~~Receiver implementation for the new PUCCH format is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool.~~ The size of the sequence pool over which the receiver for the new PUCCH format needs to perform correlation increases exponentially with the number of UCI bits. *(move this part to 1st bullet)*   The part in square brackets could be simplified, listing only the potential implications. But we are fine with current version if majority agrees. |
| Ericsson | **Some quick suggestions in view of the tight deadline:**  **Proposal 3-3: For DMRS-less PUCCH, capture the following in the TR**  **Impact to receiver:**   * Need to implement a non-coherent sequence detector/correlator for the new PUCCH format. * ~~The new PUCCH format does not require channel and noise estimation to be received.~~ * DTX detection needs to be modified to support the DMRS-less operation in the new PUCCH format. * Receiver implementation for the new PUCCH format is an extension of the PUCCH format 0 receiver with similarity that both are noncoherent sequence detectors, while the new receiver needs to perform correlation over a larger sequence pool. The size of the sequence pool over which the receiver for the new PUCCH format needs to perform correlation increases exponentially with the number of UCI bits. * Computation efficient implementations of the receiver for the new PUCCH format have been studied. Their complexity can be lower or higher than the decoder for existing NR PUCCH coherent receiver depending on the adopted sequence, on the UCI payload size and on the implementation of the considered coherent receiver. * Noise/interference estimation needs to be modified for the new PUCCH format. * Channel measurement and tracking needs to be modified to support DMRS-less operation in the new PUCCH format. * [Similar to PUCCH format 0, the new PUCCH format does not have DMRS for interference suppression and tracking loops. Two companies raised concern that absence of DMRS in the new PUCCH format may hinder feasibility of advanced interference suppression and tracking loops. Regarding the inter-cell interference suppression, one company pointed out the sequence based PUCCH can be resistant to inter-cell interference by properly choosing sequences across cells. One company compared the performance of the new PUCCH format with NR PF3 and observed that the new format is more robust to inter-cell interference than NR PF3. Regarding the time and frequency tracking loops, one company compared the performance of the new PUCCH format with NR PF3 and observed that the new format is more robust to timing error and frequency error than NR PF3.]   **Proposal 3-4: For DMRS-less PUCCH, capture the following in the TR**  **Impact to UE implementation**   * ~~UE does not need to make use of existing channel encoder for the new PUCCH format~~ * UE encodes UCI using a new scheme * Rel-15/16 CGS/ZC/Gold/m-sequences implementation can be reused in the new PUCCH format implementation, if Rel-15/16 CGS/ZC/Gold/m-sequences is adopted to support the new PUCCH format.   **[Impact to system]**   * [FFS the impact to system] |
| Qualcomm | @Ericsson, FL,  If current DTX detection is based on using the DMRS + reencoded UCI, then no change to this block is necessary. If on the other hand, only DMRS is currently used for DTX detection, then yes, some changes are necessary. We suggest rewording as follows:   * Depending on the type of DTX detection implemented for legacy PUCCH formats, some modification may or may not be necessary to support the DMRS-less operation in the new PUCCH format. For example, if current implementations rely on DMRS for DTX detection, some changes are necessary.   Regarding the comment on noise/interference estimation: we don’t see what is needed here. With seq-based approach, we do not perform noise/interference estimation. Are we missing something here?  Similar comment for channel estimation/tracking. Is Ericsson thinking of data-aided channel estimation? If not, we don’t see what is needed here. We are assuming that channel tracking will simply skip this PUCCH instance.  Prefer no restriction on formats at this stage.  Regarding noise estimation for DTX detection, our understanding is that the DTX threshold at the gNB is a static or semi-static threshold. A threshold can be pre-selected/pre-measured. In particular, for a certain deployment scenario, a threshold can be selected based on the 1% tail of the noise/interference measurement at the gNB in the unused UL resources. There is no need for the gNB to dynamically estimate interference or noise for each received DMRS-less PUCCH in order to determine the DTX threshold. A table in gNB can store multiple threshold values for different deployment scenarios. gNB just needs to take a value from the table based on the deployment scenario and use it as DTX threshold. A slow adjustment/fine-tuning of the threshold could be done by a diligent gNB, which can follow similar approach as link adaptation.  In fact, we are concerned that adjusting the DTX threshold dynamically for each PUCCH may lead to failure of 1% FA requirement. It is not clear to us why adjusting the DTX threshold dynamically can help meet 1% FA. gNB vendors can further confirm whether this is the case, or clarify what is their approach to DTX detection for PUCCH Format 0.  Text in Proposal 3-3: We are in general fine with drawing parallels and comparisons to PF0. The text serves to provide more context and nuance for a new reader who may not be entirely familiar with this discussion. A little redundancy does not hurt. We don’t wish to spend more time finessing this point. |
| Nokia/NSB | We still do not see the point of drawing parallels and comparisons to PF0. This is arbitrary and done only for this enhancement. A new reader, not entirely familiar with any discussion we are having in all AI 8.8.2.x AIs, would need context and nuance for all proposals to have a better understanding. However, that’s not the purpose of this exercise from our perspective. We should strive to have streamlined descriptions related to the specific enhancement at hand. If specific studies were performed on the possible architectural comparisons with PF0, and/or if PF0 was agreed to be a baseline for performance evaluation, then of course the situation would be different.  Concerning the bullet on DTX detection, and abiding to the logic above, we would also suggest streamlining it to capture the essential part of what is currently said:   * Changes to existing implementation for DTX detection at the receiver may be necessary if the latter relies on the presence of DMRS to this end. |
| Intel | @Qualcomm, regarding the DTX threshold, our understanding is that for DMRS based scheme, receiver can estimate the SNR using DMRS. Then the receiver can use the estimated SNR for DTX detection to determine a threshold from a threshold table. The threshold table can be pre-calculated and stored at the receiver.  For DMRS-less scheme, our understanding is that the receiver would need to do similar exercise, e.g., estimate the SNR and determine the appropriate threshold. This certainly depends on gNB implementation. But without DMRS, we share similar view as Nokia that the change is definitely needed for DTX detection compared to the existing PF.  We are fine with Nokia’s suggestion, but would like to add  Changes to existing implementation for DTX detection at the receiver, including noise and interference estimation, may be necessary if the latter relies on the presence of DMRS to this end. |
| FL | Thanks for the discussion.  Regarding the debate on noise estimation. As a FL, I think it is important to make sure companies are on the same page and discussion the same “noise estimation”  There are three types of noise estimation in my view   1. Long term noise power estimation – this can be done by gNB from time to time in background 2. Instantaneous noise power estimation – this has to be done for each PUCCH on the fly 3. Instantaneous noise covariance matrix estimation – this is needed to do MMSE based equalization in coherent receiver. I think it should be common understanding that is not needed for non-coherent receiver.   Now, to help settle down the debate between QC and Intel whether noise estimation is needed to choose DTX detection threshold. If I understand correctly, QC’s point is that the DTX threshold selection is semi-static, which can be based on long term noise power estimation. No instantaneous noise estimation or SNR estimation is needed. Intel’s point is that the DTX detection threshold select has to be dynamic per PUCCH, and the selection is based on SNR estimation or instantaneous noise power estimation.  Companies are welcome to share your view on this and continue the discussion. But as a FL, my view is that: despite which approach is better or make more sense, at the end, this is just gNB implementation. Maybe we can capture it as following: To determine the DTX detection threshold, depends on gNB implementation, instantaneous noise power estimation may or may not needed. Can companies please check if this is acceptable? |

## 2.4 PUSCH repetition Type-B like PUCCH repetition

One company provided LLS results for this scheme. The following table is extracted from [23].

Table 2: Performance gain observed for PUSCH repetition Type-B like PUCCH repetition

|  |  |  |
| --- | --- | --- |
| Company | Observed performance gain | Key simulation assumptions |
| VIVO | 0.5dB (w/o DMRS bundling)  1~1.5dB (w DMRS bundling) | 11 bits UCI, w/ DTX detection, 1% BLER  Receiver for Rel-15/16 PUCCH: coherent detection, DTX is performed based on union of DMRS and UCI symbols, as shown in figure 2(b) of our updated contribution, R1-2009648.  Receiver for PUCCH enhancement scheme: with and without joint channel estimation for the consecutive PUCCH repetitions, in addition to receiver for Rel-15 and Rel-16 UEs. |

Besides the LLS simulations to study the gain of the scheme, a few other aspects of the schemes are also discussed/studied:

* The spec impact of the scheme is discussed in [4][6]
* Restrictions to apply the scheme in certain scenarios such as >11 bits UCI [4]
* Some design details of the scheme are discussed in [9][20]

Based on the input from companies in Section 4.2, the following proposal is made.

**Proposal 4-1: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**

**Use case:** Aim to reduce PUCCH latency and improve the efficiency of uplink symbols utilization in TDD structure. But its benefit to coverage enhancement is not clear. [The scheme may only be beneficial for short PUCCH repetition.]

**Restriction of the scheme:**

* Only applicable to UCI <=11 bits
* [Only applicable to actual PUCCH repetitions in a same PUCCH format]

**Prerequisite of the scheme:** None

**Performance gain:** Captured in Table 2, where Table 2 is subject to change based on new simulation results

**Proposal 4-2: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**

**Potential Spec impact:**

* Nominal repetition, actual repetition, segmentation for type B PUCCH repetition, and flexible time domain resource allocation in each slot need to be specified
* Procedure to handle postpone/cancel PUCCH repetitions (including interaction with dynamic SFI) needs to be specified
* [PUSCH type B repetition specification can be leveraged]
* ~~[Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified, if 1/2/3 OFDM symbol actual type B PUCCH repetition is supported]~~
  + ~~[Potentially new DMRS patterns need to be specified]~~
* The issue of whether supporting type B PUCCH repetitions with different PUCCH formats was studied and three options were identified to resolve this issue:
  + Option 1: Restrict type B PUSCCH repetition applicable to actual repetitions with the same PUCCH format.
  + Option 2: Allow type B PUCCH repetition with different PUCCH formats. The procedure to handle format switch between repetitions needs to be specified.
  + Option 3: Introduce and specify PUCCH format 3/4 of length 1/2/3 OFDM symbols to support type B PUCCH repetition.
* Procedure and RAN4 requirements to handle different PUCCH formats (with potential switching between different waveforms of OFDM and DFT-S-OFDM) cross actual repetitions needs to be specified
* Power control for actual repetitions needs to be specified
* [CSI and HARQ-ACK multiplexing with type B PUCCH repetition need to be specified]

**Agreements: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**

**Impact to receiver:**

* gNB needs to process more than one PUCCH repetitions in a slot
* gNB needs to combine multiple repetitions with different code rates/time length

**Proposal 4-4: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**

**Impact to UE implementation**

* UE needs to implement PUCCH postponement/cancellation procedure
* UE needs to implement PUCCH repetitions with different code rates/time length
* UE needs to implement transmissions of more than one PUCCH repetitions in a slot
* [UE may need faster PUCCH processing capability than normal eMBB UE]

**[Impact to system]**

[FFS the impact to system]

Comments to the above FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| Ericsson | Similar to FL comment, it would be good to clarify if this is only for short PUCCH repetition. Also, is this one scheme or many? That is, are all spec impacts required for all proposals? |
| Qualcomm | If repetitions across slot boundaries, then phase continuity issues come up. Prefer to take a cautious approach in this case, and seek RAN4 input first. |
| Samsung | The proposal for Type-B like PUCCH repetitions intends to leverage for PUCCH the Rel-16 support for PUSCH. Almost all aspects mentioned by the FL already exist for PUSCH Type B repetitions.  The PUCCH format for different repetitions need not be different but, even if it is, that does not matter as decoding is based on soft bits and the gNB can receive any PUCCH format that has a corresponding configured PUCCH resource.  Procedure to handle postpone/cancel PUCCH repetitions is already specified in Rel-15.  Support is intended to be limited to below 12 bits (repetition coding or RM coding) – the impact on UE/gNB implementation relative to Type-B PUSCH repetitions is trivial. |
| Intel | * Regarding “use case”   + For “The scheme may only be beneficial for short PUCCH repetition.” We are not sure whether this is valid. Assuming special slot of 7 UL symbols and 14-symbol uplink slot, we can use this scheme to transmit 7-symbol long PUCCH with 3 repetitions, while existing PUCCH repetition scheme can only have 2 repetitions. It is clear that this is also beneficial for long PUCCH format. We suggest to remove this. * Regarding “Prerequisite of the scheme”,   + Similar the comment as above, we would like to consider long PUCCH format as Prerequisite of the scheme at least for NR Coverage enhancement SI/WI. * Regarding “spec impact”   + As mentioned in the first round of email discussion and also by other company, we also consider flexible time domain resource allocation in each slot for enhanced PUCCH repetition scheme, which can avoid the introduction of segmentation to some extent. We suggest to update the first sub-bullet as “Nominal repetition, actual repetition, and segmentation for PUCCH and flexible time domain resource allocation in each slot need to be specified   + For “Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified – Potentially new DMRS patterns need to be specified”, we are not sure which company proposed this for spec impact. Does this mean we would need to introduce a new PUCCH format for DFT-s-OFDM waveform with 1/2/3 OFDM symbols, e.g., for PF0 and PF2? And we would like to introduce a new PUCCH format to cover 3 symbol PUCCH? We suggest to remove this. |
| CATT | For the use case, we are not comfortable on the last sentence, i.e. ‘The scheme may only be beneficial for short PUCCH repetition’. It is well known long PUCCH format is the typical format in the coverage-limited scenario. Maybe it’s better to remove it instead of putting it in bracket if no justification is identified. |
| Intel | Some of our comments in the 1st round were not captured in the updated proposal. Copy below with some update:   * Regarding “Prerequisite of the scheme”,   + Similar the comment as above, we would like to consider long PUCCH format as Prerequisite of the scheme at least for NR Coverage enhancement SI/WI. * Regarding “spec impact”   + For “Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified if 1/2/3 OFDM symbol actual type B PUCCH repetition is supported – Potentially new DMRS patterns need to be specified”, we are not sure which company proposed this for spec impact. Does this mean we would need to introduce a new PUCCH format for DFT-s-OFDM waveform with 1/2/3 OFDM symbols, e.g., for PF0 and PF2? And we would like to introduce a new PUCCH format to cover 3 symbol PUCCH? The spec impact is substantial if we consider this. We strongly suggest to remove this.   + For the above one, we check the proposals from different companies but could not find the corresponding proposal. Can FL clarify which company proposed this? |
| Sharp | Use case: At least 8 companies (Samsung, Sharp, CMCC, LG, Vivo, Intel, Interdigital, WILUS) indicate utilization of uplink symbols in TDD structure. We propose to indicate “Efficient utilization of uplink symbols in TDD structure” as a use-case.  Spec impact: “Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified” may be required if we agree to restrict it to “Only applicable to actual PUCCH repetitions in a same PUCCH format”. If different PUCCH formats are applicable to type B, no new length 1/2/3 for PUCCH format is necessary. Therefore, we suggest to add square brackets to the statement of new length PUCCH format. |
| Qualcomm | Suggest simplifying use case to: “PUCCH type B repetition can reduce PUCCH latency and improve resource utilization efficiency”.  The additional statement (“But its benefit to coverage enhancement is not clear. [The scheme may only be beneficial for short PUCCH repetition.]”) on benefit can be captured under performance gain or under a new sub-bullet titled “Impact on coverage”.  Support removing square brackets around: [Only applicable to actual PUCCH repetitions in a same PUCCH format]  A UE may choose to support type-B reps for PUCCH but not for PUSCH since this is being discussed for eMBB traffic, while the latter was discussed in the context of URLLC. In such a case this is new effort for UE implementation. Prefer to list the requirements even if some overlap with PUSCH type B repetitions. |
| FL | Answer to Intel’s question:     * Regarding “spec impact”   + For “Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified if 1/2/3 OFDM symbol actual type B PUCCH repetition is supported – Potentially new DMRS patterns need to be specified”, we are not sure which company proposed this for spec impact. Does this mean we would need to introduce a new PUCCH format for DFT-s-OFDM waveform with 1/2/3 OFDM symbols, e.g., for PF0 and PF2? And we would like to introduce a new PUCCH format to cover 3 symbol PUCCH? The spec impact is substantial if we consider this. We strongly suggest to remove this.   + For the above one, we check the proposals from different companies but could not find the corresponding proposal. Can FL clarify which company proposed this?   [FL] Sharp has the following proposal, which infer there is an open issue of type B PUCCH: whether and how to support format 3 with 1/2/3 OFDM symbols?  **Proposal 4: Two actual repetitions with different PUCCH formats (e.g., one for format 2 and another for format 3) should be allowed for Channel segmentation.**  Maybe there is a confusion regarding the intension “of potential spec impact”. Let me clarify. The intension of “potential spec impact” to capture the potential spec impact, IF a scheme is adopted. Adding a bullet to capture the “potentially spec impact” of a scheme does not mean the scheme will be adopted in spec. Hope this clarify the confusion.  Now coming back to this particular issue, “whether and how to support format 3 with 1/2/3 OFDM symbols” is indeed an issue for type B PUSCH which requires some study of the spec impact. As a group, we should capture this issue and potential solution with potential spec impact in the TR. |
| Sharp | @Intel and FL  The following Sharp’s proposal does not indicate actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols, and we think it is sufficient to reuse conventional PUCCH formats.  **Proposal 4: Two actual repetitions with different PUCCH formats (e.g., one for format 2 and another for format 3) should be allowed for Channel segmentation.**  If “[Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified, if 1/2/3 OFDM symbol actual type B PUCCH repetition is supported]” is based on only our Proposal 4, we can remove it. |
| OPPO | We think the scheme is more or less require mini-slot like processing. Thus, in the UE implementation, the impact should include ”UE need shorter PUCCH processing capability than normal eMBB UE.” |
| vivo | For the main bullet, we agree with other companies that “[The scheme may only be beneficial for short PUCCH repetition.]” should be removed.  Besides, this scheme is also benefit for coverage, and the performance gain is straightforward, since more resources are utilized for PUCCH transmission, like other PUCCH repetition schemes. We suggest to remove the ‘But its benefit to coverage enhancement is not clear’. |
| Qualcomm | A few quick remarks:  Concerns on whether this scheme is able to help a 14-symbol PUCCH transmission remains (this was the baseline scenario). It can potentially help short PUCCH formats. Will be good to capture these concerns someplace.  Regarding the debate on 1/2/3 symbol repetitions, we don’t want to extend this current scheme to consider repetitions that also pick and choose the formats. It has a rather large spec impact with only marginal gains. How to address repetitions with < 4 symbols for the long PUCCH format is a concern and we’ll need to discuss this further. |
| Intel | Regarding “Proposal 4-1”, as we provided example before, assuming special slot of 7 UL symbols and 14-symbol uplink slot, we can use this scheme to transmit 7-symbol long PUCCH with 3 repetitions, while existing PUCCH repetition scheme can only have 2 repetitions. This is clear to improve the coverage compared to existing repetition scheme. We suggest to remove “But its benefit to coverage enhancement is not clear.”  Further, for Prerequisite of the scheme”, as mentioned previously, we suggest to consider long PUCCH format as Prerequisite of the scheme at least for NR Coverage enhancement SI/WI. Note that based on baseline performance study, we only agree to study long PUCCH format for analysis.  For Proposal 4-2, thanks for the clarification from Sharp. Based on this, we suggest to remove “ [Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified, if 1/2/3 OFDM symbol actual type B PUCCH repetition is supported] o [Potentially new DMRS patterns need to be specified]”.  For “Procedure and RAN4 requirements to handle different PUCCH formats (with potential switching between different waveforms of OFDM and DFT-S-OFDM) cross actual repetitions needs to be specified”, we are not sure whether we would introduce PUCCH format switching between nominal and actual repetitions. We suggest to put this in []. |
| Samsung | **Proposal 4-1: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**  **Use case:** Aim to reduce PUCCH latency and improve the efficiency of uplink symbols utilization in TDD structure. But its benefit to coverage enhancement is not clear. ~~[The scheme may only be beneficial for short PUCCH repetition.]~~  **Restriction of the scheme:**   * Only applicable to UCI <=11 bits * ~~[Only applicable to actual PUCCH repetitions in a same PUCCH format]~~   **Prerequisite of the scheme:** None  **Performance gain:** Captured in Table 2, where Table 2 is subject to change based on new simulation results  **Proposal 4-2: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**  **Potential Spec impact:**   * Nominal repetition, actual repetition, segmentation for type B PUCCH repetition, and flexible time domain resource allocation in each slot need to be specified * Procedure to handle postpone/cancel PUCCH repetitions (including interaction with dynamic SFI) needs to be specified * ~~[PUSCH type B repetition specification can be leveraged]~~ * ~~[Procedure to transmit actual repetition in DFT-S-OFDM waveform with 1/2/3 OFDM symbols needs to be specified, if 1/2/3 OFDM symbol actual type B PUCCH repetition is supported]~~   + ~~[Potentially new DMRS patterns need to be specified]~~ * Procedure and RAN4 requirements to handle different PUCCH formats (with potential switching between different waveforms of OFDM and DFT-S-OFDM) cross actual repetitions needs to be specified * Power control for actual repetitions needs to be specified * ~~[CSI and HARQ-ACK multiplexing with type B PUCCH repetition need to be specified]~~   **Proposal 4-4: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**  **Impact to UE implementation**   * UE needs to implement PUCCH postponement/cancellation procedure * UE needs to implement PUCCH repetitions with different code rates/time length * UE needs to implement transmissions of more than one PUCCH repetitions in a slot * ~~[UE needs faster PUCCH processing capability than normal eMBB UE]~~ |
| FL | Based on the comments received, for type-B PUCCH repetition, there is an open technical issue needs to be address. As Rel-15/16 does not support PUCCH format 3/4 (which is DFT-S-OFDM waveform) with 1/2/3 OFDM symbols. Now, for a PUCCH in format 3/4, what if a PUCCH actual repetition end up with 1/2/3 OFDM symbols? There are two options to take care of it.  Option 1: Live with the restriction that type-B PUSCCH repetition does not apply to repetitions with different PUCCH format. In other words, if a PF3 PUCCH is in type B repetition, it has to skip slots with only 1/2/3 consecutive UL OFDM symbols.  Option 2: allow type B PUCCH repetition with different PUCCH formats. Then for a PF3 PUCCH is in type B repetition, at least it can use slots with 1/2 UL OFDM symbols by switching to PF2 on those slots.  Option 3: Extend PF3/4 to 1/2/3 OFDM symbols.  As FL, I don’t have any preference here. But this open issue needs to be discussed and the outcome should be captured in the TR. |
| Sharp | In our view, those 3 options can be captured in the TR, possibly with Pros/Cons, given that down-selection seems difficult at this stage.  Our analysis on each option is as follows.   * Option 1: Pros: Lowest spec impact, Cons: New PUCCH repetition scheme doesn't support repetition with "S" slot with 2 UL symbols * Option 2: Pros: New PUCCH repetition scheme supports repetition with "S" slot with 2 UL symbols, Cons: PUCCH format switching needs to be adopted * Option 3: Pros: New PUCCH repetition scheme supports repetition with "S" slot with 2 UL symbols, Cons: New DMRS configuration needs to be determined for 1/2/3 symbol PF3   By the way, Sharp slightly prefer Option 2, which is also indicated in our contribution. |
| vivo | We are fine to capture these 3 options. But we suggest not to further discuss the pros and cons of these options in detail, which are not so urgent in SI phase. |
| WILUS | We have preference on option 1 that has lower specification impact. |
| OPPO | One question related to the some related content for spec. impact of DMRS-less. We have:   * [UCI info bits size (X) needs to be specified]   Then the other case should also have that. The type B we have common understanding that the bits should be limited to 11, otherwise there have to consider much more change in coding scheme or so. At least proposal 4-2 should add that.  We think the solutions for not supporting certain format should be discussed. Option 1 seems to be ok. Now we can list all 3. |
| Intel | Although it is a bit early to discuss the details in the SI phase, it may be good to share our views on this aspect. We prefer Option 1 as this simplifies the design and implementation substantially.  For Option 2, it requires PUCCH format switching between nominal and actual repetition. The benefit is not very clear for coverage enhancement if we have 2 symbols for actual repetition and when 14 symbols are allocated for nominal repetition.  For Option 3, the spec impact is significant. Note that we may not be able to extend PF3/4 to 1 OFDM symbols due to DFT-s-OFDM waveform and TDM multiplexing of DMRS and UCI symbols. |
| Samsung | In proposal 4-2, the details of how to transmit an actual repetition of 1/2/3 symbols (either option 2 or 3) are to be addressed in a WI phase. We suggest to either remove the original bullet or change to   * Procedures to transmit actual PUCCH repetitions with different PUCCH formats   In proposal 4-2, the reason we suggest to remove the last bullet is that the multiplexing of CSI and HARQ-ACK is a different topic and it does not need to be discussed in CovEnh or in the context of repetitions (such multiplexing is not currently supported for PUCCH repetitions)  In proposal 4-4, the last bullet is not meaningful/accurate and depends on proprietary implementation and the statement can be not true.  It is also noted that for all proposals the following are already supported   1. Procedure to handle postpone/cancel PUCCH repetitions 2. gNB needs to process more than one PUCCH repetitions in a slot (already the case in MIMO M-TRP and in URLLC) 3. UE needs to implement PUCCH postponement/cancellation procedure 4. UE needs to implement PUCCH repetitions with different code rates/time length (no issue as the UE can do this for different transmissions – no impact on UE implementation to do this if the transmissions are repetitions) 5. UE needs to implement transmissions of more than one PUCCH repetitions in a slot (already the case in Rel-15 for PUCCH with different UCIs, for MIMO M-TRP, and for URLLC – no impact on UE implementation if the transmissions are repetitions of same UCI) |
| Apple | In short, we are against this “PUSCH repetition type-B like PUCCH repetition” as we don’t see a good justification for such a specification for PUCCH repetition under CovEnh. Besides, the scope of Proposal is too large, for example is segmentation/nominal/actual repetitions within a slot allowed! In respect to all efforts and discussions so far, and to move forward, we are OK to consider it only for the case of SU, with all symbols in between usable for PUCCH, i.e. no intra-slot segmentation/repetition allowed. |
| CATT | We have the same feeling with Apple. As we commented at beginning, the motivation of Repetition type B like PUCCH is far from being justified.  It’s ok to capture it into TR just for information what we studied in SI, with the following modification:  **Proposal 4-1: For PUSCH repetition type-B like PUCCH repetition, capture the following in the TR**  **Use case:** Aim to reduce PUCCH latency and improve the efficiency of uplink symbols utilization in TDD structure. But its benefit to coverage enhancement is not clear. [The scheme may only be beneficial for short PUCCH repetition.]  **Restriction of the scheme:**   * Only applicable to UCI <=11 bits * [Only applicable to actual PUCCH repetitions in a same PUCCH format]   **Prerequisite of the scheme:** ~~None~~ The techniques applied to UCI>11 bits is not sufficient.  **Performance gain:** Simulation results without DMRS bundling ~~C~~captured in Table 2, where Table 2 is subject to change based on new simulation results  For the first change, the reason is that if the coverage of UCI larger than 11 bits can be guaranteed, there is no reason to further enhance a smaller UCI. The techniques should be payload agnostic, which is the common understanding from the very beginning.  For the second change, the simulation results with DMRS bundling is more relevant to DMRS bundling which is handled in section 2.5. |
|  |  |

## 2.5 (Explicit or implicit) Dynamic PUCCH repetition factor indication

Two companies provided simulation results for this scheme. The following table is extracted from [23].

Table 3: Performance gain observed for Dynamic PUCCH repetition factor indication

|  |  |  |
| --- | --- | --- |
| Company | Observed performance gain | Key simulation assumptions |
| Ericsson | 5 dB (with repetition factor 8) | 11 bits CSI, w/o DTX detection, 10% BLER  Receiver for Rel-15/16 PUCCH:  Receiver for PUCCH enhancement scheme: |
| ZTE | Reducing the number of PUCCH repetitions for more than 70% cases. | 11 bits UCI, w/o DTX detection, 1% BLER |

A point was raised in [19] that this scheme cannot be considered as an independent solution for PUCCH coverage enhancement, because this is only a scheme to enhance signalling which does not offer extra coverage.

Based on the input from companies in Section 4.3, the following proposal is made.

**Proposal 5-1: For dynamic PUCCH repetition factor indication, capture the following in the TR**

**Use case:** Aim to allow more flexible indication of PUCCH repetition factor to improve resource utilization efficiency. [But its benefit to coverage enhancement is not clear.]

**Restriction of the scheme:** None

**Prerequisite of the scheme:** None

**Performance gain:** captured in Table 3, where Table 3 is subject to change based on new simulation results

**Agreements: For dynamic PUCCH repetition factor indication, capture the following in the TR**

**Potential Spec impact:**

* a new PUCCH repetition signalling mechanism needs to be specified

**Impact to receiver: None**

**Impact to UE implementation:**

* Need implement transmissions of the PUCCH repetitions based on the dynamic indicator

**[Impact to system]**

* [FFS the impact to system]

Comments to the above FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| Ericsson | Regarding coverage vs. spectral efficiency: These two aspects are nearly indistinguishable. For example, we can use UL heavy TDD patterns to improve coverage, but we study only DL heavy TDD patterns in this study item. The reason is that we want the DL spectral efficiency. For dynamic PUCCH repetition, in a coverage scenario we may want say 8 repetitions, but an 8x constant increase in PUCCH overhead is not desirable, and dynamic control of repetition factor can solve this problem. Suggest for use case: ‘More flexible indication of PUCCH repetition factor to improve coverage while maintaining spectral efficiency’. |
| Samsung | Largely agree with the FL comments. Support for PUCCH repetitions in Rel-15 was imported from LTE Rel-8 that was designed for 1 HARQ-ACK bit. As a result, that support is broken for the purposes of NR.  Regarding the impact on coverage, there isn’t any if the number of configured repetitions is always the maximum one corresponding to the maximum possible UCI payload that RAN1 will agree to support. But that is clearly a flawed design.  Also, the number of repetitions should depend on the UCI and not be the same for all UCI types – a network does not always target a same reliability for HARQ-ACK/SR/CSI. |
| Intel | * Regarding “Prerequisite of the scheme”,   + Similar the comment as above, we would like to consider long PUCCH format as Prerequisite of the scheme at least for NR Coverage Enhancement SI/WI. |
| ZTE | We basically agree with Ericsson’s suggestion on the use case of this scheme. |
| CATT | Agree with Ericsson. |
| Intel | As we commented in the 1st round, we would like to consider long PUCCH format as Prerequisite of the scheme at least for NR Coverage Enhancement SI/WI. |
| Qualcomm | Suggest simplifying use case to: “More flexible indication of PUCCH repetition factor to improve resource utilization efficiency”.  The additional statement (“But its benefit to coverage enhancement is not clear”) on benefit can be captured under performance gain or under a new sub-bullet titled “Impact on coverage”. |
| Huawei, HiSilicon | Similar view with Ericsson that spectrum efficiency needs to be considered in dynamical indication of PUCCH repetitions.  Suggest to call “potential spec impact” as commented before. |
| Intel | As we commented in the 1st and 2nd round, we would like to consider long PUCCH format as Prerequisite of the scheme at least for NR Coverage Enhancement SI/WI. |
| Ericsson | **Some quick comments in view of the tight deadline:**  **Proposal 5-1: For dynamic PUCCH repetition factor indication, capture the following in the TR**  **Use case:** ~~Aim to allow more flexible indication of PUCCH repetition factor to improve resource utilization efficiency.~~ More flexible indication of PUCCH repetition factor to improve coverage while maintaining spectral efficiency ~~[But its benefit to coverage enhancement is not clear.]~~  **Restriction of the scheme:** None  **Prerequisite of the scheme:** None  **Performance gain:** captured in Table 3, where Table 3 is subject to change based on new simulation results  **Proposal 5-2: For dynamic PUCCH repetition factor indication, capture the following in the TR**  **Potential Spec impact:**   * a new PUCCH repetition signalling mechanism needs to be specified   **Impact to receiver: None**  **Impact to UE implementation:**   * Need to implement transmissions of the PUCCH repetitions based on the dynamic indicator   **[Impact to system]**   * [FFS the impact to system] |
| OPPO | We think the proposal 5-1 modified by Ericsson is better, if it can be approved quickly. |
| Apple | We share similar view as QC |
| FL | To QC/Apple and Ericsson/OPPO:  The common ground looks like “This scheme can improve resource utilization efficiency”  The debate seems about if “dynamic PUCCH repetition factor repetition” can improve UL coverage? Please continue to discuss this issue and see if consensus can be achieved.  In the main time, please check if FL proposed compromise can be acceptable?  ‘More flexible indication of PUCCH repetition factor to allow ~~improve~~ achieving a certain coverage ~~while maintaining spectral efficiency~~ with more efficient resource utilization’.  It is just my personal view: dynamic repetition indication can not improve coverage, comparing to static repetition indication. Say if a gNB want to improve coverage, it can configure repetition factor = 16 all the time and that is the max coverage and dynamic indicator can not beat it, in terms of coverage. What dynamic indication can improve is to achieve the same coverage as static indication with more efficient resource usage. |

## 2.5 DMRS bundling cross PUCCH repetitions

Three companies provided LLS results for this scheme. The following table is extracted from [23].

Table 4: Performance gain observed for DMRS bundling cross PUCCH repetitions

|  |  |  |
| --- | --- | --- |
| Company | Observed performance gain | Key simulation assumptions |
| ZTE | 1 dB | 22 bits UCI, w/o DTX detection, 1% BLER, 4 PUCCH repetitions  Receiver for Rel-15/16 PUCCH: ML coherent receiver, w/o cross-slot channel estimation  Receiver for PUCCH enhancement scheme: ML coherent receiver, w/ cross-slot channel estimation |
| Intel | ~1.2 dB | 22 bits UCI, w/o DTX detection, 1% BLER, 8 PUCCH repetitions  Receiver for Rel-15/16 PUCCH: coherent receiver, w/o cross-slot channel estimation  Receiver for PUCCH enhancement scheme: coherent receiver, w/ cross-slot channel estimation |
| VIVO | 0.85 ~ 1.3 dB | 11 bits UCI, w/ DTX detection, 1% BLER, 2 PUCCH repetitions  Receiver for Rel-15/16 PUCCH: Coherent detection, DTX is performed based on union of DMRS and UCI symbols, as shown in figure 2(b) of our updated contribution, R1-2009648. Channel estimation is performed individually for each repetition.  Receiver for PUCCH enhancement scheme: Joint channel estimation is used for PUCCH repetitions in consecutive slots, in addition to receiver for Rel-15 and Rel-16 UEs. |

To allow DMRS bundling, one prerequisite is the phase coherency cross PUCCH repetitions. This issue was mentioned in a few contributions. It is suggested in [12] to send LS to RAN4 to ask under what conditions UE can keep phase coherence cross repetitions.

Based on the input from companies in Section 4.4, the following proposal is made.

**Proposal 6-1: For DMRS bundling cross PUCCH repetitions, capture the following in the TR**

**Use case: Aim to** improve channel estimation for [back-to-back] PUCCH repetitions

**Restriction of the scheme:**

* Phase coherency cross PUCCH repetitions
* Same frequency resource allocation cross PUCCH repetitions
* Same power cross PUCCH repetitions

**Prerequisite of the scheme:** PUCCH repetition is enabled/configured [, with multiple back-to-back repetitions]

**Performance gain:** captured in Table 4, where Table 4 is subject to change based on new simulation results

**Agreements: For DMRS bundling cross PUCCH repetitions, capture the following in the TR**

**Potential Spec impact:**

* Restrictions to guarantee phase coherency cross repetitions need to be specified
* UE behaviour needs to be defined if the phase coherency of PUCCH repetition is impacted by other procedures
* DMRS bundling with inter-slot frequency hopping pattern enhancement need to be specified, if the frequency hopping enhancement is agreed.

**Proposal 6-3: For DMRS bundling cross PUCCH repetitions, capture the following in the TR**

**Impact to receiver:**

* New channel estimator needs to be implemented to process DMRS across multiple repetitions

**Impact to UE implementation**

* Same phase and transmission power need to be maintained cross PUCCH repetitions
* [Maintaining phase coherence across slots requires UE to alter how slot boundaries events (such as timing or power adjustments) are handled]

**Proposal 6-4: For DMRS bundling cross PUCCH repetitions, capture the following in the TR**

**[Impact to system]**

* [Impacts scheduler flexibility for MU-MIMO in uplink. Scheduler cannot make independent decisions slot to slot.
* gNB needs to maintain phase coherence across slots. gNB cannot switch beamformers or make any RF adjustments across multiple slots.
* UE needs to maintain phase coherence across multiple slots. UE-side adjustments for timing and frequency will have to be postponed to a later slot. UE may not have the best timing and frequency settings for multiple uplink slots.]

Depending on the final requirements of phase coherence across slots, there may also be an impact on power consumption at the UE as certain RF circuitry cannot be turned off to save power. Overall impact on UE power consumption needs to be assessed.

**Agreements: For DMRS bundling cross PUCCH or PUSCH repetitions, send an LS to RAN4 to ask the following**

* **Under what conditions UE can keep phase continuity cross PUCCH or PUSCH repetitions**
  + **Whether back-to-back PUCCH or PUSCH repetitions is one of the conditions required to keep phase continuity cross the repetitions**
* **Power control tolerance level cross PUCCH or PUSCH repetitions**

Comments to the above FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| Qualcomm | We think some input from RAN4 may be necessary due to the phase continuity issues across slot boundary. UE architecture is heavily designed around a slot-based state machine, so bundling poses challenges to this architecture. Also, there are power consumption considerations when developing UE RF architectures to support such features, so it is best to take a more cautious approach. |
| Intel | * Regarding “use case”   + We are not sure whether we need to restrict this to back to back repetition. Certainly, this needs input from RAN4. Suggest to remove this or at least put into square bracket for further check. * Regarding spec impact   + We suggest to add “inter-slot frequency hopping with inter-slot bundling during PUCCH repetition.”, which is similar to PUSCH coverage enhancement. In our simulation results, when inter-slot frequency hopping with inter-slot bundling is employed, additional ~1.6dB performance gain can be achieved. |
| CATT | Agree with Intel to remove the restriction of back to back repetition. Considering DMRS is the only aspect when gNB do the cross-slot channel estimation, cross-slot channel estimation can be applied to the case wherein consecutive PUCCHs carrying different UCI . It may not be necessary to have the limitation of PUCCH repetition. |
| Ericsson | OK to have the LS. Agree with Intel & CATT to ask for RAN4’s input on if back-to-back is required for constrained phase and/or power differences across PUCCH transmissions. Also ‘power coherence’ is not so clear, although phase coherence would be. Something like ‘constrained phase and/or power differences’ seems better wording. |
| Intel | For Prerequisite of the scheme, suggest to put “with multiple back-to-back repetitions” into square bracket for further check after we receive response from RAN4.  Regarding proposal 6, we suggest to change the wording as “keep phase continuity” as typically “phase continuity’ is used in RAN4. Also we share similar view as Ericsson that we need to check whether back to back repetition is needed. |
| ZTE | Fine with the proposal, though we don’t know how should we make forward if no reply from RAN4 is received in this meeting. |
| Nokia/NSB | Agree with Ericsson on the wording. |
| Qualcomm | We can remove square brackets around impact to system design. Some sub-bullets to add could include:   * Impacts scheduler flexibility for MU-MIMO in uplink. Scheduler cannot make independent decisions slot to slot. * gNB needs to maintain phase coherence across slots. gNB cannot switch beamformers or make any RF adjustments across multiple slots. * UE needs to maintain phase coherence across multiple slots. UE-side adjustments for timing and frequency will have to be postponed to a later slot. UE may not have the best timing and frequency settings for multiple uplink slots. * Depending on the final requirements of phase coherence across slots, there may also be an impact on power consumption at the UE as certain RF circuitry cannot be turned off to save power. Overall impact on UE power consumption needs to be assessed. |
| Samsung | Sending the LS to RAN4 can be fine, although the timing is such that this might not be that helpful to progress this topic in RAN1. Most importantly, if an LS is sent it should include both PUSCH and PUCCH otherwise no LS should be sent.  For proposal 6-4. The gNB cannot switch beamformers. If beam switching for PUCCH repetitions is adopted, it needs to be a separate configuration – similar to the PRACH discussions. The proposal can be removed. |
| Samsung | In general sending the LS to RAN4 can be fine, although the timing is such that this might not be that helpful to progress this topic in RAN1. Most importantly, if an LS is sent it should include both PUSCH and PUCCH otherwise it makes no sense to send it. |
| OPPO | We are also ok to include both PUSCH and PUCCH for the phase continuity LS and the square bracket of PUSCH can be removed. |
| Intel | For Proposal 7, we are fine to include both PUCCH and PUSCH in LS to RAN4. It may be good to clarify “Power control tolerance level” |
| Huawei, HiSilicon | Regarding this 2nd bullet of UE implementation in proposal 6-3, we prefer to keep brackets.   * ~~[~~Maintaining phase coherence across slots requires UE to alter how slot boundaries events (such as timing or power adjustments) are handled~~]~~   For two reasons,   * It is unclear why timing or power adjustments is needed if no timing or power adjustment is scheduled by gNB for this case. Could proponent please clarify it a bit? * In case they are needed, UE implementation may be different for different companies, altering the handling of slot boundaries event is only one potential implementation. |

## 2.6 FL proposals for prioritized schemes

Based on the input from companies, the following is proposed.

**Proposed conclusion**: **For the prioritized schemes agreed in RAN1 102e for PUCCH coverage enhancement, further study and conclude in RAN1 103e the following aspects:**

* **Use case/restriction/prerequisite of the schemes**
* **Performance gains including SINR gain (to achieve the required BLER) and PAPR/CM gain**
* **Potential spec impact of the schemes**
* **Impact to base station receiver implementation including receiver complexity and sensitivity to time and frequency error**
* **Impact to UE implementation**
  + **Send LS to RAN4 for identified RAN4 related issue if any.**

Table 5: Comments to the FL proposal

|  |  |
| --- | --- |
| Company | Comments |
| vivo | We suggest to remove ‘sensitivity to time and frequency error’, and it can be reported in performance gains if needed. |
| Intel | We suggest to add the performance metric in the conclusion, i.e., 1% DTX to ACK probability as this is RAN4 requirement for all PUCCH formats carrying HARQ-ACK feedback. |
| Ericsson | Agree with FL that t/f error is relevant, especially to the DMRS bundling case.  Agree with intel that DTX detection is a relevant matric.  OK to send LS, but decisions at this meeting should not be contingent on receiving an LS reply. LSs may be helpful e.g. for early guidance in a WI stage. |
| LG | Agree with vivo and intel that additional performance metric can be reported if necessary. |
| Samsung | Fine to send LS to RAN4.  We suggest to add in the LS a request for RAN4 to consider introducing DTX performance requirements with respect to UCI payload size and PUCCH format. |

# 3 Summary of study on other schemes

The study results on other schemes for PUCCH coverage enhancement are captured in Section 3.2 in [23], and copied as below.

Table 6: Performance gain observed for other PUCCH coverage enhancement schemes

|  |  |  |
| --- | --- | --- |
| Company | Solutions | Performance gain |
| CATT | One antenna precoder cycling | 1 dB |
| IITH, IITM, CEWIT, Reliance Jio, Tejas Networks | Power boosting for pi/2 BPSK | 3 dB for <50% UL duty cycle |
| 6 dB for <25 % UL duty cycle |
| Qualcomm | UCI payload compression (FR2 L1 beam report) | Helps increase reliability of beam switching procedure |
| NTT DOCOMO | Repetition for PUCCH format 2 | 1.5 dB |
| Ericsson | Aperiodic CSI on PUCCH | 3.5 dB MIL  5.0 dB LLS |

# 4 Further discussion

The next phase is to have more technical discussions on each proposed technique. For each scheme, companies are welcome to express feedback and comments to further discuss the LLS gain, PAPR gain, the spec impact, and the impact to receiver implementation.

## 4.1 DMRS-less PUCCH

Companies are welcomed to provide views in the following table to identify the pros. and cons. of this scheme.

Table 7: Comments on the “DMRS-less PUCCH”

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Company:  Qualcomm | | Use case of the scheme: Can be used in place of PF3 for small payloads (2-22 bits). Also applicable in place of PF2. | | |
| Any Restriction to apply the scheme: primarily intended for small payloads | | |
| Any prerequisite to apply the scheme: none | | |
| Performance gain | | SNR gain: 3-4 dB |
| PAPR/CM gain: 0.5 dB over R15 PF3 with pi/2 BPSK. 3.5 dB over R15 PF3 with QPSK. |
| Spec impact: New PUCCH Format needs to be introduced. | | |
| Impact to receiver | | Receiver complexity: No need for DMRS channel estimation. Sequence detection needs to be implemented --- computationally efficient implementations available for certain choice of sequences, e.g. m-sequences. |
| Receiver sensitivity to time/frequency error: more robust to timing and frequency than NR PUCCH. |
| Impact to UE implementation | | Simple tx implementation. No explicit encoder needed. Can leverage sequence design methods that are already specified in NR. |
| Company:  CATT | | Use case of the scheme: Could be used to replace PF3 and PF4 if the coverage cannot be guaranteed by other techniques. | | |
| Any Restriction to apply the scheme: The UCI payload cannot be too large. | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: |
| PAPR gain: |
| Spec impact: new PUCCH format needs to be introduced. The resource allocation, the sequence design, the carrying UCI payload need to be further studied. | | |
| Impact to receiver | | Receiver complexity: Depends on the detail sequence design, the receiver complexity may be increased. |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | Depends on the detail sequence design. May complicate UE implementation. |
| Company:  NTT DOCOMO | | Use case of the scheme: The technique can be applied for PF2 for FR2 operation with large number of gNB antenna beams as well as for PF 1/3/4 for FR1 operation. | | |
| Any Restriction to apply the scheme: | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: |
| PAPR/CM gain: |
| Spec impact: | | |
| Impact to receiver | | Receiver complexity: |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | |  |
| Company:  Panasonic | | Use case of the scheme: Replacement of PUCCH format which is coverage bottleneck, especially PUCCH format 3. | | |
| Any Restriction to apply the scheme: Applicable for low/medium UCI payload size | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: |
| PAPR/CM gain: |
| Spec impact: New PUCCH format needs to be introduced. Sequence design/selection, the applicable payload size should be specified. | | |
| Impact to receiver | | Receiver complexity: ML non-coherent sequence detection may increase the receiver complexity. |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | No encoder is needed. |
| ZTE | | Use case of the scheme: For UCI payload of 3~11 bits for long PUCCH format | | |
| Any Restriction to apply the scheme: Only for medium payload size | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: 2 ~ 3 dB |
| PAPR gain: |
| Spec impact: Define related sequences and PUCCH resource configuration | | |
| Impact to receiver | | Receiver complexity: No need for DMRS channel estimation. Blind detection on sequence transmitted from a sequence pool. |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | Implement a new PUCCH format |
| Company:  Sharp | | Use case of the scheme: Small payload (e.g., up to 11 bits) transmission | | |
| Any Restriction to apply the scheme: None | | |
| Any prerequisite to apply the scheme: None | | |
| Performance gain | | SNR gain: 3 dB |
| PAPR/CM gain: |
| Spec impact: Introduce new PUCCH format (including complex-value sequence generation, resource mapping) | | |
| Impact to receiver | | Receiver complexity: Need to modify sequence detector for PUCCH format 0 for more than 2 bits. |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | UE is required to implement a sequence generator. UE implementation effort can be reduced by reusing conventional sequence (e.g., low PAPR sequence) |
| Company:  IITH, IITM, CEWIT, Reliance Jio, Tejas Networks | | Use case of the scheme: Match the control channel coverage and PAPR with that of PUSCH. Pi/2 BPSK can be used for PF2 re-design and PF3 re-design. | | |
| Any Restriction to apply the scheme: Smaller payloads can be used | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: |
| PAPR/CM gain: |
| Spec impact: Introduce new PUCCH format or enhance existing ones to support larger payloads, define sequences which can be used for the same. | | |
| Impact to receiver | | Receiver complexity: Can avoid DMRS based estimation |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | Reuse existing methods of receiver implementation |
| Company:  CMCC | | Use case of the scheme: could be used to replace PF3 for small payload. | | |
| Any Restriction to apply the scheme: low UCI payload | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: 1~2.7dB |
| PAPR/CM gain: |
| Spec impact: new PUCCH format should be introduced. UCI payload, sequence design, resource allocation | | |
| Impact to receiver | | Receiver complexity: depends on sequence design and sequence length  While with shorter sequence compared to the case that all REs in the PUCCH resource are used to carry a whole long sequence, and less number of sequence detections, the receiver complexity is reduced. |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | Depends on the sequence design and UCI payload |
| Company:  OPPO | | Use case of the scheme: Mainly about the small payload size 1~2bits, HARQ-operation with potentially TB bundling. The consideration is for coverage limited cases, the coverage is determined by the small payload PUCCH. Larger payload can be further considered. | | |
| Any Restriction to apply the scheme: None | | |
| Any prerequisite to apply the scheme: None | | |
| Performance gain | | SNR gain: ~3dB |
| PAPR/CM gain: FFS |
| Spec impact: Extending the current PUCCH format or introducing new format. | | |
| Impact to receiver | | Receiver complexity: ML (Exsiting) |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | Small |
| Company:  LG | | Use case of the scheme: DMRS-less (not a sequence based, only DMRS is removed) PUCCH can be applied to long PUCCH configured with repetition when the resource for it is not sufficient and adjacent slot of it contains DMRS which enables channel estimation. | | |
| Any Restriction to apply the scheme: long PUCCH | | |
| Any prerequisite to apply the scheme: none | | |
| Performance gain | | SNR gain: expected to be increased by the amount of removed DMRS of the slot since the adjacent slot which contains DMRS can help channel estimation |
| PAPR/CM gain: |
| Spec impact: minimal | | |
| Impact to receiver | | Receiver complexity: no additional complexity is required |
| Receiver sensitivity to time/frequency error: none |
| Impact to UE implementation | | Minimal |
| Company:  vivo | | Use case of the scheme: PUCCH with less or equal to 11bits | | |
| Any Restriction to apply the scheme:  Limited number of bits can be delivered. Otherwise, it will lead to high detection complexity. | | |
| Any prerequisite to apply the scheme:  Performance gain can be achieved compared with legacy PF3 with advanced receiver | | |
| Performance gain | | Performance gain |
| PAPR/CM gain: |
| Spec impact:  A new PUCCH format should be introduced.  New sequence design would be needed.  How to multiplex CSI/HARQ-Ack to a sequence based PUCCH should be considered in TS 38.213. For example, when CSI is multiplexed with HARQ-Ack, the CSI part is dropped based on the configured coding rate of PUCCH. What is the definition of coding rate of a sequence based PUCCH need to be clarified.  PUCCH format specific power adjustment component  in power control should be defined in TS 38.213.  Whether and how to support Type-B PUCCH repetition should be discussed.  New RAN4 MPR requirement should be introduced in TS 38.101.  New demodulation requirements should be defined in TS 38.104. | | |
| Impact to receiver | | Impact to receiver |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | Impact to UE implementation |
| Company:  Intel | | Use case of the scheme: PUCCH format 3 | | |
| Any Restriction to apply the scheme: relatively small payload size, i.e., 3-11 bits | | |
| Any prerequisite to apply the scheme: decisions should be made based on performance results compared to existing PUCCH format 3 scheme. | | |
| Performance gain | | SNR gain: -1.0dB for 3-bit UCI payload and 0.2 dB for 11-bit UCI payload compared to existing PF3. |
| PAPR/CM gain: |
| Spec impact: a new PUCCH format and sequence design, e.g., existing RM coded sequence with removing the first column of the codeword or other sequences. | | |
| Impact to receiver | | Receiver complexity: non-coherent detection is needed for sequence based PUCCH. |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | |  |
| Company:  InterDigital | Use case of the scheme: PUCCH payload between 2-22 bits in power-limited scenario | | | |
| Any Restriction to apply the scheme: there will be a maximum payload | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Receiver complexity:  No need for DMRS channel estimation.  Need to implement sequence detection. However, it may be possible to limit complexity/reuse implementations by mapping to Zadoff-Chu sequences for smaller payloads, or by splitting larger payloads into smaller groups (each of which being mapped to a separate sequence). | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Limited impact. | |
| Company: Nokia/NSB | Use case of the scheme: Even though the name of the scheme is referred to as ‘DMRS less PUCCH transmission’, the idea is to have a new format in which RM codes (3-11 bits) and, possibly Polar codes (12-22 bits depending on the range), are replaced by sequence-based PUCCH transmission. This implies a change of coding scheme, whose impact goes far beyond coverage enhancement, as argued below. | | | |
| Any Restriction to apply the scheme: Short block size channel coding was specified in Rel-15 and changes on that may have a significant impact on both UE and gNB implementation. Even if RAN1 captures details on this scheme in the TR, it would then be up to RAN to decide allowed changes to coding techniques in the WI discussion, as there could be other channel coding chain related proposals instead of sequence-based to improve the coverage performance in this short block range. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: All the PUCCH formats are built on top of assumption of payload sizes. For sequence-based ones, “repetition” and “simplex’ codes are applied (below 2 bits). If the sequence-based scheme is applied for larger payloads, it is not clear what background code shall be specified (compared to existing methods). | | | |
| Impact to receiver | | Receiver complexity: The sequence detection schemes, which would support same payload sizes with RM codes and/or polar codes, would have to be implemented in parallel with the existing PUCCH formats. In this regard, it is worth noting that sequence-based schemes would have to work together with existing methods, as they will still have to be used to support legacy UEs. Co-existence evaluations are also not done in this study to see the impact of this proposal on existing PUCCH formats detections. Once again, we should not ignore that a change of coding scheme is not just about coverage extension.  Furthermore, it is unclear how the FAR/PMD and miss-detections are handled in this case, as existing implementation-based techniques on RM and polar codes cannot be used to handle error detection. A problem on error detection may arise in practice and no attention is being given to this aspect. Indeed, as far as existing evaluations go in this AI, RAN1 is not carrying out simulations considering FAR and PMD. Evaluation methodology has been designed to test coverage of the channel, not the impact of a coding scheme change. In this regard, it is important to add that FAR and miss-detection evaluations and capabilities were well observed and considered in existing codes (in Rel-15 discussions). For instance, sequence-based methods were not considered as suitable methods above 2 bits of UCI payload, also considering extra complexity associated with detecting larger sequences. | |
| Receiver sensitivity to time/frequency error: The relationship between this aspect when comparing sequence-based schemes and existing methods is not clear. | |
| Impact to UE implementation | | As implied previously, more impact is expected on gNB as two different chains have to be considered and guarantee of error detection is not clear. There may also be an additional burden on gNB when satisfying error detection requirements. On the other hand, it is not trivial in our view to say that the impacts at the UE would be more straightforward, as hardware components related to encoding get impacted. UE implementations have to support both legacy PUCCH formats and new PUCCH formats as well. | |
| Company:  Ericsson | Use case of the scheme: 3-11 bit UCI in format 3 | | | |
| Any Restriction to apply the scheme: Difficult to suppress interference due to lack of DMRS; unable to use DMRS for channel tracking. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: 0 to 0.2 dB | |
| PAPR gain: FFS. Note: In our understanding, PAPR generally overestimates gain. This is why cubic metric was developed (please see e.g. R1-060023) and should be used instead. | |
| Spec impact: FFS. At least includes FEC design, channel structure, resource allocation | | | |
| Impact to receiver | | Receiver complexity: Additional receiver needed for DMRS payloads > 11 bits; may require multi-hypothesis detection, depending on FEC design; New DTX detection that is not based on DMRS is needed | |
| Receiver sensitivity to time/frequency error: Channel tracking based on DMRS not possible. | |
| Impact to UE implementation | | New PUCCH transmission scheme needed | |
| Other comments | | The name of these schemes should be clarified: are all of the DMRS-less proposals sequence based? If not, then we should use the generic ‘DMRS-less PUCCH’ description we have been using so far. | |
| Company: | Use case of the scheme: | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |

## 4.2 PUSCH repetition Type-B like PUCCH repetition

Companies are welcomed to provide views in the following table to identify the pros. and cons. of this scheme.

Table 8: Comments on the “PUSCH repetition Type-B like PUCCH repetition”

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Company:  Qualcomm | | Use case of the scheme: Use case for a cell-edge UE is not very clear. Type-B repetitions originally introduced in eURLLC with latency reduction in mind. Latency is not the primary focus in this SI. If cell-edge UE is scheduled with 14-symbol PUCCH, this scheme brings no benefit. If short PUCCH (PF2) is used for a cell edge UE then, some benefits may be possible. Scope of this scheme needs to be clarified. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: -- | |
| PAPR/CM gain: -- | |
| Spec impact: Need detailed rules on nominal/actual repetition and handling postponement/cancellation. Potentially new DMRS locations need to be specified. Depending on how repetitions across slot boundaries are handled, phase coherence requirement across slots needs to be specified. | | | |
| Impact to receiver | | Receiver complexity: gNB may need to process multiple repetitions within a single slot. | |
| Receiver sensitivity to time/frequency error: Same as NR PUCCH. | |
| Impact to UE implementation | | UE may need to reencode PUCCH payload several times within a single slot. UE may need to closely track number of repetitions and rules for repetitions. New phase coherence constraints may be imposed based on how repetitions are handled across slot boundaries. | |
| Company:  CATT | | Use case of the scheme: Use case is not clear. Type B repetition is used for reduce latency instead of improving reliability. It can only be used for UCI < 11 bits. It becomes a payload-dependent solution. | | | |
| Any Restriction to apply the scheme: Cannot be used for UCI >11 bits. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR gain: | |
| Spec impact: As mentioned by Qualcomm, the entire procedure of PUSCH repetition type B needs to be reconsidered for PUCCH. | | | |
| Impact to receiver | | Receiver complexity: Receiver complexity increases as gNB needs to receive multiple pieces of PUCCH and combination is unavoidable. Furthermore, the complexity is too high to be feasible if repetition type B is applied to a UCI > 11 bits. | |
| Receiver sensitivity to time/frequency error: no improvement. | |
| Impact to UE implementation | | UE needs to segment a UCI depending on the UL-DL TDD configuration or the slot boundary. How to choose the recourse set in the sub-slot is also needs to be carefully studied. | |
| Company:  Samsung | | Use case of the scheme: coverage limited cases, cell-edge UEs. It improves UL resource utilization and latency while ensuring reliability.  Similar to PUSCH, and at least for UCI payload less than 12 bits, support more than one repetition within a slot, transmission across the slot boundary and invalid symbols, and different number of PUCCH symbols per slot. Support transmission in all symbols indicated as UL symbols by slot configuration or by SFI.  Support repetitions together with SFI operation and, to avoid restrictions in slot configurations indicated by SFI that the gNB cannot predict in advance, consider whether the UE drops or defers repetitions that cannot be transmitted due to collisions with DL/unavailable symbols indicated by SFI (they are deferred in Rel-15)  Support dynamic indication of the number of PUCCH repetitions. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR gain: | |
| Spec impact: The indication of the number of repetitions for a PUCCH transmission can be provided by the DCI format triggering the PUCCH transmission in case HARQ-ACK information is included. The range of the number of repetitions in the Rel-16 configuration has to be increased beyond a maximum of 8 repetitions.  Text similar to the description of PUSCH Type-B repetitions needs to be added to allow multiple repetitions/different number of symbols per slot. | | | |
| Impact to receiver | | Receiver complexity: gNB may process more than one PUCCH repetition in a slot | |
| Receiver sensitivity to time/frequency error: same as R15/16 PUCCH | |
| Impact to UE implementation | | UE may transmit multiple PUCCH repetition in a slot | |
| Company:  Panasonic | | Use case of the scheme: Use case is unclear. This solution may only be beneficial for short PUCCH repetition. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Due to the segmentation, the actual length of PUCCH repetition might be different than what was nominally indicated. Since NR defines PUCCH formats depending on the duration of PUCCH, potential impact would be PUCCH format may be different among the actual repetitions if UE generates the PUCCH based on the actual repetition. Therefore, whether/how to ensure the same PUCCH format among the actual repetition should be studied. | | | |
| Impact to receiver | | Receiver complexity: gNB may need to process multiple repetitions within a single slot. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Segmentation process is needed. | |
| Company:  Sharp | | Use case of the scheme: Utilize available symbols in special and subsequent UL slots | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: How to support repetitions with different time length. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Transmission of multiple repetitions with different time length | |
| Company:  CMCC | | Use case of the scheme: improve the coverage of PUCCH and fully use the uplink symbols in the special slot in TDD. Current PUCCH repetition occupies a same number of consecutive symbols in the repeated slots. And the starting symbol of each occupied slot should be the same. This limited the use of the 4 uplink symbol in the special slot and the later 2 full slots in the 2.6GHz configuration. A more flexible resource allocation schemes should be introduced for the PUCCH repetition. | | | |
| Any Restriction to apply the scheme: feasible UCI payload should be considered | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: depends on the repetition number | |
| PAPR/CM gain: | |
| Spec impact: introduce the PUSCH type B like repetition in PUCCH. Different starting symbol in each slot and maybe different occupied symbols in different slots | | | |
| Impact to receiver | | Receiver complexity: similar with type B repetition. Different resource allocation assumptions in each slot (if the rules are clarified, this is not an issue.). | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | UCI payload limitation and the predefined resource allocation rule (may not include the slot boundary issue) | |
| Company:  OPPO | | Use case of the scheme: With payload size restriction of 11 bits. The scheme can be used for coverage enhancement of both HARQ-ACK and CSI report. | | | |
| Any Restriction to apply the scheme: URLLC capable UE, which was defined as different set of UE capablility. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: New or enhanced repetition schemes. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Higher UE processing complexity for mini-slot like resources. | |
| Company:  LG | | Use case of the scheme: when more resource is needed to boost coverage of PUCCH and/or uplink resource is limited due to the TDD frame structure (i.e., S slot). | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: increased due to exploiting resources which was conventionally not. | |
| PAPR/CM gain: | |
| Spec impact: nominal/actual repetition and segmentation of PUCCH should be introduced. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  vivo | | Use case of the scheme: For TDD PUCCH repeated in S slot and U slot, where 2 UL symbols for Slot. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: TDD spectrum with DL heavy frame structure. | | | |
| Performance gain | | Performance gain | |
| PAPR/CM gain: | |
| Spec impact:   * *Concept of nominal PUCCH repetition and actual PUCCH repetition needs be introduced;* * *Segmentation rule to determine occasions for actual PUCCH repetition and the channel design including UCI and DMRS pattern need be defined for the actual PUCCH repetition*   *A reference number of REs, e.g. number of RE of nominal PUCCH repetition, is used to determine the transmission power of actual PUCCH repetition.* | | | |
| Impact to receiver | | Impact to receiver | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Impact to UE implementation | |
| Company: Apple | | Use case of the scheme: not well justified as mentioned by couple of companies. Do not support | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company: Intel | Use case of the scheme: contiguous repetition is helpful for PUCCH coverage enhancement so as to allow PUCCH to occupy the uplink/flexible symbols as much as possible. | | | |
| Any Restriction to apply the scheme: long PUCCH formats only and UCI payload size <= 11 bits | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact:  Separate starting symbol and length of symbols for each slot during repetition can be configured by higher layers for a PUCCH resource. Cancellation of nominal PUCCH due to collision with invalid DL symbols/invalid symbols. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  InterDigital | Use case of the scheme: Enable full utilization of available UL resources for PUCCH, such as UL symbols in special slot. In DL-dominated slot configurations (common scenario) such as DDDSU, the UL symbols in special represent a significant fraction of all available UL symbols. The coverage gain from utilizing these resources can be quite significant. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Need to indicate number of repetitions either dynamically or semi-statically. Possible splitting of resource in case “nominal” PUCCH repetition crosses slot boundary. | | | |
| Impact to receiver | | Receiver complexity: Processing/combining of multiple PUCCH repetitions in shorter tie period than for existing scheme. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Transmission of multiple PUCCH repetitions in shorter period than existing scheme | |
| Company: Nokia/NSB | Use case of the scheme: The use case of repetition type B for PUCCH coverage enhancement is unclear, especially when PUCCH cannot use all UL resources, e.g., PUSCH is also scheduled. In addition, the applicability of this solution also depends on the frame structure, e.g., in frame structures where S slot contains only 2 UL symbols then the scheme may only be beneficial for PUCCH repetition with short format (case 1), whereas the benefit of mixing the PUCCH formats across repetitions is unclear (case 3 in the figure below). | | | | |
| Any Restriction to apply the scheme: If this scheme is supported, we may need to restrict it for the case when PUCCH repetitions have the same format, i.e. only case 1 and case 4 in the figure above. In contrast, if the intention is to allow different PUCCH formats on different “actual” PUCCH repetitions, it may introduce significant specification impact and complexity at the receiver. | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | SNR gain: | | |
| PAPR/CM gain: | | |
| Spec impact: Indication/determination of number of repetitions and PUCCH formats configuration for different repetitions (if different formats are allowed). | | | | |
| Impact to receiver | | Receiver complexity: Receiver would need to decode different PUCCH formats for one PUCCH transmission, if any, and multiple PUCCH repetitions per slots. | | |
| Receiver sensitivity to time/frequency error: | | |
| Impact to UE implementation | |  | | |
| Company:  WILUS | Use case of the scheme: Efficient resource utilization with more UL symbols in TDD for coverage limited UEs (e.g., cell-edge UEs). | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: At first, definition of enhanced PUCCH repetition must be clarified, considering differences between PUCCH and PUSCH (e.g., PUCCH format). Then, we can discuss about spec impact such as repetition indication and segmentation rule. Potential solutions in A.I. 8.8.2.1 such as slot boundary relaxation and larger than 14 symbols also can be considered to this issue. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company: | Use case of the scheme: | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
|  |  | |  | |

## 4.3 (Explicit or implicit) Dynamic PUCCH repetition factor indication

Companies are welcomed to provide views in the following table to identify the pros. and cons. of this scheme.

Table 9: Comments on the “(Explicit or implicit) Dynamic PUCCH repetition factor indication”

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| Company:  Qualcomm | | Use case of the scheme: Currently PUCCH repetitions are tied to formats and not resources. Flexibility to dynamically indicate PUCCH repetition factor is useful in scenarios where the PUCCH payload needs additional protection/reliability. FR2 beam switching operations are one example. | | | | |
| Any Restriction to apply the scheme: | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Need to introduce new signaling mechanism. Can be explicit (for e.g., via DCI) or implicit. | | | | |
| Impact to receiver | | | Receiver complexity: minimal | |
| Receiver sensitivity to time/frequency error: Same as NR PUCCH | |
| Impact to UE implementation | | | minimal | |
| Company:  CATT | | Use case of the scheme: Alleviate the collision between PUCCH and other uplink channels. Reduce the overall overhead of PUCCH transmission. | | | | |
| Any Restriction to apply the scheme: None | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | | SNR gain: | |
| PAPR gain: | |
| Spec impact: Specify how to indicate the repetition number, implicitly or explicitly. | | | | |
| Impact to receiver | | | Receiver complexity: None | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | | None | |
| Company:  Panasonic | | Use case of the scheme: In Rel.15, the number of PUCCH repetitions is semi-statically configured. The UCI payload size may be changed dynamically based on the DL data size and/or resource availability. Dynamic indication may reduce the redundant repetitions. | | | | |
| Any Restriction to apply the scheme: | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: How to indicate the number of repetitions dynamically should be specified. | | | | |
| Impact to receiver | | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | |  | |
| ZTE | | Use case of the scheme: Can be adaptive to variation of channel conditions, e.g., O2O case with relatively high UE speed. This could ensure the reliability or improve system efficiency. | | | | |
| Any Restriction to apply the scheme: | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | | In our simulation, the signal power is set to an SNR of -12.8dB, which is the required SNR for the case with 11bits UCI and 4 repetitions. The simulation is to get the distribution of instantaneous received SNR at certain RBs and to see the percentage of instantaneous received SNR higher/lower than the required SNR for 4 repetitions. We find that the instantaneous received SNR is higher than the required SNR of 2 repetitions for more than 70% samples. In such cases, it can be indicated to 2 repetitions instead, which improves the system efficiency. Also, the instantaneous received SNR is lower than the required SNR of 4 repetitions for about 10% samples. In such cases, 8 repetitions should be indicated to ensure the reliability. | |
| PAPR/CM gain: | |
| Spec impact: Dynamic repetition indication. | | | | |
| Impact to receiver | | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | | Very small. | |
| Company:  Sharp | | Use case of the scheme: TDD | | | | |
| Any Restriction to apply the scheme: | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Signalling of repetition number | | | | |
| Impact to receiver | | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | | Dynamic change of repetition for a PUCCH format | |
| Company:  OPPO | | Use case of the scheme: PUCCH ack dynamic repetition, indicated by scheduling DCI. | | | | |
| Any Restriction to apply the scheme: no | | | | |
| Any prerequisite to apply the scheme: no | | | | |
| Performance gain | | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Very small, 1 additional bit filed in the DCI format. | | | | |
| Impact to receiver | | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | | Very small. | |
| Company:  vivo | | Use case of the scheme: Indication of the number of PUCCH repetition through PRI | | | | |
| Any Restriction to apply the scheme: No | | | | |
| Any prerequisite to apply the scheme: the number of PUCCH repetition is configured on PUCCH resource instead of configured on PUCCH format in Rel-15. | | | | |
| Performance gain | | | Performance gain | |
| PAPR/CM gain: | |
| Spec impact:  *PUCCH repetition number is configured on PUCCH resource instead of configured on PUCCH format;* | | | | |
| Impact to receiver | | | Impact to receiver | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | | Impact to UE implementation | |
| Company:  Apple | | Use case of the scheme: potentially improves system efficiency, although the gain in not clear | | | | |
| Any Restriction to apply the scheme: | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | | Performance gain | |
| PAPR/CM gain: | |
| Spec impact: | | | | |
| Impact to receiver | | | Impact to receiver | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | | Impact to UE implementation | |
| Company:  Intel | Use case of the scheme: more flexible repetitions for PUCCH compared to existing mechanism where number of repetitions is configured per PUCCH format. | | | | | |
| Any Restriction to apply the scheme: long PUCCH format only | | | | | |
| Any prerequisite to apply the scheme: | | | | | |
| Performance gain | | | SNR gain: | | |
| PAPR/CM gain: | | |
| Spec impact: number of repetitions is configured in each PUCCH resource. | | | | | |
| Impact to receiver | | | Receiver complexity: | | |
| Receiver sensitivity to time/frequency error: | | |
| Impact to UE implementation | | |  | | |
| Company: Nokia/NSB | | | Use case of the scheme: Reducing the number of repetitions dynamically can help reducing the overhead. This however comes at the expense of reliability, and vice versa. From our perspective, this solution may introduce some flexibility for the gNB in terms of indicating the number of PUCCH repetitions. However, it cannot be considered as an independent solution for PUCCH coverage enhancement. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | | SNR gain: |
| PAPR/CM gain: |
| Spec impact: Indication mechanism (depending on whether explicit or implicit method is adopted). | | | |
| Impact to receiver | | | Receiver complexity: |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | | |  |
| Company:  Ericsson | | | Use case of the scheme: Increased PUCCH format 3 coverage without excessive overhead | | | |
| Any Restriction to apply the scheme: No | | | |
| Any prerequisite to apply the scheme: No | | | |
| Performance gain | | | SNR gain: 5.0 dB in LLS; 3.5 dB MIL vs. no repetition (since dynamic repetition is not supported) |
| PAPR gain: None (uses Rel-15 waveform) |
| Spec impact: DCI carries repetition indication | | | |
| Impact to receiver | | | Receiver complexity: Same as Rel-15 |
| Receiver sensitivity to time/frequency error: Same as Rel-15 |
| Impact to UE implementation | | | UE must receive new DCI content |
| Company: | Use case of the scheme: | | | | | |
| Any Restriction to apply the scheme: | | | | | |
| Any prerequisite to apply the scheme: | | | | | |
| Performance gain | | | SNR gain: | | |
| PAPR/CM gain: | | |
| Spec impact: | | | | | |
| Impact to receiver | | | Receiver complexity: | | |
| Receiver sensitivity to time/frequency error: | | |
| Impact to UE implementation | | |  | | |

## 4.4 DMRS bundling cross PUCCH repetitions

Table 10: Comments on the “DMRS bundling cross PUCCH repetitions”

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Company: Qualcomm | | Use case of the scheme: The following comment is made assuming the current PUCCH repetition framework. This scheme may potentially benefit a cell-edge UE configured with (a) long-format PUCCH (PF3) spanning all 14 symbols of a slot (b) with PUCCH repetitions enabled and (c) slot pattern that has multiple contiguous U slots. Given the sparsity of uplink resources in TDD systems, unclear if the above three conditions are likely to ever occur for a cell-edge UE. | | | |
| Any Restriction to apply the scheme: Phase coherence needs to be maintained across repetitions, so there can be no gaps in transmission, no change in RB allocation, and no change in power across repetitions. | | | |
| Any prerequisite to apply the scheme: PUCCH needs to be configured with repetitions. Requires slot pattern to have multiple contiguous U slots. | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: none | |
| Spec impact: Rules for maintaining phase coherence across slots needs to be specified. Spec needs to specify how UE-side events such as power and timing adjustments that occur at slot boundary need to be handled. Given the rather large impact on overall UE architecture, limits of how long phase coherence needs to be maintained need to be imposed. | | | |
| Impact to receiver | | Receiver complexity: receivers need to be designed to process DMRS across multiple slots/repetitions. Time-frequency domain interpolation algorithms need to be updated. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Maintaining phase coherence across slots requires UE to alter how slot boundaries are handled. Events (timing or power adjustments for example) queued up for slot boundaries will need to be postponed or cancelled. | |
| Company:  CATT | | Use case of the scheme: Improve the accuracy of channel estimation when PUCCH repetition is configured and transmitted on consecutive symbols. | | | |
| Any Restriction to apply the scheme: Same frequency resource allocation, same power on consecutive repetitions, phase should be continuous, etc. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR gain: | |
| Spec impact: Small | | | |
| Impact to receiver | | Receiver complexity: gNB may needs to determine whether to handle the channel estimation based on the DMRS across the repetition or not. The efforts on channel estimation increases. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | minimal | |
| Company:  Panasonic | | Use case of the scheme: In poor channel conditions, the improvement of channel estimation performance is essential. | | | |
| Any Restriction to apply the scheme: To support cross-slot or cross-repetition channel estimation, phase continuity needs to be ensured. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: In what condition phase continuity can be kept should be clarified. | | | |
| Impact to receiver | | Receiver complexity: Receiver needs channel estimation process over multiple slots. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | The transmission power is not changed over the multiple slots. | |
| ZTE | | Use case of the scheme: Both TDD and FDD with consecutive UL slots for PUCCH repetition. | | | |
| Any Restriction to apply the scheme: Phase continuity should be kept | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: 1dB | |
| PAPR/CM gain: | |
| Spec impact: Rules may be needed to maintain the phase continuity. | | | |
| Impact to receiver | | Receiver complexity: gNB needs to perform cross-slot channel estimation. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Keep phase continuity for multiple slots. | |
| Company:  Sharp | | Use case of the scheme: TDD and FDD | | | |
| Any Restriction to apply the scheme: Power consistency and phase continuity should be preserved. Same frequency position of DMRS. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Specify duration for power consistency and phase continuity | | | |
| Impact to receiver | | Receiver complexity: Channel estimator and buffer needs to be enhanced such that multiple inputs from DMRS samples in different slot/repetition needs to be combined | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | OFDM signal generation to preserve power consistency and phase continuity | |
| Company:  OPPO | | Use case of the scheme: Any existing PUCCH format with repetition. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: PUCCH repetition with same frequency location of in different slots. | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: Enhanced Hopping pattern over the existing hopping schemes. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Small. | |
| Company:  LG | | Use case of the scheme: when the channel estimation of repeated PUCCH degrades due to the low SNR, it can be applied to improve channel estimation performance. | | | |
| Any Restriction to apply the scheme: the same frequency resource should be maintained during the bundled slot. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: it should be tied to inter-slot frequency hopping | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  vivo | | Use case of the scheme: For long PUCCH with 14 symbols and repeated on consecutive slots. | | | |
| Any Restriction to apply the scheme: consecutive PUCCH transmission | | | |
| Any prerequisite to apply the scheme: UE need to guarantee coherency among the PUCCH repetitions. | | | |
| Performance gain | | Performance gain | |
| PAPR/CM gain: | |
| Spec impact:   * *UE need to keep the same Tx power across PUCCH repetitions if DMRS bundling is configured;* * *The time domain granularity should be defined for DMRS bundling;* * *Potential UE behavior needs to be defined if the coherency of PUCCH repetition is impacted by other procedures, e.g. simultaneous transmission if configured with CA.* | | | |
| Impact to receiver | | Impact to receiver | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Impact to UE implementation | |
| Company:  Apple | | Use case of the scheme: Technically enhances the coverage once repetition is performed. If the feature is supported in PUSCH, no reason it is not discussed/supported for PUCCH. | | | |
| Any Restriction to apply the scheme: consecutive PUCCH transmission | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | Performance gain | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Impact to receiver | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Impact to UE implementation | |
| Company:  Intel | Use case of the scheme: for coverage limited scenario, channel estimation is typically a bottleneck in terms of link level performance. It is important to improve channel estimation performance so as to enhance coverage. | | | |
| Any Restriction to apply the scheme: phase coherence for PUCCH repetition. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: 1.2dB compared to without cross-slot channel estimation. Further, when inter-slot frequency hopping with inter-slot bundling is employed, additional ~1.6dB performance gain can be achieved. | |
| PAPR/CM gain: | |
| Spec impact: Same Tx power and inter-slot frequency hopping with inter-slot bundling during PUCCH repetition. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  InterDigital | Use case of the scheme: Same as for PUSCH, i.e. improve accuracy of channel estimation. This is especially useful in case “Type-B like” PUCCH repetition is supported since the time span of the DMRS transmissions is shorter. | | | |
| Any Restriction to apply the scheme: As for PUSCH. However, for PUCCH, it may be more applicable in case of PUCCH repetition. | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: conditions/signalling to apply bundling need to be specified | | | |
| Impact to receiver | | Receiver complexity: DMRS processing within a bundle | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Need to maintain same phase and same power across slots | |
| Company: Nokia/NSB | Use case of the scheme: This solution could help improving the quality of channel estimation. However, this should be discussed together or decided after the discussion on cross-slot channel estimation solution for PUSCH. | | | | |
| Any Restriction to apply the scheme: | | | | |
| Any prerequisite to apply the scheme: | | | | |
| Performance gain | | SNR gain: | | |
| PAPR/CM gain: | | |
| Spec impact: Similar spec impacts/restrictions as for cross-slot channel estimation solution for PUSCH. | | | | |
| Impact to receiver | | Receiver complexity: | | |
| Receiver sensitivity to time/frequency error: | | |
| Impact to UE implementation | |  | | |
| Company: | Use case of the scheme: | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |

## 4.5 Other schemes

Table 11: Comments on the “Other schemes”

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| Company:  CATT | | Scheme:  One-antenna port pre-coder cycling | Use case of the scheme: a universal solution to improve transmission performance | | | |
| Any Restriction to apply the scheme: At least two physical Tx is needed at UE side | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: at least 1 dB | |
| PAPR gain: | |
| Spec impact: totally transparent and minimal specification impacts | | | |
| Impact to receiver | | Receiver complexity: Same as the current PUCCH receptition | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | Minimal. The only thing UE needs to do is to scramble the bit sequence with a coder before transmit it on the physical Tx. | |
| Company: NTT DOCOMO | | Scheme: Repetition for PUCCH short formats | Use case of the scheme: PUCCH short formats are selected for FR2 with considering practical NW operation of using large number of BS antenna beams. And enhancement of short PUCCH format may avoid the complexity (e.g. different PUCCH formats for different antenna beams.) for the NW configuration and implementation. | | | |
| Any Restriction to apply the scheme: None | | | |
| Any prerequisite to apply the scheme: None | | | |
| Performance gain | | SNR gain: 1.5 dB for PF2 | |
| PAPR/CM gain: | |
| Spec impact: | | | |
| Impact to receiver | | Receiver complexity: None, since repetition for PUCCH format 1/3/4 is already supported. | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | | None, since repetition for PUCCH format 1/3/4 is already supported. | |
| Company: Samsung | Scheme: Introduce an offset value to ∆\_(F\_PUCCH ) (F) for SR and CSI report | | | Use case of the scheme: Allow the network to separately control the BLER targets for UCI types when multiplexing is in a PUCCH  Decouple target BLERs for different UCI types in PUCCH (they are decoupled in LTE or in the PUSCH). | | |
| Any Restriction to apply the scheme: | | |
| Any prerequisite to apply the scheme: | | |
| Performance gain | | SNR gain: |
| PAPR gain: |
| Spec impact: RRC specifications to introduce corresponding RRC parameters. RAN1 specifications are practically unchanged - only impact is to add the offset to the values of *deltaF-PUCCH-f0*, …, *deltaF-PUCCH-f4* and to allow different number of repetitions for different UCI types. | | |
| Impact to receiver | | Receiver complexity: |
| Receiver sensitivity to time/frequency error: |
| Impact to UE implementation | |  |
| Company: Samsung | | Scheme: Introduce PHR for PUCCH | Use case of the scheme: NR does not currently support PHR for PUCCH. Not always possible to derive PHR for PUCCH from PHR for PUSCH (which is supported). | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR gain: | |
| Spec impact: Duplicate PUSCH PHR description to define PUCCH PHR (exchange PUSCH parameters with PUCCH parameters). Practically no RAN1 specification impact. MAC specifications already have a placeholder and can re-use the LTE mechanism for Type2-PHR (although Type-2 PHR in NR would not be for simultaneous PUCCH and PUSCH transmissions on the PCell). | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  IITH, IITM, CEWIT, Reliance Jio, Tejas Networks | | Scheme: Power boosting for pi/2 BPSK | Use case of the scheme: Provides additional transmit power and directly enhances coverage | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: 26 dBm solution already existing in the spec. Have to get RAN4 inputs on the possibility of further boosting. The boosting will be a function of UL duty cycle. Some indication for the same will be introduced. | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  CMCC | | Scheme: PUCCH repetition with non-consecutive uplink slots | Use case of the scheme: solve the PUSCH transmission and long PUCCH repetition conflict issue in the uplink slot limited situation such as 7D1S2U. | | | |
| Any Restriction to apply the scheme: | | | |
| Any prerequisite to apply the scheme: | | | |
| Performance gain | | SNR gain: | |
| PAPR/CM gain: | |
| Spec impact: new repetition pattern for PUCCH | | | |
| Impact to receiver | | Receiver complexity: | |
| Receiver sensitivity to time/frequency error: | |
| Impact to UE implementation | |  | |
| Company:  Ericsson | | Scheme: A-CSI on PUCCH | Use case of the scheme: Increased PUCCH format 3 coverage without excessive overhead | | | |
| Any Restriction to apply the scheme: No | | | |
| Any prerequisite to apply the scheme: No | | | |
| Performance gain | | SNR gain: 5.0 dB in LLS; 3.5 dB MIL vs. no repetition (since dynamic repetition is not supported) | |
| PAPR/CM gain: None (uses Rel-15 waveform) | |
| Spec impact: DCI triggers CSI on PUCCH. Timing of A-CSI on PUCCH will need to be specified, as well as if DL DCI, UL DCI, or both are used to trigger. Rel-15 CSI content and coding for PUSCH can be reused. | | | |
| Impact to receiver | | Receiver complexity: Same as Rel-15 | |
| Receiver sensitivity to time/frequency error: Same as Rel-15 | |
| Impact to UE implementation | | UE must receive new DCI content and transmit according to trigger timing. | |
|  | |  |  | |  | |

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