Rel-16 5G V2X maintenance email discussion topics

* Thread 1: Remaining issues for PT-RS design
  + Issue PS-1-1: For PT-RS sequence generation, when r(m) is defined, which DM-RS symbol is used for reference is not clear.
  + Issue PS-1-2: Removal of duplication for PT-RS mapping
* Thread 2: Location of PSFCH/PSSCH/PSCCH/2nd SCI mapping
  + Issue PS-2-1: To clarify starting symbol and location for PSFCH
  + Issue PS-2-2: To clarify time-domain location of PSSCH
  + Issue PS-2-3: To clarify time-domain and frequency-domain location of PSCCH
  + Issue PS-2-4: To modify the amount of resources for 2nd SCI rate matching => will be kept?
* Alternative Thread 2: some issue in mode 1, e.g.,
  + RRC values of sl-DCI-ToSL-Trans for DCI 3-0
* Thread 3: The outstanding agreement from RAN1#102-e on the minimum gap between PSFCH and next SL retransmission.
  + Issue M1-1-1: Conditions for applicability
  + Issue M1-1-2: Value of delta
* Thread 4: Remaining issues in re-evaluation/pre-emption procedure
  + Issue M2-1: Fix undefined UE behaviour for the case of re-evaluation performed during periodic reservation process
  + Fix the issue of unreachable pre-emption event condition due to prior exclusion of slots related to non-monitored slots in the sensing window
* Thread 5: Indication/derivation of TDD configuration.
  + Issue SY-1-1: TDD configuration derivation
  + Issue SY-1-2: TDD configuration for OoC UEs
  + Issue SY-1-3: Indication of the non-TDD case in sl-TDD-config
* Thread 6: Remaining issues in prioritization between SL and UL
  + Issue PP-2: Prioritization rule between PSCCH/PSSCH and PUCCH without SL HARQ reports and SR/PUSCH without UL-SCH/SRS
  + Issue PP-3: Prioritization rule between PSFCH/S-SSB reception(s) and UL transmission(s)
  + Issue PP-4: PRACH and MsgA PUSCH are prioritized over SL transmission
* Thread 7: Clarification on the sidelink slot index => will be replaced with the early termination issue in Mode 1?
  + Issue PP-7: Clarification on the logical slot set for resource allocation procedure
  + Issue M2-6: Logical slot index vs physical slot index for slots where candidate set is determined
  + Issue M2-4: Logical slot period calculation clarification in section 8.1.7 of 38.214
* Thread 8: CRs for the agreements from previous meetings (Physical layer structure)
  + Issue PS-3: To clarify that PSCCH is assumed with wideband precoding
* Thread 9: CRs for the agreements from previous meetings (Mode 1)
* Thread 10: CRs for the agreements from previous meetings (Mode 2)
* Thread 11: CRs for the agreements from previous meetings (Synchronization)
  + Issue SY-3 Timing determination for NR V2X
  + Issue SY-4: S-SSB power control
* Thread 12: CRs for the agreements from previous meetings (Physical layer procedure)
  + Issue PP-8: Capture constraint on total SL TX power in case of simultaneous transmission of SL and UL