

<h2 style="margin: 0;">CHANGE REQUEST</h2>		Please see embedded help file at the bottom of this page for instructions on how to fill in this form correctly.
25.211	CR	029
GSM (AA.BB) or 3G (AA.BBB) specification number ↑		↑ CR number as allocated by MCC support team
For submission to: WG1 # 10 <small>list expected approval meeting # here ↑</small>		Current Version: V 3.1.0
for approval <input checked="" type="checkbox"/> for information <input type="checkbox"/>		strategic <input type="checkbox"/> non-strategic <input type="checkbox"/> <small>(for SMG use only)</small>

Form: CR cover sheet, version 2 for 3GPP and SMG The latest version of this form is available from: ftp://ftp.3gpp.org/Information/CR-Form-v2.doc

Proposed change affects: (U)SIM ME UTRAN / Radio Core Network
(at least one should be marked with an X)

Source: Texas Instruments **Date:** 13 Dec 1999

Subject: Modifications to STTD text

Work item: TS 25.211

Category:	F Correction <input type="checkbox"/> A Corresponds to a correction in an earlier release <input type="checkbox"/> B Addition of feature <input type="checkbox"/> C Functional modification of feature <input type="checkbox"/> D Editorial modification <input checked="" type="checkbox"/>	Release:	Phase 2 <input type="checkbox"/> Release 96 <input type="checkbox"/> Release 97 <input type="checkbox"/> Release 98 <input type="checkbox"/> Release 99 <input checked="" type="checkbox"/> Release 00 <input type="checkbox"/>
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(only one category shall be marked with an X)

Reason for change: Better readability and notation for STTD description

Clauses affected: 5.3.2.1

Other specs affected:	Other 3G core specifications <input type="checkbox"/> Other GSM core specifications <input type="checkbox"/> MS test specifications <input type="checkbox"/> BSS test specifications <input type="checkbox"/> O&M specifications <input type="checkbox"/>	→ List of CRs: → List of CRs: → List of CRs: → List of CRs: → List of CRs:	
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Other comments:

<----- double-click here for help and instructions on how to create a CR.

5.3.2.1 STTD for DPCH

The pilot bit pattern for the DPCH channel transmitted on the diversity antenna is given in table 14. For $N_{pilot} = 8, 16$ the shadowed part indicates pilot bits that are obtained by STTD encoded from the corresponding (shadowed) bits in Table 12. The non-shadowed pilot bit pattern is orthogonal to the corresponding (non-shadowed) pilot bit pattern in table 12. For $N_{pilot} = 4$, the diversity antenna pilot bit pattern is obtained by STTD encoding both the shadowed and non-shadowed pilot bits in table 12. For $N_{pilot} = 2$, the diversity antenna pilot pattern is obtained by STTD encoding the two pilot bits in table 12 with the last two bits (data or DTX) of the second data field (data2) of the slot. For the SF=256 DPCH, if there are only two dedicated pilot bits ($N_{pilot} = 2$ in Tables 12 and 14), they are STTD encoded together with the last two bits (data or DTX) of the second data field (data2) of the slot. In this case, the diversity antenna frame structure has the diversity antenna pilot bits preceding the STTD encoded last two bits of the second data field (data2). STTD encoding for the DPDCH, TPC, and TFCI fields is done as described in section 5.3.1.1.1. For the SF=512 DPCH, the first two bits in each slot, i.e. TPC bits, are not STTD encoded and the same bits are transmitted with equal power from the two antennas. The following remaining four bits are STTD encoded.

Table 14: Pilot pattern of the DPCH channel for the diversity antenna using STTD

Symbol #	$N_{pilot} = 2$	$N_{pilot} = 4$		$N_{pilot} = 8$				$N_{pilot} = 16$							
	0	0	1	0	1	2	3	0	1	2	3	4	5	6	7
Slot #0	01	01	10	11	00	00	10	11	00	00	10	11	00	00	10
1	10	10	10	11	00	00	01	11	00	00	01	11	10	00	10
2	11	11	10	11	11	00	00	11	11	00	00	11	10	00	11
3	10	10	10	11	10	00	01	11	10	00	01	11	00	00	00
4	00	00	10	11	11	00	11	11	11	00	11	11	01	00	10
5	01	01	10	11	00	00	10	11	00	00	10	11	11	00	00
6	01	01	10	11	10	00	10	11	10	00	10	11	01	00	11
7	00	00	10	11	10	00	11	11	10	00	11	11	10	00	11
8	11	11	10	11	00	00	00	11	00	00	00	11	01	00	01
9	01	01	10	11	01	00	10	11	01	00	10	11	01	00	01
10	11	11	10	11	11	00	00	11	11	00	00	11	00	00	10
11	00	00	10	11	01	00	11	11	01	00	11	11	00	00	01
12	00	00	10	11	10	00	11	11	10	00	11	11	11	00	00
13	10	10	10	11	01	00	01	11	01	00	01	11	10	00	01
14	10	10	10	11	01	00	01	11	01	00	01	11	11	00	11