

TSG-RAN Working Group 1 meeting No. 10
January 18 – 21, 2000, Beijing, China

TSGR1-00-0188

Source: RAN WG1
To: RAN WG2, RAN WG3, RAN WG4
Subject: Liaison on turbo codes for small block size

RAN WG1 would like to inform RAN WG2, RAN WG3 and RAN WG4 that when turbo coding is used, the physical layer will support all block sizes from 1 bit to 5114 bits, from the perspective of WG2 and WG3. However for the block sizes below 40 bits, the physical layer will insert padding bits up to 40 bits.

The current understanding in RAN WG1 is that the short size Turbo code is optional for both UE and Node B. However, it is believed that from the technical point of view, the support of short size should be mandatory for the UE if it supports Turbo coding. In the meanwhile, WG1 has decided to postpone the decision on whether the small block size turbo coding will be mandatory for Turbo code capable UEs in release 99 until TSG RAN WG1#11.