

Source: RAN2

To: TSG RAN WG1

Title: Liaison Statement to WG1 Regarding the Use of PICH Reserved Bits

During WG2#7, a contribution from Motorola (R2-99B61) was discussed which proposes use currently reserved bits on the PICH to indicate a change in system information for UEs after re-selection of a cell in which they were previously. According to the contribution, UE power consumption can be reduced by having the UE decode the reserved PICH bits rather than decoding a PCH frame. This addressed in particular UEs in between two cells.

WG2 would like to ask WG1 to answer the following three questions regarding the PICH:

1. Can the currently reserved bits on the PICH be allocated for the use of the higher layers?
2. What are the bit-error-rate characteristics of the PICH?
3. What is the battery life improvement of decoding the reserved PICH bits rather than decoding a PCH frame?