

Agenda Item: 5.4
Source: Nokia
Title: Text proposals for S1.12
Document for: Decision

1. Chapter 4.2.1 CRC calculation

It is not mentioned if the CRC bits will be placed to the end of each transport block or to the beginning of each transport block.

Following sentence is proposed to be added:

“The CRC bits shall be placed to the beginning of each transport block.”

The reason that CRC bits should be at the beginning of a block is to facilitate the CRC-based blind rate detection.

2. Chapter 4.2.2.1 Convolutional coding

Definition for convolutional coding termination (tail bits) is missing.

Following sentence is proposed to be added:

“K-1 tail bits (value 0) shall be added to the end of the coding block”

3. Chapter 4.2.13.1 SFN(System Frame Number)

It is said, "SFN indicates reverse link scrambling code phase and super frame synchronisation".

It is not valid anymore that "SFN indicates reverse link scrambling code phase", so it should be deleted because the UL scrambling code period is one radio frame (10 ms).

Old text:

“SFN indicates reverse link scrambling code phase and super frame synchronisation. It is broadcasted in BCH. (See S1.11)”

Proposed text:

“SFN indicates super frame synchronisation. It is broadcasted in BCH. (See S1.11)”

4. Chapter 4.2.14 Bit transmission Sequence

The contents of this chapter is currently following:

- (1) DCH shall be transmitted in the order it was input. Other various information shall be transmitted from the MSB side.
- (2) Tail bits shall all transmit "0".
- (3) CRC bits shall be transmitted from the higher number bits to the lower ones.
- (4) Dummy bit shall always be "0".
- (5) Dummy is the subject of CRC coding.

Only the first and third points are valid and it is proposed that the other points (2, 4, and 5) would be deleted.

Point (2): Tail bit values could be told in chapter 4.2.2.

Points (4) and (5): dummy bits are not used.

Third point is proposed to be “CRC bits will be transmitted most significant bit first”.

The proposed text for this chapter is following:

- (1) DCH shall be transmitted in the order it was input. Other various information shall be transmitted from the MSB side.
- (2) CRC bits shall be transmitted most significant bit first.