**3GPP TSG-RAN4 WG4 Meeting #** **100-e *R4-2115860***

**Electronic meeting, August. 16- 27, 2021**

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| *CR-Form-v12.1* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.141-2** | **CR** | xxxx | **rev** | **-** | **Current version:** | **15.10.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
|  | | | | | | | | |

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME |  | Radio Access Network | **x** | Core Network |  |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | |
| ***Title:*** | Big CR for TS 38.141-2 Maintenance Demod part (Rel-15 CAT F) | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | MCC, Huawei, HiSilicon | | | | | | | | | |
| ***Source to TSG*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** |  | | | | |  | ***Date:*** | | | 2021-08-29 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-15 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-15 (Release 15) Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | As per RAN4 leadship’s guidance, big CR approach will be used to merge several draft CRs to minimize the MCC’s workload for implementation of the specification.  This big CR merges the mutile endorsed draft CRs. The reason for change in each endorsed draft CR are copied below:  R4-2115680 draftCR: UCI and PTRS overhead for channel bits calculation in PUSCH FRC   * The UCI and PT-RS overhead are not taken into account for the calculation of “Total number of bits per slot” and “Total symbols per slot” in the PUSCH FRCs, but they are rate matched during the data mapping. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | The summary of change in each each endorsed draft CR is copied below.  R4-2115680 draftCR: UCI and PTRS overhead for channel bits calculation in PUSCH FRC   * Added one note to clarify that “Total number of bits per slot” and “Total symbols per slot” do include the REs taken up by CSI part 1 and CSI part 2 for PUSCH FRCs for UCI multiplexing on PUSCH requirements * Updated FRCs to include two rows for “Total number of bits per slot” and “Total symbols per slot” each, one including PT-RS and another excluding PT-RS | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | The consequences if not approved for each endorsed draft CR are coppied below.  R4-2115680 draftCR: UCI and PTRS overhead for channel bits calculation in PUSCH FRC  Confusions will still exist in the specification | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | R4-2115680 draftCR: UCI and PTRS overhead for channel bits calculation in PUSCH FRC   * A.4,A.5 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **x** | Other core specifications | | | | TS/TR … CR … | | |
| ***affected:*** | |  | **x** | Test specifications | | | | TS/TR … CR … | | |
| ***(show related CRs)*** | |  | **x** | O&M Specifications | | | | TS/TR … CR … | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |

|  |  |
| --- | --- |
| ***This CR’s revision history:*** |  |

**<Start of Change 1>**

# A.4 Fixed Reference Channels for performance requirements (16QAM, R=658/1024)

<Unchanged Sections Skipped>

Table A.4-2: FRC parameters for FR1 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos1* and 1 transmission layer (16QAM, R=658/1024)

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR1-A4-8 | G-FR1-A4-9 | G-FR1-A4-10 | G-FR1-A4-11(Note 3) | G-FR1-A4-12 | G-FR1-A4-13 | G-FR1-A4-14 |
| Subcarrier spacing [kHz] | 15 | 15 | 15 | 30 | 30 | 30 | 30 |
| Allocated resource blocks | 25 | 52 | 106 | 24 | 51 | 106 | 273 |
| CP-OFDM Symbols per slot (Note 1) | 12 | 12 | 12 | 12 | 12 | 12 | 12 |
| Modulation | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM |
| Code rate (Note 2) | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 |
| Payload size (bits) | 9224 | 19464 | 38936 | 8968 | 18960 | 38936 | 100392 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 2 | 3 | 5 | 2 | 3 | 5 | 12 |
| Code block size including CRC (bits) (Note 2) | 4648 | 6520 | 7816 | 4520 | 6352 | 7816 | 8392 |
| Total number of bits per slot | 14400 | 29952 | 61056 | 13824 | 29376 | 61056 | 157248 |
| Total symbols per slot | 3600 | 7488 | 15264 | 3456 | 7344 | 15264 | 39312 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos1*, *l0*= 2 and *l*=11 for PUSCH mapping type A, *l0*= 0 and *l* =10 for PUSCH mapping type B as per table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: The calculation of the “Total number of bits per slot” and “Total symbols per slot” fields include the REs taken up by CSI part 1 and CSI part 2, if present. | | | | | | | |

**<End of Change 1>**

**<Start of Change 2>**

Table A.4-5: FRC parameters for FR2 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos0* and 1 transmission layer (16QAM, R=658/1024)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR2-A4-1 | G-FR2-A4-2 | G-FR2-A4-3(Note 3) | G-FR2-A4-4 | G-FR2-A4-5 |
| Subcarrier spacing [kHz] | 60 | 60 | 120 | 120 | 120 |
| Allocated resource blocks | 66 | 132 | 32 | 66 | 132 |
| CP-OFDM Symbols per slot (Note 1) | 9 | 9 | 9 | 9 | 9 |
| Modulation | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM |
| Code rate (Note 2) | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 |
| Payload size (bits) | 18432 | 36896 | 8968 | 18432 | 36896 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 3 | 5 | 2 | 3 | 5 |
| Code block size including CRC (bits) (Note 2) | 6176 | 7408 | 4520 | 6176 | 7408 |
| Total number of bits per slot without PT-RS | 28512 | 57024 | 13824 | 28512 | 57024 |
| Total number of bits per slot with PT-RS (Note 4) | 27324 | 54648 | 13248 | 27324 | 54648 |
| Total symbols per slot without PT-RS | 7128 | 14256 | 3456 | 7128 | 14256 |
| Total symbols per slot with PT-RS (Note 4) | 6831 | 13662 | 3312 | 6831 | 13662 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos0* with *l0*= 0 as per Table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: The calculation of the “Total number of bits per slot” and “Total symbols per slot” fields include the REs taken up by CSI part 1 and CSI part 2, if present.  NOTE 4: PT-RS configuration *KPT-RS =2, LPT-RS =1*. | | | | | |

Table A.4-6: FRC parameters for FR2 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos0* and 2 transmission layers (16QAM, R=658/1024)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR2-A4-6 | G-FR2-A4-7 | G-FR2-A4-8 | G-FR2-A4-9 | G-FR2-A4-10 |
| Subcarrier spacing [kHz] | 60 | 60 | 120 | 120 | 120 |
| Allocated resource blocks | 66 | 132 | 32 | 66 | 132 |
| CP-OFDM Symbols per slot (Note 1) | 9 | 9 | 9 | 9 | 9 |
| Modulation | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM |
| Code rate (Note 2) | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 |
| Payload size (bits) | 36896 | 73776 | 17928 | 36896 | 73776 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 5 | 9 | 3 | 5 | 9 |
| Code block size including CRC (bits) (Note 2) | 7408 | 8224 | 6008 | 7408 | 8224 |
| Total number of bits per slot without PT-RS | 57024 | 114048 | 27648 | 57024 | 114048 |
| Total number of bits per slot with PT-RS (Note 3) | 54648 | 109296 | 26496 | 54648 | 109296 |
| Total symbols per slot without PT-RS | 14256 | 28512 | 6912 | 14256 | 28512 |
| Total symbols per slot with PT-RS (Note 3) | 13662 | 27324 | 6624 | 13662 | 27324 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos0* with *l0*= 0 as per Table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: PT-RS configuration *KPT-RS =2, LPT-RS =1*. | | | | | |

Table A.4-7: FRC parameters for FR2 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos1* and 1 transmission layer (16QAM, R=658/1024)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR2-A4-11 | G-FR2-A4-12 | G-FR2-A4-13 (Note 3) | G-FR2-A4-14 | G-FR2-A4-15 |
| Subcarrier spacing [kHz] | 60 | 60 | 120 | 120 | 120 |
| Allocated resource blocks | 66 | 132 | 32 | 66 | 132 |
| CP-OFDM Symbols per slot (Note 1) | 8 | 8 | 8 | 8 | 8 |
| Modulation | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM |
| Code rate (Note 2) | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 |
| Payload size (bits) | 16392 | 32776 | 7936 | 16392 | 32776 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | - | 24 | 24 |
| Number of code blocks - C | 2 | 4 | 1 | 2 | 4 |
| Code block size including CRC (bits) (Note 2) | 8232 | 8224 | 7960 | 8232 | 8224 |
| Total number of bits per slot without PT-RS | 25344 | 50688 | 12288 | 25344 | 50688 |
| Total number of bits per slot with PT-RS (Note 4) | 24288 | 48576 | 11776 | 24288 | 48576 |
| Total symbols per slot without PT-RS | 6336 | 12672 | 3072 | 6336 | 12672 |
| Total symbols per slot with PT-RS (Note 4) | 6072 | 12144 | 2944 | 6072 | 12144 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos1* with *l0*= 0 and *l* =8 as per Table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: The calculation of the “Total number of bits per slot” and “Total symbols per slot” fields include the REs taken up by CSI part 1 and CSI part 2, if present.  NOTE 4: PT-RS configuration *KPT-RS =2, LPT-RS =1*. | | | | | |

Table A.4-8: FRC parameters for FR2 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos1* and 2 transmission layers (16QAM, R=658/1024)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR2-A4-16 | G-FR2-A4-17 | G-FR2-A4-18 | G-FR2-A4-19 | G-FR2-A4-20 |
| Subcarrier spacing [kHz] | 60 | 60 | 120 | 120 | 120 |
| Allocated resource blocks | 66 | 132 | 32 | 66 | 132 |
| CP-OFDM Symbols per slot (Note 1) | 8 | 8 | 8 | 8 | 8 |
| Modulation | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM |
| Code rate (Note 2) | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 |
| Payload size (bits) | 32776 | 65576 | 15880 | 32776 | 65576 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 4 | 8 | 2 | 4 | 8 |
| Code block size including CRC (bits) (Note 2) | 8224 | 8224 | 7976 | 8224 | 8224 |
| Total number of bits per slot without PT-RS | 50688 | 101376 | 24576 | 50688 | 101376 |
| Total number of bits per slot with PT-RS (Note 3) | 48576 | 97152 | 23552 | 48576 | 97152 |
| Total symbols per slot without PT-RS | 12672 | 25344 | 6144 | 12672 | 25344 |
| Total symbols per slot with PT-RS (Note 3) | 12144 | 24288 | 5888 | 12144 | 24288 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos1* with *l0*= 0 and *l* =8 as per Table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: PT-RS configuration *KPT-RS =2, LPT-RS =1*. | | | | | |

**<End of Change 2>**

**<Start of Change 3>**

# A.5 Fixed Reference Channels for performance requirements (64QAM, R=567/1024)

<Unchanged Sections Skipped>

Table A.5-3: FRC parameters for FR2 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos0* and 1 transmission layer (64QAM, R=567/1024)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR2-A5-1 | G-FR2-A5-2 | G-FR2-A5-3 | G-FR2-A5-4 | G-FR2-A5-5 |
| Subcarrier spacing [kHz] | 60 | 60 | 120 | 120 | 120 |
| Allocated resource blocks | 66 | 132 | 32 | 66 | 132 |
| CP-OFDM Symbols per slot (Note 1) | 9 | 9 | 9 | 9 | 9 |
| Modulation | 64QAM | 64QAM | 64QAM | 64QAM | 64QAM |
| Code rate (Note 2) | 567/1024 | 567/1024 | 567/1024 | 567/1024 | 567/1024 |
| Payload size (bits) | 23568 | 47112 | 11528 | 23568 | 47112 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 3 | 6 | 2 | 3 | 6 |
| Code block size including CRC (bits) (Note 2) | 7888 | 7880 | 5800 | 7888 | 7880 |
| Total number of bits per slot without PT-RS | 42768 | 85536 | 20736 | 42768 | 85536 |
| Total number of bits per slot with PT-RS (Note 3) | 40986 | 81972 | 19872 | 40986 | 81972 |
| Total symbols per slot without PT-RS | 7128 | 14256 | 3456 | 7128 | 14256 |
| Total symbols per slot with PT-RS (Note 3) | 6831 | 13662 | 3312 | 6831 | 13662 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos0* with *l0*= 0 as per Table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: PT-RS configuration *KPT-RS =2, LPT-RS =1*. | | | | | |

Table A.5-4: FRC parameters for FR2 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos1* and 1 transmission layer (64QAM, R=567/1024)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR2-A5-6 | G-FR2-A5-7 | G-FR2-A5-8 | G-FR2-A5-9 | G-FR2-A5-10 |
| Subcarrier spacing [kHz] | 60 | 60 | 120 | 120 | 120 |
| Allocated resource blocks | 66 | 132 | 32 | 66 | 132 |
| CP-OFDM Symbols per slot (Note 1) | 8 | 8 | 8 | 8 | 8 |
| Modulation | 64QAM | 64QAM | 64QAM | 64QAM | 64QAM |
| Code rate (Note 2) | 567/1024 | 567/1024 | 567/1024 | 567/1024 | 567/1024 |
| Payload size (bits) | 21000 | 42016 | 10248 | 21000 | 42016 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 3 | 5 | 2 | 3 | 5 |
| Code block size including CRC (bits) (Note 2) | 7032 | 8432 | 5160 | 7032 | 8432 |
| Total number of bits per slot without PT-RS | 38016 | 76032 | 18432 | 38016 | 76032 |
| Total number of bits per slot with PT-RS (Note 3) | 36432 | 72864 | 17664 | 36432 | 72864 |
| Total symbols per slot without PT-RS | 6336 | 12672 | 3072 | 6336 | 12672 |
| Total symbols per slot with PT-RS (Note 3) | 6072 | 12144 | 2944 | 6072 | 12144 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos1* with *l0*= 0 and *l* =8 as per Table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: PT-RS configuration *KPT-RS =2, LPT-RS =1*. | | | | | |

**<End of Change 3>**