**3GPP TSG-RAN4 WG4 Meeting #** **100-e *R4-2115857***

**Electronic meeting, August. 16- 27, 2021**

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| *CR-Form-v12.0* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.141-1** | **CR** | xxxx | **rev** | **-** | **Current version:** | **15.9.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network |  | Core Network |  |

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| ***Title:*** | Big CR to TS 38.141-1 Maintenance Demod part(Rel-15, CAT F) | | | | | | | | | |
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| ***Source to WG:*** | MCC, Ericsson | | | | | | | | | |
| ***Source to TSG*** | R4 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_newRAT-Perf | | | | |  | ***Date:*** | | | 2021-08-29 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-15 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) Rel-12 (Release 12)* *Rel-13 (Release 13) Rel-14 (Release 14) Rel-15 (Release 15) Rel-16 (Release 16)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | This big CRs merge the endorsed draf CR. The reason for change in each endorsed draft CR is copied below.  R4-2113757 draftCR: UCI overhead for channel bits calculation in PUSCH FRC in TS 38.141-1 (Rel-15)  The UCI overhead are not taken into account for the calculation of “Total number of bits per slot” and “Total symbols per slot” in the PUSCH FRCs for UCI multiplexed on PUSCH requirements, but they are rate matched during the data mapping | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | The summary of change in each each endorsed draft CR is copied below.  R4-2113757 draftCR: UCI overhead for channel bits calculation in PUSCH FRC in TS 38.141-1 (Rel-15)  Added one note to clarify that “Total number of bits per slot” and “Total symbols per slot” do include the REs taken up by CSI part 1 and CSI part 2 for PUSCH FRCs for UCI multiplexed on PUSCH requirements | | | | | | | | |
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| ***Consequences if not approved:*** | | The consequences if not approved for each endorsed draft CR are coppied below.  R4-2113757 draftCR: UCI overhead for channel bits calculation in PUSCH FRC in TS 38.141-1 (Rel-15)  Confusions will still exist in the specification. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | R4-2113757 draftCR: UCI overhead for channel bits calculation in PUSCH FRC in TS 38.141-1 (Rel-15)  A.4 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **x** | Other core specifications | | | | TS/TR … CR … | | |
| ***affected:*** | | **x** |  | Test specifications | | | | TS 38.141-2 | | |
| ***(show related CRs)*** | |  | **x** | O&M Specifications | | | | TS/TR … CR … | | |
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| ***Other comments:*** | |  | | | | | | | | |

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| ***This CR’s revision history:*** |  |

***<Start of change1 From R4-2113757 >***

# A.4 Fixed Reference Channels for performance requirements (16QAM, R=658/1024)

<Unchanged Sections Skipped>

Table A.4-2: FRC parameters for FR1 PUSCH performance requirements, transform precoding disabled, *Additional DM-RS position = pos1* and 1 transmission layer (16QAM, R=658/1024)

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Reference channel | G-FR1-A4-8 | G-FR1-A4-9 | G-FR1-A4-10 | G-FR1-A4-11 (Note 3) | G-FR1-A4-12 | G-FR1-A4-13 | G-FR1-A4-14 |
| Subcarrier spacing [kHz] | 15 | 15 | 15 | 30 | 30 | 30 | 30 |
| Allocated resource blocks | 25 | 52 | 106 | 24 | 51 | 106 | 273 |
| CP-OFDM Symbols per slot (Note 1) | 12 | 12 | 12 | 12 | 12 | 12 | 12 |
| Modulation | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM | 16QAM |
| Code rate (Note 2) | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 | 658/1024 |
| Payload size (bits) | 9224 | 19464 | 38936 | 8968 | 18960 | 38936 | 100392 |
| Transport block CRC (bits) | 24 | 24 | 24 | 24 | 24 | 24 | 24 |
| Code block CRC size (bits) | 24 | 24 | 24 | 24 | 24 | 24 | 24 |
| Number of code blocks - C | 2 | 3 | 5 | 2 | 3 | 5 | 12 |
| Code block size including CRC (bits) (Note 2) | 4648 | 6520 | 7816 | 4520 | 6352 | 7816 | 8392 |
| Total number of bits per slot | 14400 | 29952 | 61056 | 13824 | 29376 | 61056 | 157248 |
| Total symbols per slot | 3600 | 7488 | 15264 | 3456 | 7344 | 15264 | 39312 |
| NOTE 1: *DM-RS configuration type* = 1 with *DM-RS duration = single-symbol DM-RS* and the number of DM-RS CDM groups without data is 2, *Additional DM-RS position = pos1*, *l0*= 2 and *l*=11 for PUSCH mapping type A, *l0*= 0 and *l* =10 for PUSCH mapping type B as per table 6.4.1.1.3-3 of TS 38.211 [5].  NOTE 2: Code block size including CRC (bits) equals to *K'* in clause 5.2.2 of TS 38.212 [15].  NOTE 3: The calculation of the “Total number of bits per slot” and “Total symbols per slot” fields include the REs taken up by CSI part 1 and CSI part 2, if present. | | | | | | | |

***<End of change1>***