Agenda Item: 8.1, General UTRAN Architecture

Source: Siemens, Italtel

Title: **Power Control Functions, FDD – TDD alignment**

Document for:

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1.   References

[1]   UMTS ZZ.01, v0.1.0, 1999-01, UTRAN Architecture Description
[XX09]   UMTS XX.09, v1.2.0, 1998-12, UTRA TDD, Transport Channels and Physical Channels
            Description
[XX12]   UMTS XX.12, v0.1.0, 1998-12, UTRA TDD, Radio Transmission and Reception
[XX13]   UMTS XX.13, v0.4.0, 1999-01, UTRA TDD, Physical Layer Procedures Description

2.   Current text

In [1], chapters 11.2.4.5.1 to 11.2.4.5.4 provide a description of
-    UL Outer Loop Power Control
-    DL Outer Loop Power Control
-    UL Inner Loop Power Control
-    DL Inner Loop Power Control

The text of these chapters has been put in square brackets and has been preceded by the label "FDD", to indicate its applicability to the FDD mode only. This was a consequence of the discussion at the ARC meeting #8 on Tdoc ARC 264/98 "Power Control Functions".

However, from Layer 1 documents on TDD mode, e.g.: UMTS XX.13 [XX13], chapter 6.4; UMTS XX.09 [XX09]; and UMTS XX.12 [XX12], chapters 6.5.1, 6.5.2, it can be seen that these four PC functions are applicable to TDD. When comparing TDD and FDD, some parameters may be different, but the PC functions still apply. So the current restriction of these PC descriptions to FDD in [1] is not consistent with the contents of the related Layer 1 UTRA TDD documents.

3.   Proposal

Therefore the proposal is to remove the restriction to "FDD" in the headings, as well as the square brackets, of the subchapters 11.2.4.5.1 to 11.2.4.5.4 in the UTRAN Architecture Description [1].