**3GPP TSG-RAN WG2 Meeting #117-e *R2-2203132***

**Online, 21 February – 03 March 2022**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| *CR-Form-v12.1* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | **38.331** | **CR** | **2929** | **rev** | **1** | **Current version:** | **16.7.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
|  | | | | | | | | |

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | |
| ***Title:*** | Correction on invalid symbol pattern | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Huawei, HiSilicon | | | | | | | | | |
| ***Source to TSG:*** | RAN2 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_L1enh\_URLLC-Core | | | | |  | ***Date:*** | | | 2022-02-21 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **F** |  | | | | | ***Release:*** | | | Rel-16 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-15 (Release 15) Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | 1) The need codes of the fields *invalidSymbolPatternIndicatorDCI-0-1* and *invalidSymbolPatternIndicatorDCI-0-2* are “Need S” in TS 38.331. But it is not specified in current RAN2 spec when the fields are absent. In this case, UE does not know whether the symbols are valid or not if the RRC fields are absent.  According to TS 38.214, clause 6.1.2.1 as follows, if *invalidSymbolPatternIndicatorDCI-0-1* or *invalidSymbolPatternIndicatorDCI-0-2* is not configured, UE applies the invaild symbol pattern for DCI format 0\_1and DCI format 0\_2, respectively. RRC spec should refer to the corresponding description to avoid possible misunderstanding.   |  | | --- | | - if the PUSCH is scheduled by DCI format 0\_1, or corresponds to a Type 2 configured grant activated by DCI format 0\_1, and if *invalidSymbolPatternIndicatorDCI-0-1* is configured,  - if invalid symbol pattern indicator field is set 1, the UE applies the invalid symbol pattern;  - otherwise, the UE does not apply the invalid symbol pattern;  - if the PUSCH is scheduled by DCI format 0\_2, or corresponds to a Type 2 configured grant activated by DCI format 0\_2, and if *invalidSymbolPatternIndicatorDCI-0-2* is configured,  - if invalid symbol pattern indicator field is set 1, the UE applies the invalid symbol pattern;  - otherwise, the UE does not apply the invalid symbol pattern;  - otherwise, the UE applies the invalid symbol pattern. |   2) In current 38.306, a symbol level bitmap and slot level repetition pattern in time domain, specified as BIT STRING type, are indicated within *InvalidSymbolPattern-r16* IE. However, the meaning of each bit for the BIT STINRG is missing in current field description of RRC spec. Actually in RAN1#99e, RAN1 has agreed that the invalid symbol pattern should reuse the pattern definition of *rateMatchPattern* in time domain for PDSCH.   |  | | --- | | Agreement (RRC impact)  For DG PUSCH with PUSCH repetition type B, if dynamic SFI is configured, introduce a first RRC parameter that indicates one pattern for invalid symbols for PUSCH transmission repetition type B applicable to both DCI format 0\_1 and 0\_2, and introduce a second RRC parameter for each of DCI format 0\_1 and 0\_2 to indicate the presence of an additional bit in the DCI to indicate whether the pattern applies or not.   * If the first RRC parameter is not configured, semi-static flexible symbols are used for PUSCH. Segmentation occurs only around semi-static DL symbols. * If the first RRC parameter is configured and the additional bit exists in a DCI,   + Value ‘0’ means semi-static flexible symbols are used for PUSCH, and segmentation occurs only around semi-static DL symbols.   + Value ‘1’ means that segmentation occurs around semi-static DL symbols and invalid symbols in the pattern, and the remaining symbols are used for PUSCH. * If the first RRC parameter is configured and the additional bit does not exist in a DCI, segmentation occurs around semi-static DL symbols and invalid symbols in the pattern, and the remaining symbols are used for PUSCH. * The first RRC parameter reuses the pattern definition of *rateMatchPattern* in time domain for PDSCH. |   Therefore, we believe that the similar pattern description of *symbolsInResourceBlock* in *rateMatchPattern* as follows, could be also added to *InvalidSymbolPattern-r16*. Otherwise, without such description, there may be misunderstanding between UE and network on the usage of the BIT STRING for *InvalidSymbolPattern-r16*.   |  | | --- | | ***symbolsInResourceBlock***  A symbol level bitmap in time domain. It indicates with a bit set to true that the UE shall rate match around the corresponding symbol. This pattern recurs (in time domain) with the configured periodicityAndPattern (see TS 38.214 [19], clause 5.1.4.1).  For *oneSlot*, if ECP is configured, the first 12 bits represent the symbols within the slot and the last two bits within the bitstring are ignored by the UE; Otherwise, the 14 bits represent the symbols within the slot.  For *twoSlots*, if ECP is configured, the first 12 bits represent the symbols within the first slot and the next 12 bits represent the symbols in the second slot and the last four bits within the bit string are ignored by the UE; Otherwise, the first 14 bits represent the symbols within the first slot and the next 14 bits represent the symbols in the second slot. For the bits representing symbols in a slot, the most significant bit of the bit string represents the first symbol in the slot and the second most significant bit represents the second symbol in the slot and so on. |   Also the field description of *periodicityAndPattern* in *InvalidSymbolPattern-r16* is incomplete and it is supposed to align with that in *RateMatchPattern*. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Summary of change:*** | | 1) Clarify that when the *invalidSymbolPatternIndicatorDCI-0-1* or *invalidSymbolPatternIndicatorDCI-0-2* is absent, the UE behavior is specified in TS 38.214 clause 6.1.2.1.  2) Clarify the usage of each bit for the symbol level bitmap and slot level repetition pattern defined for *InvalidSymbolPattern-r16*.  **Impact analysis**  Impacted 5G architecture options:  SA, (NG)EN-DC, NE-DC, NR-DC  Impacted functionality:  In valid symbol pattern for DCI format 0\_1 and 0\_2  Inter-operability:  If the network is implemented according to thie CR while the UE is not, there may be misunderstanding between the UE and the network on the valid symbols.  It the UE is implemented according to this CR while the network is not, there may be misunderstanding between the UE and the network on the valid symbols. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Consequences if not approved:*** | | 1) The UE does not know whether the symbols are valid or not for DCI format 0\_1 or 0\_2 when *invalidSymbolPatternIndicatorDCI-0-1-r16* or *invalidSymbolPatternIndicatorDCI-0-2-r16* is absent from the RRC spec.  2) There may be misunderstanding between UE and network on the usage of the symbol level bitmap and slot level repetition pattern bitmap defined for *InvalidSymbolPattern-r16*. | | | | | | | | |
|  | |  | | | | | | | | |
| ***Clauses affected:*** | | 6.3.2 | | | | | | | | |
|  | |  | | | | | | | | |
|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  |  | Other core specifications | | | | TS/TR ... CR ... | | |
| ***affected:*** | |  |  | Test specifications | | | | TS/TR ... CR ... | | |
| ***(show related CRs)*** | |  |  | O&M Specifications | | | | TS/TR ... CR ... | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

<Start of modification>

6.3.2 Radio resource control information elements

< omit >

– *PUSCH-Config*

The IE *PUSCH-Config* is used to configure the UE specific PUSCH parameters applicable to a particular BWP.

***PUSCH-Config* information element**

-- ASN1START

-- TAG-PUSCH-CONFIG-START

PUSCH-Config ::= SEQUENCE {

dataScramblingIdentityPUSCH INTEGER (0..1023) OPTIONAL, -- Need S

txConfig ENUMERATED {codebook, nonCodebook} OPTIONAL, -- Need S

dmrs-UplinkForPUSCH-MappingTypeA SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

dmrs-UplinkForPUSCH-MappingTypeB SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

pusch-PowerControl PUSCH-PowerControl OPTIONAL, -- Need M

frequencyHopping ENUMERATED {intraSlot, interSlot} OPTIONAL, -- Need S

frequencyHoppingOffsetLists SEQUENCE (SIZE (1..4)) OF INTEGER (1.. maxNrofPhysicalResourceBlocks-1)

OPTIONAL, -- Need M

resourceAllocation ENUMERATED { resourceAllocationType0, resourceAllocationType1, dynamicSwitch},

pusch-TimeDomainAllocationList SetupRelease { PUSCH-TimeDomainResourceAllocationList } OPTIONAL, -- Need M

pusch-AggregationFactor ENUMERATED { n2, n4, n8 } OPTIONAL, -- Need S

mcs-Table ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

mcs-TableTransformPrecoder ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

transformPrecoder ENUMERATED {enabled, disabled} OPTIONAL, -- Need S

codebookSubset ENUMERATED {fullyAndPartialAndNonCoherent, partialAndNonCoherent,nonCoherent}

OPTIONAL, -- Cond codebookBased

maxRank INTEGER (1..4) OPTIONAL, -- Cond codebookBased

rbg-Size ENUMERATED { config2} OPTIONAL, -- Need S

uci-OnPUSCH SetupRelease { UCI-OnPUSCH} OPTIONAL, -- Need M

tp-pi2BPSK ENUMERATED {enabled} OPTIONAL, -- Need S

...,

[[

minimumSchedulingOffsetK2-r16 SetupRelease { MinSchedulingOffsetK2-Values-r16 } OPTIONAL, -- Need M

ul-AccessConfigListDCI-0-1-r16 SetupRelease { UL-AccessConfigListDCI-0-1-r16 } OPTIONAL, -- Need M

-- Start of the parameters for DCI format 0\_2 introduced in V16.1.0

harq-ProcessNumberSizeDCI-0-2-r16 INTEGER (0..4) OPTIONAL, -- Need R

dmrs-SequenceInitializationDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

numberOfBitsForRV-DCI-0-2-r16 INTEGER (0..2) OPTIONAL, -- Need R

antennaPortsFieldPresenceDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2-r16 SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2-r16 SetupRelease { DMRS-UplinkConfig } OPTIONAL, -- Need M

frequencyHoppingDCI-0-2-r16 CHOICE {

pusch-RepTypeA ENUMERATED {intraSlot, interSlot},

pusch-RepTypeB ENUMERATED {interRepetition, interSlot}

} OPTIONAL, -- Need S

frequencyHoppingOffsetListsDCI-0-2-r16 SetupRelease { FrequencyHoppingOffsetListsDCI-0-2-r16} OPTIONAL, -- Need M

codebookSubsetDCI-0-2-r16 ENUMERATED {fullyAndPartialAndNonCoherent, partialAndNonCoherent,nonCoherent}

OPTIONAL, -- Cond codebookBased

invalidSymbolPatternIndicatorDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

maxRankDCI-0-2-r16 INTEGER (1..4) OPTIONAL, -- Cond codebookBased

mcs-TableDCI-0-2-r16 ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

mcs-TableTransformPrecoderDCI-0-2-r16 ENUMERATED {qam256, qam64LowSE} OPTIONAL, -- Need S

priorityIndicatorDCI-0-2-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

pusch-RepTypeIndicatorDCI-0-2-r16 ENUMERATED { pusch-RepTypeA, pusch-RepTypeB} OPTIONAL, -- Need R

resourceAllocationDCI-0-2-r16 ENUMERATED { resourceAllocationType0, resourceAllocationType1, dynamicSwitch}

OPTIONAL, -- Need M

resourceAllocationType1GranularityDCI-0-2-r16 ENUMERATED { n2,n4,n8,n16 } OPTIONAL, -- Need S

uci-OnPUSCH-ListDCI-0-2-r16 SetupRelease { UCI-OnPUSCH-ListDCI-0-2-r16} OPTIONAL, -- Need M

pusch-TimeDomainAllocationListDCI-0-2-r16 SetupRelease { PUSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

-- End of the parameters for DCI format 0\_2 introduced in V16.1.0

-- Start of the parameters for DCI format 0\_1 introduced in V16.1.0

pusch-TimeDomainAllocationListDCI-0-1-r16 SetupRelease { PUSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

invalidSymbolPatternIndicatorDCI-0-1-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

priorityIndicatorDCI-0-1-r16 ENUMERATED {enabled} OPTIONAL, -- Need S

pusch-RepTypeIndicatorDCI-0-1-r16 ENUMERATED { pusch-RepTypeA, pusch-RepTypeB} OPTIONAL, -- Need R

frequencyHoppingDCI-0-1-r16 ENUMERATED {interRepetition, interSlot} OPTIONAL, -- Cond RepTypeB

uci-OnPUSCH-ListDCI-0-1-r16 SetupRelease { UCI-OnPUSCH-ListDCI-0-1-r16 } OPTIONAL, -- Need M

-- End of the parameters for DCI format 0\_1 introduced in V16.1.0

invalidSymbolPattern-r16 InvalidSymbolPattern-r16 OPTIONAL, -- Need S

pusch-PowerControl-v1610 SetupRelease {PUSCH-PowerControl-v1610} OPTIONAL, -- Need M

ul-FullPowerTransmission-r16 ENUMERATED {fullpower, fullpowerMode1, fullpowerMode2} OPTIONAL, -- Need R

pusch-TimeDomainAllocationListForMultiPUSCH-r16 SetupRelease { PUSCH-TimeDomainResourceAllocationList-r16 }

OPTIONAL, -- Need M

numberOfInvalidSymbolsForDL-UL-Switching-r16 INTEGER (1..4) OPTIONAL -- Cond RepTypeB2

]]

}

UCI-OnPUSCH ::= SEQUENCE {

betaOffsets CHOICE {

dynamic SEQUENCE (SIZE (4)) OF BetaOffsets,

semiStatic BetaOffsets

} OPTIONAL, -- Need M

scaling ENUMERATED { f0p5, f0p65, f0p8, f1 }

}

MinSchedulingOffsetK2-Values-r16 ::= SEQUENCE (SIZE (1..maxNrOfMinSchedulingOffsetValues-r16)) OF INTEGER (0..maxK2-SchedulingOffset-r16)

UCI-OnPUSCH-DCI-0-2-r16 ::= SEQUENCE {

betaOffsetsDCI-0-2-r16 CHOICE {

dynamicDCI-0-2-r16 CHOICE {

oneBit-r16 SEQUENCE (SIZE (2)) OF BetaOffsets,

twoBits-r16 SEQUENCE (SIZE (4)) OF BetaOffsets

},

semiStaticDCI-0-2-r16 BetaOffsets

} OPTIONAL, -- Need M

scalingDCI-0-2-r16 ENUMERATED { f0p5, f0p65, f0p8, f1 }

}

FrequencyHoppingOffsetListsDCI-0-2-r16 ::= SEQUENCE (SIZE (1..4)) OF INTEGER (1.. maxNrofPhysicalResourceBlocks-1)

UCI-OnPUSCH-ListDCI-0-2-r16 ::= SEQUENCE (SIZE (1..2)) OF UCI-OnPUSCH-DCI-0-2-r16

UCI-OnPUSCH-ListDCI-0-1-r16 ::= SEQUENCE (SIZE (1..2)) OF UCI-OnPUSCH

UL-AccessConfigListDCI-0-1-r16 ::= SEQUENCE (SIZE (1..64)) OF INTEGER (0..63)

-- TAG-PUSCH-CONFIG-STOP

-- ASN1STOP

|  |
| --- |
| ***PUSCH-Config* field descriptions** |
| ***antennaPortsFieldPresenceDCI-0-2***  Configure the presence of "Antenna ports" field in DCI format 0\_2. When the field is configured, then the "Antenna ports" field is present in DCI format 0\_2. Otherwise, the field size is set to 0 for DCI format 0\_2 (See TS 38.212 [17], clause 7.3.1.1.3). If neither *dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2* nor *dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2* is configured, this field is absent. |
| ***codebookSubset, codebookSubsetDCI-0-2***  Subset of PMIs addressed by TPMI, where PMIs are those supported by UEs with maximum coherence capabilities (see TS 38.214 [19], clause 6.1.1.1). The field *codebookSubset* applies to DCI format 0\_1 and the field *codebookSubsetDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.1.1). |
| ***dataScramblingIdentityPUSCH***  Identifier used to initialise data scrambling (c\_init) for PUSCH. If the field is absent, the UE applies the physical cell ID. (see TS 38.211 [16], clause 6.3.1.1). |
| ***dmrs-SequenceInitializationDCI-0-2***  Configure whether the field "DMRS Sequence Initialization" is present or not in DCI format 0\_2. If the field is absent, then 0 bit for the field "DMRS Sequence Initialization" in DCI format 0\_2. If the field is present, then the number of bits is determined in the same way as DCI format 0\_1 (see TS 38.212 [17], clause 7.3.1). |
| ***dmrs-UplinkForPUSCH-MappingTypeA, dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2***  DMRS configuration for PUSCH transmissions using PUSCH mapping type A (chosen dynamically via *PUSCH-TimeDomainResourceAllocation*). Only the fields *dmrs-Type*, *dmrs-AdditionalPosition* and *maxLength* may be set differently for mapping type A and B. The field *dmrs-UplinkForPUSCH-MappingTypeA* applies to DCI format 0\_1 and the field *dmrs-UplinkForPUSCH-MappingTypeA-DCI-0-2* applies to DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***dmrs-UplinkForPUSCH-MappingTypeB, dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2***  DMRS configuration for PUSCH transmissions using PUSCH mapping type B (chosen dynamically via *PUSCH-TimeDomainResourceAllocation*). Only the fields *dmrs-Type*, *dmrs-AdditionalPosition* and *maxLength* may be set differently for mapping type A and B. The field *dmrs-UplinkForPUSCH-MappingTypeB* applies to DCI format 0\_1 and the field *dmrs-UplinkForPUSCH-MappingTypeB-DCI-0-2* applies to DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***frequencyHopping***  The value *intraSlot* enables 'Intra-slot frequency hopping' and the value *interSlot* enables 'Inter-slot frequency hopping'. If the field is absent, frequency hopping is not configured for 'pusch-RepTypeA' (see TS 38.214 [19], clause 6.3). The field *frequencyHopping* applies to DCI format 0\_0 and 0\_1 for 'pusch-RepTypeA'. |
| ***frequencyHoppingDCI-0-1***  Indicates the frequency hopping scheme for DCI format 0\_1 when *pusch-RepTypeIndicatorDCI-0-1* is set to 'pusch-RepTypeB', The value *interRepetition* enables 'Inter-repetition frequency hopping', and the value *interSlot* enables 'Inter-slot frequency hopping'. If the field is absent, frequency hopping is not configured for DCI format 0\_1 (see TS 38.214 [19], clause 6.1). |
| ***frequencyHoppingDCI-0-2***  Indicate the frequency hopping scheme for DCI format 0\_2. The value *intraSlot* enables 'intra-slot frequency hopping', and the value *interRepetition* enables 'Inter-repetition frequency hopping', and the value *interSlot* enables 'Inter-slot frequency hopping'. When *pusch-RepTypeIndicatorDCI-0-2* is set to '*pusch-RepTypeA*', the frequency hopping scheme can be chosen between 'intra-slot frequency hopping and 'inter-slot frequency hopping' if enabled. When *pusch-RepTypeIndicatorDCI-0-2* is set to '*pusch-RepTypeB'*, the frequency hopping scheme can be chosen between 'inter-repetition frequency hopping' and 'inter-slot frequency hopping' if enabled. If the field is absent, frequency hopping is not configured for DCI format 0\_2 for 'pusch-RepTypeB' (see TS 38.214 [19], clause 6.3). |
| ***frequencyHoppingOffsetLists, frequencyHoppingOffsetListsDCI-0-2***  Set of frequency hopping offsets used when frequency hopping is enabled for granted transmission (not msg3) and type 2 configured grant activation (see TS 38.214 [19], clause 6.3). The field *frequencyHoppingOffsetLists* applies to DCI format 0\_0 and DCI format 0\_1 and the field *frequencyHoppingOffsetListsDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.3). |
| ***harq-ProcessNumberSizeDCI-0-2***  Configure the number of bits for the field "HARQ process number" in DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1). |
| ***invalidSymbolPattern***  Indicates one pattern for invalid symbols for PUSCH transmission repetition type B applicable to both DCI format 0\_1 and 0\_2. If *InvalidSymbolPattern* is not configured, semi-static flexible symbols are used for PUSCH. Segmentation occurs only around semi-static DL symbols (see TS 38.214 [19] clause 6.1). |
| ***invalidSymbolPatternIndicatorDCI-0-1, invalidSymbolPatternIndicatorDCI-0-2***  Indicates the presence of an additional bit in the DCI format 0\_1/0\_2. If *invalidSymbolPattern* is absent, then both *invalidSymbolPatternIndicatorDCI-0-1* and *invalidSymbolPatternIndicatorDCI-0-2* are absent. The field *invalidSymbolPatternIndicatorDCI-0-1* applies to the DCI format 0\_1 and the field *invalidSymbolPatternIndicatorDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19] clause 6.1). If the field is absent, the UE behaviour is specified in TS 38.214 [19], clause 6.1.2.1. |
| ***maxRank, maxRankDCI-0-2***  Subset of PMIs addressed by TRIs from 1 to ULmaxRank (see TS 38.214 [19], clause 6.1.1.1). The field *maxRank* applies to DCI format 0\_1 and the field *maxRankDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.1.1). |
| ***mcs-Table, mcs-TableFormat0-2***  Indicates which MCS table the UE shall use for PUSCH without transform precoder (see TS 38.214 [19], clause 6.1.4.1). If the field is absent the UE applies the value 64QAM. The field *mcs-Table* applies to DCI format 0\_0 and DCI format 0\_1 and the field *mcs-TableDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.4.1). |
| ***mcs-TableTransformPrecoder, mcs-TableTransformPrecoderDCI-0-2***  Indicates which MCS table the UE shall use for PUSCH with transform precoding (see TS 38.214 [19], clause 6.1.4.1) If the field is absent the UE applies the value 64QAM. The field *mcs-TableTransformPrecoder* applies to DCI format 0\_0 and DCI format 0\_1 and the field *mcs-TableTransformPrecoderDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.4.1). |
| ***minimumSchedulingOffsetK2***  List of minimum K2 values. Minimum K2 parameter denotes minimum applicable value(s) for the *Time domain resource assignment* table for PUSCH (see TS 38.214 [19], clause 6.1.2.1). |
| ***numberOfBitsRV-DCI-0-2***  Configures the number of bits for "Redundancy version" in the DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.214 [19], clause 6.1.2.1). |
| ***numberOfInvalidSymbolsForDL-UL-Switching***  Indicates the number of symbols after the last semi-static DL symbol that are invalid symbols for PUSCH repetition Type B. If it is absent, no symbol is explicitly defined for DL-to-UL switching (see TS 38.214 [19], clause 6.1). |
| ***priorityIndicatorDCI-0-1, priorityIndicatorDCI-0-2***  Configures the presence of "priority indicator" in DCI format 0\_1/0\_2. When the field is absent in the IE, then the UE shall apply 0 bit for "Priority indicator" in DCI format 0\_1/0\_2. The field *priorityIndicatorDCI-0-1* applies to DCI format 0\_1 and the field *priorityIndicatorDCI-0-2* applies to DCI format 0\_2 (see TS 38.212 [17] clause 7.3.1 and TS 38.213 [13] clause 9). |
| ***pusch-AggregationFactor***  Number of repetitions for data (see TS 38.214 [19], clause 6.1.2.1). If the field is absent the UE applies the value 1. |
| ***pusch-RepTypeIndicatorDCI-0-1, pusch-RepTypeIndicatorDCI-0-2***  Indicates whether UE follows the behavior for "PUSCH repetition type A" or the behavior for "PUSCH repetition type B" for the PUSCH scheduled by DCI format 0\_1/0\_2 and for Type 2 CG associated with the activating DCI format 0\_1/0\_2.The value *pusch-RepTypeA* enables the 'PUSCH repetition type A' and the value *pusch-RepTypeB* enables the 'PUSCH repetition type B'. The field *pusch-RepTypeIndicatorDCI-0-1* applies to DCI format 0\_1 and the field *pusch-RepTypeIndicatorDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.2.1). |
| ***pusch-TimeDomainAllocationList***  List of time domain allocations for timing of UL assignment to UL data (see TS 38.214 [19], table 6.1.2.1.1-1). The field *pusch-TimeDomainAllocationList* applies to DCI formats 0\_0 or DCI format 0\_1 when the field *pusch-TimeDomainAllocationListDCI-0-1* is not configured (see TS 38.214 [19], table 6.1.2.1.1-1 and table 6.1.2.1.1-1A). The network does not configure the *pusch-TimeDomainAllocationList* (without suffix) simultaneously with the *pusch-TimeDomainAllocationListDCI-0-2-r16* or *pusch-TimeDomainAllocationListDCI-0-1-r16* or *pusch-TimeDomainAllocationListForMultiPUSCH-r16*. |
| ***pusch-TimeDomainAllocationListDCI-0-1***  Configuration of the time domain resource allocation (TDRA) table for DCI format 0\_1 (see TS 38.214 [19], clause 6.1, table 6.1.2.1.1-1A). |
| ***pusch-TimeDomainAllocationListDCI-0-2***  Configuration of the time domain resource allocation (TDRA) table for DCI format 0\_2 (see TS 38.214 [19], clause 6.1.2, table 6.1.2.1.1-1B). |
| ***pusch-TimeDomainAllocationListForMultiPUSCH***  Configuration of the time domain resource allocation (TDRA) table for multiple PUSCH (see TS 38.214 [19], clause 6.1.2). The network configures at most 16 rows in this TDRA table in *PUSCH-TimeDomainResourceAllocationList-r16* configured by this field. This field is not configured simultaneously with *pusch-AggregationFactor*. |
| ***rbg-Size***  Selection between configuration 1 and configuration 2 for RBG size for PUSCH. The UE does not apply this field if *resourceAllocation* is set to *resourceAllocationType1*. Otherwise, the UE applies the value *config1* when the field is absent (see TS 38.214 [19], clause 6.1.2.2.1). |
| ***resourceAllocation, resourceAllocationDCI-0-2***  Configuration of resource allocation type 0 and resource allocation type 1 for non-fallback DCI (see TS 38.214 [19], clause 6.1.2). The field *resourceAllocation* applies to DCI format 0\_1 and the field *resourceAllocationDCI-0-2* applies to DCI format 0\_2 (see TS 38.214 [19], clause 6.1.2). |
| ***resourceAllocationType1GranularityDCI-0-2***  Configures the scheduling granularity applicable for both the starting point and length indication for resource allocation type 1 in DCI format 0\_2. If this field is absent, the granularity is 1 PRB (see TS 38.214 [19], clause 6.1.2.2.2). |
| ***tp-pi2BPSK***  Enables pi/2-BPSK modulation with transform precoding if the field is present and disables it otherwise. |
| ***transformPrecoder***  The UE specific selection of transformer precoder for PUSCH (see TS 38.214 [19], clause 6.1.3). When the field is absent the UE applies the value of the field *msg3-transformPrecoder*. |
| ***txConfig***  Whether UE uses codebook based or non-codebook based transmission (see TS 38.214 [19], clause 6.1.1). If the field is absent, the UE transmits PUSCH on one antenna port, see TS 38.214 [19], clause 6.1.1. |
| ***uci-OnPUSCH-ListDCI-0-1, uci-OnPUSCH-ListDCI-0-2***  Configuration for up to 2 HARQ-ACK codebooks specific to DCI format 0\_1/0\_2. The field uci-OnPUSCH-ListDCI-0-1 applies to DCI format 0\_1 and the field uci-OnPUSCH-ListDCI-0-2 applies to DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13] clause 9.3). |
| ***ul-AccessConfigListDCI-0-1***  List of the combinations of cyclic prefix extension, channel access priority class (CAPC), and UL channel access type (see TS 38.212 [17], Table 7.3.1.1.2-35). |
| ***ul-FullPowerTransmission***  Configures the UE with UL full power transmission mode as specified in TS 38.213. |

|  |
| --- |
| ***UCI-OnPUSCH* field descriptions** |
| ***betaOffsets***  Selection between and configuration of dynamic and semi-static beta-offset for DCI formats other than DCI format 0\_2. If the field is not configured, the UE applies the value 'semiStatic' (see TS 38.213 [13], clause 9.3). |
| ***scaling***  Indicates a scaling factor to limit the number of resource elements assigned to UCI on PUSCH for DCI formats other than DCI format 0\_2. Value *f0p5* corresponds to 0.5, value *f0p65* corresponds to 0.65, and so on. The value configured herein is applicable for PUSCH with configured grant (see TS 38.212 [17], clause 6.3). |

|  |
| --- |
| ***UCI-OnPUSCH-DCI-0-2* field descriptions** |
| ***betaOffsetsDCI-0-2***  Configuration of beta-offset for DCI format 0\_2. If semiStaticDCI-0-2 is chosen, the UE shall apply the value of 0 bit for the field of beta offset indicator in DCI format 0\_2. If dynamicDCI-0-2 is chosen, the UE shall apply the value of 1 bit or 2 bits for the field of beta offset indicator in DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13] clause 9.3). |
| ***dynamicDCI-0-2***  Indicates the UE applies the value 'dynamic' for DCI format 0\_2 (see TS 38.212 [17], clause 7.3.1 and TS 38.213 [13], clause 9.3). |
| ***semiStaticDCI-0-2***  Indicates the UE applies the value 'semiStatic' for DCI format 0\_2. (see TS 38.212 [17], clause 7.3.1 and see TS 38.213 [13], clause 9.3). |
| ***scalingDCI-0-2***  Indicates a scaling factor to limit the number of resource elements assigned to UCI on PUSCH for DCI format 0\_2. Value f0p5 corresponds to 0.5, value *f0p65* corresponds to 0.65, and so on (see TS 38.212 [17], clause 6.3). |

|  |  |
| --- | --- |
| **Conditional Presence** | **Explanation** |
| *codebookBased* | The field is mandatory present if *txConfig* is set to codebook and absent otherwise. |
| *RepTypeB* | The field is optionally present, Need S, if *pusch-RepTypeIndicatorDCI-0-1* is set to pusch-RepTypeB. It is absent otherwise. |
| *RepTypeB2* | The field is optionally present, Need S, if *pusch-RepTypeIndicatorDCI-0-1* or *pusch-RepTypeIndicatorDCI-0-2* is set to pusch-RepTypeB. It is absent otherwise. |

<Next modification>

– *InvalidSymbolPattern*

The IE *InvalidSymbolPattern* is used to configure one invalid symbol pattern for PUSCH transmission repetition type B applicable for both DCI format 0\_1 and 0\_2, see TS 38.214 [19], clause 6.1.

***InvalidSymbolPattern* information element**

-- ASN1START

-- TAG-INVALIDSYMBOLPATTERN-START

InvalidSymbolPattern-r16 ::= SEQUENCE {

symbols-r16 CHOICE {

oneSlot BIT STRING (SIZE (14)),

twoSlots BIT STRING (SIZE (28))

},

periodicityAndPattern-r16 CHOICE {

n2 BIT STRING (SIZE (2)),

n4 BIT STRING (SIZE (4)),

n5 BIT STRING (SIZE (5)),

n8 BIT STRING (SIZE (8)),

n10 BIT STRING (SIZE (10)),

n20 BIT STRING (SIZE (20)),

n40 BIT STRING (SIZE (40))

} OPTIONAL, -- Need M

...

}

-- TAG-INVALIDSYMBOLPATTERN-STOP

-- ASN1STOP

|  |
| --- |
| ***InvalidSymbolPattern* field descriptions** |
| ***periodicityAndPattern***  A time domain repetition pattern at which the pattern defined by *symbols* recurs. This slot pattern repeats itself continuously. When the field is not configured, the UE uses the value n1 (see TS 38.214 [19], clause 6.1). |
| ***symbols***  A symbol level bitmap in time domain (see TS 38.214[19], clause 6.1).  For *oneSlot*, if ECP is configured, the first 12 bits represent the symbols within the slot and the last two bits within the bitstring are ignored by the UE; Otherwise, the 14 bits represent the symbols within the slot.  For *twoSlots*, if ECP is configured, the first 12 bits represent the symbols within the first slot and the next 12 bits represent the symbols in the second slot and the last four bits within the bit string are ignored by the UE; Otherwise, the first 14 bits represent the symbols within the first slot and the next 14 bits represent the symbols in the second slot.  For the bits representing symbols in a slot, the most significant bit of the bit string represents the first symbol in the slot and the second most significant bit represents the second symbol in the slot and so on.  This pattern recurs (in time domain) with the configured *periodicityAndPattern*. |

<End of modification>