**3GPP TSG-RAN WG2 Meeting #109bis-e R2-2xxxxx**

**Electronic, 20 April – 30 April 2020**

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| *CR-Form-v12.0* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
|  | | | | | | | | |
|  | 38.321 | **CR** |  | **rev** |  | **Current version:** | 16.0.0 |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network | **x** | Core Network |  |

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| ***Title:*** | Add P bit in single entry PHR MAC CE | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | OPPO | | | | | | | | | |
| ***Source to TSG:*** | R2 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | TEI16 | | | | |  | ***Date:*** | | | 2020-04-27 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | F |  | | | | | ***Release:*** | | | *Re-16* |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) Rel-12 (Release 12)* *Rel-13 (Release 13) Rel-14 (Release 14) Rel-15 (Release 15) Rel-16 (Release 16)* | |
|  |  | | | | | | | | | |
| ***Reason for change:*** | | The P bit is not included in R15 single entry PHR MAC CE but it is inlcuded in multiple entry PHR MAC CE.  RAN2 agreed to inlcude the P bit in single entry PHR MAC CE since R16.  RAN2 also agree to introduce a UE capbiity to address it. | | | | | | | | |
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| ***Summary of change:*** | | Introduce the P bit in single entry PHR MAC CE. | | | | | | | | |
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| ***Consequences if not approved:*** | | The P bit in single PHR MAC CE is missing. | | | | | | | | |
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| ***Clauses affected:*** | |  | | | | | | | | |
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|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | |  | **X** | Other core specifications | | | | TS/TR ... CR ... | | |
| ***affected:*** | |  | **X** | Test specifications | | | | TS/TR ... CR ... | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | | TS/TR ... CR ... | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

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### 5.4.6 Power Headroom Reporting

The Power Headroom reporting procedure is used to provide the serving gNB with the following information:

- Type 1 power headroom: the difference between the nominal UE maximum transmit power and the estimated power for UL-SCH transmission per activated Serving Cell;

- Type 2 power headroom: the difference between the nominal UE maximum transmit power and the estimated power for UL-SCH and PUCCH transmission on SpCell of the other MAC entity (i.e. E-UTRA MAC entity in EN-DC, NE-DC, and NGEN-DC cases);

- Type 3 power headroom: the difference between the nominal UE maximum transmit power and the estimated power for SRS transmission per activated Serving Cell.

RRC controls Power Headroom reporting by configuring the following parameters:

- *phr-PeriodicTimer*;

- *phr-ProhibitTimer*;

- *phr-Tx-PowerFactorChange*;

- *phr-Type2OtherCell*;

- *phr-ModeOtherCG*;

- *multiplePHR*.

A Power Headroom Report (PHR) shall be triggered if any of the following events occur:

- *phr-ProhibitTimer* expires or has expired and the path loss has changed more than *phr-Tx-PowerFactorChange* dB for at least one activated Serving Cell of any MAC entity which is used as a pathloss reference since the last transmission of a PHR in this MAC entity when the MAC entity has UL resources for new transmission;

NOTE 1: The path loss variation for one cell assessed above is between the pathloss measured at present time on the current pathloss reference and the pathloss measured at the transmission time of the last transmission of PHR on the pathloss reference in use at that time, irrespective of whether the pathloss reference has changed in between.

- *phr-PeriodicTimer* expires;

- upon configuration or reconfiguration of the power headroom reporting functionality by upper layers, which is not used to disable the function;

- activation of an SCell of any MAC entity with configured uplink;

- addition of the PSCell (i.e. PSCell is newly added or changed);

- *phr-ProhibitTimer* expires or has expired, when the MAC entity has UL resources for new transmission, and the following is true for any of the activated Serving Cells of any MAC entity with configured uplink:

- there are UL resources allocated for transmission or there is a PUCCH transmission on this cell, and the required power backoff due to power management (as allowed by P-MPRc as specified in TS 38.101-1 [14], TS 38.101-2 [15], and TS 38.101-3 [16]) for this cell has changed more than *phr-Tx-PowerFactorChange* dB since the last transmission of a PHR when the MAC entity had UL resources allocated for transmission or PUCCH transmission on this cell.

NOTE 2: The MAC entity should avoid triggering a PHR when the required power backoff due to power management decreases only temporarily (e.g. for up to a few tens of milliseconds) and it should avoid reflecting such temporary decrease in the values of PCMAX,f,c/PH when a PHR is triggered by other triggering conditions.

NOTE 3: If a HARQ process is configured with *cg-RetransmissionTimer* and if the PHR is already included in a MAC PDU for transmission by this HARQ process, but not yet transmitted by lower layers, it is up to UE implementation how to handle the PHR content.

If the MAC entity has UL resources allocated for a new transmission the MAC entity shall:

1> if it is the first UL resource allocated for a new transmission since the last MAC reset:

2> start *phr-PeriodicTimer*;

1> if the Power Headroom reporting procedure determines that at least one PHR has been triggered and not cancelled; and

1> if the allocated UL resources can accommodate the MAC CE for PHR which the MAC entity is configured to transmit, plus its subheader, as a result of LCP as defined in clause 5.4.3.1:

2> if *multiplePHR* with value *true* is configured:

3> for each activated Serving Cell with configured uplink associated with any MAC entity:

4> obtain the value of the Type 1 or Type 3 power headroom for the corresponding uplink carrier as specified in clause 7.7 of TS 38.213 [6];

4> if this MAC entity has UL resources allocated for transmission on this Serving Cell; or

4> if the other MAC entity, if configured, has UL resources allocated for transmission on this Serving Cell and *phr-ModeOtherCG* is set to *real* by upper layers:

5> obtain the value for the corresponding PCMAX,f,c field from the physical layer.

3> if *phr-Type2OtherCell* with value *true* is configured:

4> if the other MAC entity is E-UTRA MAC entity:

5> obtain the value of the Type 2 power headroom for the SpCell of the other MAC entity (i.e. E-UTRA MAC entity);

5> if *phr-ModeOtherCG* is set to *real* by upper layers:

6> obtain the value for the corresponding PCMAX,f,c field for the SpCell of the other MAC entity (i.e. E-UTRA MAC entity) from the physical layer.

3> instruct the Multiplexing and Assembly procedure to generate and transmit the Multiple Entry PHR MAC CE as defined in clause 6.1.3.9 based on the values reported by the physical layer.

2> else (i.e. Single Entry PHR format is used):

3> obtain the value of the Type 1 power headroom from the physical layer for the corresponding uplink carrier of the PCell;

3> if the MAC entity applies power backoff due to power management for this serving cell and UE supports *singlePHR-P*:

4> set the P bit to 1;

3> obtain the value for the corresponding PCMAX,f,c field from the physical layer;

3> instruct the Multiplexing and Assembly procedure to generate and transmit the Single Entry PHR MAC CE as defined in clause 6.1.3.8 based on the values reported by the physical layer.

2> start or restart *phr-PeriodicTimer*;

2> start or restart *phr-ProhibitTimer*;

2> cancel all triggered PHR(s).

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#### 6.1.3.8 Single Entry PHR MAC CE

The Single Entry PHR MAC CE is identified by a MAC subheader with LCID as specified in Table 6.2.1-2.

It has a fixed size and consists of two octets defined as follows (figure 6.1.3.8-1):

- R: Reserved bit, set to 0;

- Power Headroom (PH): This field indicates the power headroom level. The length of the field is 6 bits. The reported PH and the corresponding power headroom levels are shown in Table 6.1.3.8-1 below (the corresponding measured values in dB are specified in TS 38.133 [11]);

- P: This field indicates whether the MAC entity applies power backoff due to power management (as allowed by P-MPRc as specified in TS 38.101-1 [14], TS 38.101-2 [15], and TS 38.101-3 [16]). The MAC entity shall set the P field to 1 if the corresponding PCMAX,f,c field would have had a different value if no power backoff due to power management had been applied;

- PCMAX,f,c: This field indicates the PCMAX,f,c (as specified in TS 38.213 [6]) used for calculation of the preceding PH field. The reported PCMAX,f,c and the corresponding nominal UE transmit power levels are shown in Table 6.1.3.8-2 (the corresponding measured values in dBm are specified in TS 38.133 [11]).



Figure 6.1.3.8-1: Single Entry PHR MAC CE without P bit



Figure 6.1.3.8-2: Single Entry PHR MAC CE with P bit

Table 6.1.3.8-1: Power Headroom levels for PHR

|  |  |
| --- | --- |
| PH | Power Headroom Level |
| 0 | POWER\_HEADROOM\_0 |
| 1 | POWER\_HEADROOM\_1 |
| 2 | POWER\_HEADROOM\_2 |
| 3 | POWER\_HEADROOM\_3 |
| … | … |
| 60 | POWER\_HEADROOM\_60 |
| 61 | POWER\_HEADROOM\_61 |
| 62 | POWER\_HEADROOM\_62 |
| 63 | POWER\_HEADROOM\_63 |

Table 6.1.3.8-2: Nominal UE transmit power level for PHR

|  |  |
| --- | --- |
| PCMAX,f,c | Nominal UE transmit power level |
| 0 | PCMAX\_C\_00 |
| 1 | PCMAX\_C\_01 |
| 2 | PCMAX\_C\_02 |
| … | … |
| 61 | PCMAX\_C\_61 |
| 62 | PCMAX\_C\_62 |
| 63 | PCMAX\_C\_63 |

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