

**TSG-RAN Working Group 1 Meeting #19**  
**Las Vegas, USA, February 27 – March 2, 2001**

*TSGR1#19(01)0341*

**Agenda Item: R99**

**Source:** InterDigital Comm. Corp., Siemens AG

**Title:** CR 25.221 - 048 “Correction to Table 5.b “Timeslot formats for the Uplink”

**Document for:** Decision

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This CR corrects the number of bits for “ $N_{\text{data/data field}(1)}$  (bits)” and “ $N_{\text{data/data field}(2)}$  (bits)” in Table 5.b from line 66 to 89.

## CHANGE REQUEST

✎
25.221 CR 048
✎
rev
✎
Current version:
3.5.0
✎

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ✎ symbols.

**Proposed change affects:** ✎ (U)SIM  ME/UE  Radio Access Network  Core Network

<b>Title:</b>	✎ Corrections to Table 5.b "Timeslot formats for the Uplink"		
<b>Source:</b>	✎ InterDigital Comm. Corp., Siemens AG		
<b>Work item code:</b>	✎	<b>Date:</b>	✎ February 27, 2001
<b>Category:</b>	✎ <b>F</b>	<b>Release:</b>	✎ R99
Use <u>one</u> of the following categories: <b>F</b> (essential correction) <b>A</b> (corresponds to a correction in an earlier release) <b>B</b> (Addition of feature), <b>C</b> (Functional modification of feature) <b>D</b> (Editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900.		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) REL-4 (Release 4) REL-5 (Release 5)	

<b>Reason for change:</b>	✎ The numbers of bit for "N <sub>data/data field(1)</sub> (bits)" and "N <sub>data/data field(2)</sub> (bits)" from line 66 to line 89 in Table 5b "Timeslot formats for the Uplink" of are incorrect.		
<b>Summary of change:</b>	✎ The numbers for N <sub>data/data field(1)</sub> (bits) and N <sub>data/data field(2)</sub> (bits) in Table 5.b are corrected		
<b>Consequences if not approved:</b>	✎ .Incorrect information in the specification.		

<b>Clauses affected:</b>	✎ 5.2.2.6.2		
<b>Other specs affected:</b>	<input type="checkbox"/> Other core specifications <input type="checkbox"/> Test specifications <input type="checkbox"/> O&M Specifications	✎	✎
<b>Other comments:</b>	✎		

### How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at: [http://www.3gpp.org/3G\\_Specs/CRs.htm](http://www.3gpp.org/3G_Specs/CRs.htm). Below is a brief summary:

- 1) Fill out the above form. The symbols above marked ✎ contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://www.3gpp.org/specs/> For the latest version, look for the directory name with the latest date e.g. 2000-09 contains the specifications resulting from the September 2000 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

#### 5.2.2.6.2 Uplink timeslot formats

The uplink timeslot format depends on the spreading factor, midamble length, guard period length and on the number of the TFCI bits. Due to TPC, different amount of bits are mapped to the two data fields. The timeslot formats are depicted in the table 4b.

Table 5b: Timeslot formats for the Uplink

Slot Format #	Spreading Factor	Midamble length (chips)	Guard Period (chips)	$N_{\text{TCI}}$ (bits)	$N_{\text{TPC}}$ (bits)	Bits/slot	$N_{\text{Data/Slot}}$ (bits)	$N_{\text{data/data field(1)}}$ (bits)	$N_{\text{data/data field(2)}}$ (bits)
0	16	512	96	0	0	244	244	122	122
1	16	512	96	0	2	244	242	122	120
2	16	512	96	4	2	244	238	120	118
3	16	512	96	8	2	244	234	118	116
4	16	512	96	16	2	244	226	114	112
5	16	512	96	32	2	244	210	106	104
6	16	256	96	0	0	276	276	138	138
7	16	256	96	0	2	276	274	138	136
8	16	256	96	4	2	276	270	136	134
9	16	256	96	8	2	276	266	134	132
10	16	256	96	16	2	276	258	130	128
11	16	256	96	32	2	276	242	122	120
12	8	512	96	0	0	488	488	244	244
13	8	512	96	0	2	486	484	244	240
14	8	512	96	4	2	482	476	240	236
15	8	512	96	8	2	478	468	236	232
16	8	512	96	16	2	470	452	228	224
17	8	512	96	32	2	454	420	212	208
18	8	256	96	0	0	552	552	276	276
19	8	256	96	0	2	550	548	276	272
20	8	256	96	4	2	546	540	272	268
21	8	256	96	8	2	542	532	268	264
22	8	256	96	16	2	534	516	260	256
23	8	256	96	32	2	518	484	244	240
24	4	512	96	0	0	976	976	488	488
25	4	512	96	0	2	970	968	488	480
26	4	512	96	4	2	958	952	480	472
27	4	512	96	8	2	946	936	472	464
28	4	512	96	16	2	922	904	456	448
29	4	512	96	32	2	874	840	424	416
30	4	256	96	0	0	1104	1104	552	552
31	4	256	96	0	2	1098	1096	552	544
32	4	256	96	4	2	1086	1080	544	536
33	4	256	96	8	2	1074	1064	536	528
34	4	256	96	16	2	1050	1032	520	512
35	4	256	96	32	2	1002	968	488	480
36	2	512	96	0	0	1952	1952	976	976
37	2	512	96	0	2	1938	1936	976	960
38	2	512	96	4	2	1910	1904	960	944
39	2	512	96	8	2	1882	1872	944	928
40	2	512	96	16	2	1826	1808	912	896
41	2	512	96	32	2	1714	1680	848	832
42	2	256	96	0	0	2208	2208	1104	1104
43	2	256	96	0	2	2194	2192	1104	1088
44	2	256	96	4	2	2166	2160	1088	1072
45	2	256	96	8	2	2138	2128	1072	1056
46	2	256	96	16	2	2082	2064	1040	1024
47	2	256	96	32	2	1970	1936	976	960

Slot Format #	Spreading Factor	Midamble length (chips)	Guard Period (chips)	N <sub>TFCI</sub> (bits)	N <sub>TPC</sub> (bits)	Bits/slot	N <sub>Data/Slot</sub> (bits)	N <sub>data/data field(1)</sub> (bits)	N <sub>data/data field(2)</sub> (bits)
48	1	512	96	0	0	3904	3904	1952	1952
49	1	512	96	0	2	3874	3872	1952	1920
50	1	512	96	4	2	3814	3808	1920	1888
51	1	512	96	8	2	3754	3744	1888	1856
52	1	512	96	16	2	3634	3616	1824	1792
53	1	512	96	32	2	3394	3360	1696	1664
54	1	256	96	0	0	4416	4416	2208	2208
55	1	256	96	0	2	4386	4384	2208	2176
56	1	256	96	4	2	4326	4320	2176	2144
57	1	256	96	8	2	4266	4256	2144	2112
58	1	256	96	16	2	4146	4128	2080	2048
59	1	256	96	32	2	3906	3872	1952	1920
60	16	512	192	0	0	232	232	122	110
61	16	512	192	0	2	232	230	122	108
62	16	512	192	4	2	232	226	120	106
63	16	512	192	8	2	232	222	118	104
64	16	512	192	16	2	232	214	114	100
65	16	512	192	32	2	232	198	106	92
66	8	512	192	0	0	464	464	<del>244</del> <del>232</del>	<del>220</del> <del>232</del>
67	8	512	192	0	2	462	460	<del>244</del> <del>232</del>	<del>216</del> <del>228</del>
68	8	512	192	4	2	458	452	<del>240</del> <del>228</del>	<del>212</del> <del>224</del>
69	8	512	192	8	2	454	444	<del>236</del> <del>224</del>	<del>208</del> <del>220</del>
70	8	512	192	16	2	446	428	<del>228</del> <del>246</del>	<del>200</del> <del>242</del>
71	8	512	192	32	2	430	396	<del>212</del> <del>200</del>	<del>184</del> <del>496</del>
72	4	512	192	0	0	928	928	<del>488</del> <del>464</del>	<del>440</del> <del>464</del>
73	4	512	192	0	2	922	920	<del>488</del> <del>464</del>	<del>432</del> <del>456</del>
74	4	512	192	4	2	910	904	<del>480</del> <del>456</del>	<del>424</del> <del>448</del>
75	4	512	192	8	2	898	888	<del>472</del> <del>448</del>	<del>416</del> <del>440</del>
76	4	512	192	16	2	874	856	<del>456</del> <del>432</del>	<del>400</del> <del>424</del>
77	4	512	192	32	2	826	792	<del>424</del> <del>400</del>	<del>368</del> <del>392</del>
78	2	512	192	0	0	1856	1856	<del>976</del> <del>928</del>	<del>880</del> <del>928</del>
79	2	512	192	0	2	1842	1840	<del>976</del> <del>928</del>	<del>864</del> <del>942</del>
80	2	512	192	4	2	1814	1808	<del>960</del> <del>942</del>	<del>848</del> <del>896</del>
81	2	512	192	8	2	1786	1776	<del>944</del> <del>896</del>	<del>832</del> <del>880</del>
82	2	512	192	16	2	1730	1712	<del>912</del> <del>864</del>	<del>800</del> <del>848</del>
83	2	512	192	32	2	1618	1584	<del>848</del>	<del>736</del>

Slot Format #	Spreading Factor	Midamble length (chips)	Guard Period (chips)	N <sub>TFCI</sub> (bits)	N <sub>TPC</sub> (bits)	Bits/slot	N <sub>Data/Slot</sub> (bits)	N <sub>data/data field(1)</sub> (bits)	N <sub>data/data field(2)</sub> (bits)
								800	784
84	1	512	192	0	0	3712	3712	<u>1952</u> <del>1856</del>	<u>1760</u> <del>1856</del>
85	1	512	192	0	2	3682	3680	<u>1952</u> <del>1856</del>	<u>1728</u> <del>1824</del>
86	1	512	192	4	2	3622	3616	<u>1920</u> <del>1824</del>	<u>1696</u> <del>1792</del>
87	1	512	192	8	2	3562	3552	<u>1888</u> <del>1792</del>	<u>1664</u> <del>1760</del>
88	1	512	192	16	2	3442	3424	<u>1824</u> <del>1728</del>	<u>1600</u> <del>1696</del>
89	1	512	192	32	2	3202	3168	<u>1696</u> <del>1600</del>	<u>1472</u> <del>1568</del>