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Agenda Item: AH21
Source: CWTS/CATT
To: TSG RAN WG1
Title: CCPCH Multiframe Structure
Document for: Information

1. Summary

In 1.28Mcps TDD, the P-CCPCH is always located in the timeslot#0 and use channelisation code

$c_{Q^{16}}^{(k?1)}$ and $c_{Q^{16}}^{(k?2)}$. The S-CCPCH and PICH can be time multiplexed with P-CCPCH. They are always mapped onto two code channels with spreading factor SF=16.

In the following figures, some examples for Multiframe Structures on Primary and Secondary CCPCH are given. The figures show the placement of Common Transport Channels on the Common Control Physical Channels. Additional S-CCPCH capacity can be allocated on other codes and timeslots of course, e.g. FACH capacity is related to overall cell capacity and can be configured according to the actual needs. Every TrCH-box in the figures is assumed to be valid for two frames (see row 'Frame #'), i.e. the transport channels in CCPCHs have an interleaving time of 20msec.

The actual CCPCH Multiframe Scheme used in the cell is described and broadcast on BCH. Thus the system information structure has its roots in this particular transport channel and allocations of other Common Channels can be handled this way, i.e. by pointing from BCH.

2. Proposal

We propose to include the following paragraphs in the working CR for the TS25.221 section Annex E the CCPCH multiframe structure for 1.28Mcps TDD.

----- Changes to working CR of 25.221 begin -----

Annex E (Informative): CCPCH Multiframe Structure

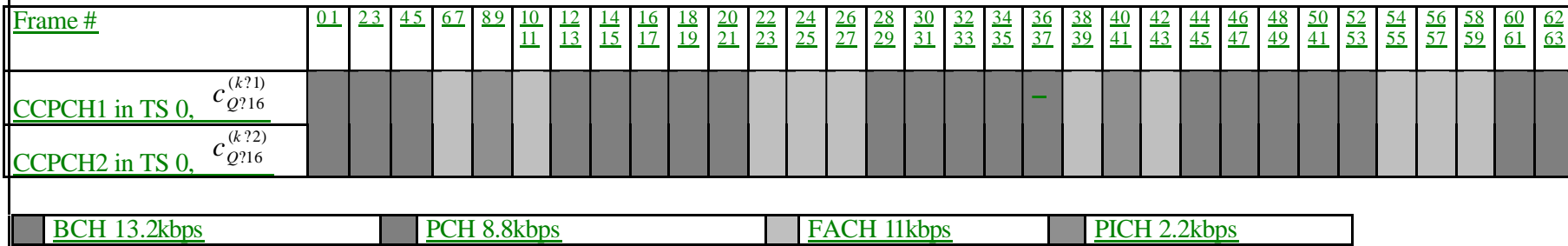


Figure E.1: Example for a multiframe structure for CCPCHs and PICH that is repeated every 64th frame (128 sub-frame)

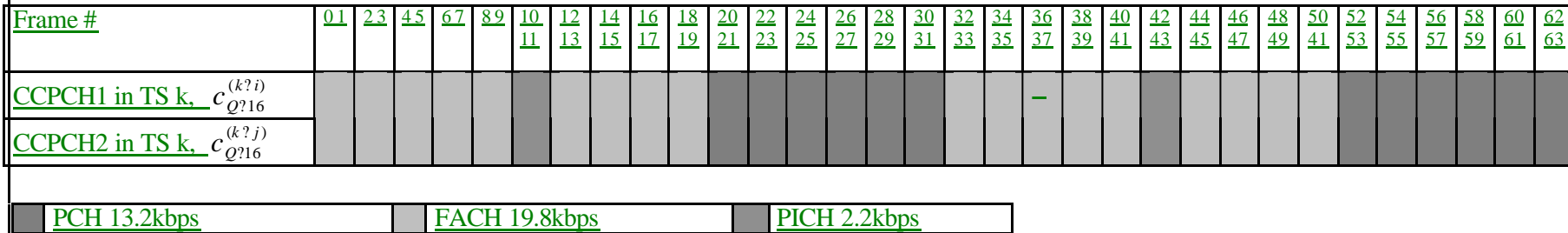


Figure E.2: Example for a multiframe structure for S-CCPCHs and PICH that is repeated every 64th frame, i,j=1? 16 (i?),k?0, 1,(128 sub-frame)

----- Changes to working CR of 25.221 end -----